版本: 1.0

规格书

<u>产品</u>: TFT 模块

型号: SX035HV006

客户			老友记电子			
批准	审核	检查	批准	审核	拟制	

用于承认规格书

用于承认规格书和样品

深圳市老友记电子有限公司

GOOD OLD FRIENDS ELECTRONIC CO.,LTD

修改记录

日期	版本	修改内容	拟制	检查	核准
2015-7-9	1.0	初版发行	Gilbert		

SX035HV006 标准文本 产品规格书 型号 页数 2 目录 1. 概述 -------3 3 2. 3. 3 4. 4 5. 5 6. 极限技术参数 ------6 电气参数 ------7. 6 7 8. 极限环境参数 ---------7 9. 10. 光电参数-------8 可靠性测试 ---------11. 11 12. LCM 检验标准 -------12 13. 15 深圳市老友记电子有限公司

1. 概述

1-1范围:

此份规格书涵盖了LCM从老友记电子到客户的运输过程中应该注意的所有要求。

1-2 产品:

LCD 模块 (LCM)

1-3 型号:

SX035HV006

2. 产品特征

transmissive

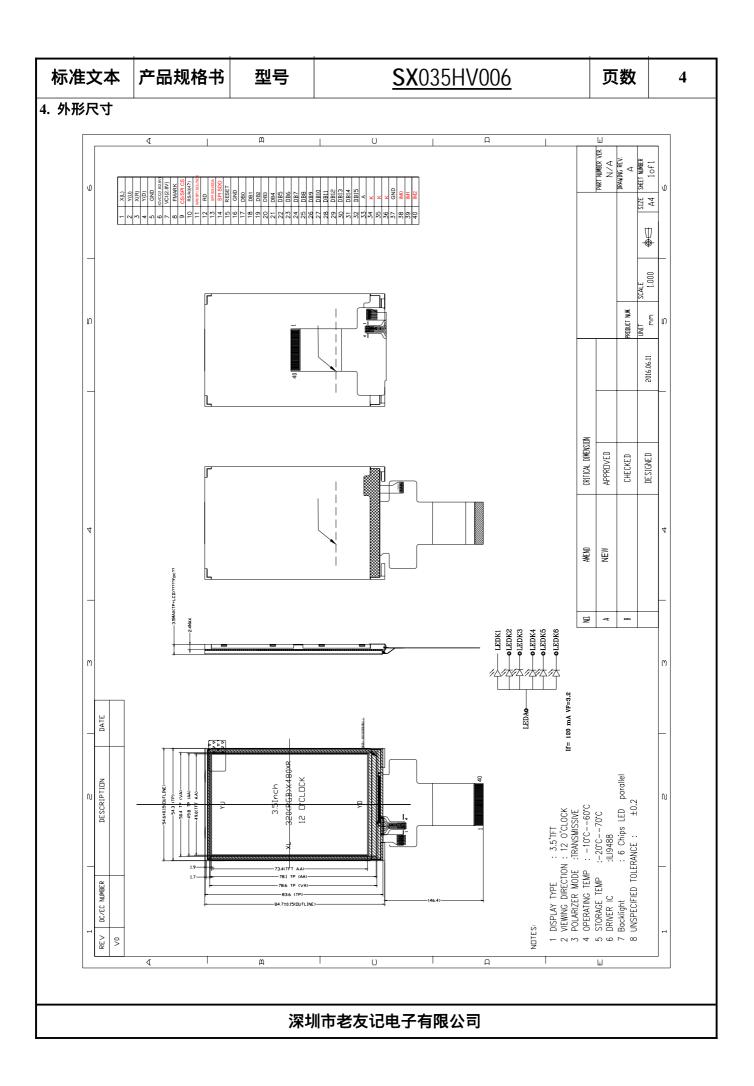
(1) 显示类型: Transmissive (全透式), 3.5" QVGA TFT, COG;

(2)显示色彩:262K;(3)驱动 IC: ILI9488(4)视角 :12点

(5) 点阵 : 320 (RGB) (W) ×480H) Pixels;

3. 机械规格

项目	规格	单位
外形尺寸	53.50*83.32*2.26	mm
可视区域	49.00*73.40	mm
显示内容	320RGB *480Dots	
结构类型	COG+FPC+BL	
背光类型	6-WHITE LED PARALLEL	-



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5.接口定义:

## 接口定义 5 脚功能说明				
2	编号	接口定义	接口定义 引脚功能说明	
3	1	XL	TOUCH PIN	1/0
TOUCH PIN 1/0 5 GND GROUND FOWER SUPLY POWER TOUCH 2.8V) POWER SUPLY POWER Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. O	2	YU	TOUCH PIN	1/0
5 GND GROUND GROUND 6 IOVCC(1.8/2.8V) POWER SUPLY POWER 7 VCI(2.8V) POWER SUPLY POWER 8 FMARK Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. O 9 CS/SPI CS Chip select input pin ("Low" enable). I 10 RS/A0(4 WIRE) This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. I 11 -8080-/8080 - system (WRX): Serves as a write signal and writes data at the rising edge.	3	XR	TOUCH PIN	1/0
FOWER SUPLY POWER SUPLY POWER	4	YD	TOUCH PIN	1/0
7 VCI(2.8V) POWER SUPLY POWER 8 FMARK Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. O 9 CS/SPI CS Chip select input pin ("Low" enable). I 10 RS/A0(4 WIRE) This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. I 11 - 8080- /8080 - system (WRX): Serves as a write signal and writes data at the rising edge. I 12 RD 8080- /8080 - system (RDX): Serves as a read signal and MCU read data at the rising edge. I 13 SPI SDI/SDA When IM[3]: Low, Serial in/out signal. I/O 14 SPI SDO Serial output signal. I/O 15 RESET This signal will reset the device and must be applied to properly initialize the chip. I 16 GND GROUND GROUND 17-32 DB0-DB15 data bus I/O 17-32 DB0-DB15 data bus I/O 33 A LED CATHODE I 34 K LED CATHODE I 35	5	GND	GROUND	GROUND
Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. - 8080-/8080 - system (WRX): Serves as a write signal and writes data at the rising edge 4-line system (D/CX): Serves as command or parameter select. RD 8080-/8080 - system (RDX): Serves as a read signal and MCU read data at the rising edge. When IM[3]: Low, Serial in/out signal. When IM[3]: High, Serial input signal. SPI SDI/SDA When IM[3]: High, Serial input signal. O This signal will reset the device and must be applied to properly initialize the chip. RESET to GROUND GROUND GROUND Tr-32 DB0-DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND GROUND GROUND GROUND HIT GROUND GROUND GROUND GROUND HIT GROUND GROUND GROUND HIT GROUND GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND GROUND HIT GROUND HIT GROUND GROUND HIT GROUND HIT GROUND GROUND HIT GROUND HIT GROUND HIT GROUND HIT GROUND HIT HIT GROUND	6	IOVCC(1.8/2.8V)	POWER SUPLY	POWER
FMARK frame writing, activated by S/W command. Chip select input pin ("Low" enable). This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. - 8080-/8080 - system (WRX): Serves as a write signal and writes data at the rising edge 4-line system (D/CX): Serves as command or parameter select. RD 8080-/8080 - system (RDX): Serves as a read signal and MCU read data at the rising edge. When IM[3]: Low, Serial in/out signal. When IM[3]: High, Serial input signal. SPI SDI/SDA When IM[3]: High, Serial input signal. O Serial output signal. This signal will reset the device and must be applied to properly initialize the chip. GROUND GROUND GROUND Tr-32 DB0-DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND GROUND 38 IMO Interface selected Pin I	7	VCI(2.8V)	POWER SUPLY	POWER
This pin is used to select "Data or Command" in the parallel interface or 4-wire 8-bit serial data interface. - 8080-/8080 - system (WRX): Serves as a write signal and writes data at the rising edge 4-line system (D/CX): Serves as command or parameter select. 12 RD 8080-/8080 - system (RDX): Serves as a read signal and MCU read data at the rising edge. When IM[3]: Low, Serial in/out signal. When IM[3]: High, Serial input signal. SPI SDO Serial output signal. O This signal will reset the device and must be applied to properly initialize the chip. RESET GROUND GROUND Tr-32 DB0-DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND GROUND 38 IMO Interface selected Pin I	8	FMARK		О
RS/A0(4 WIRE) parallel interface or 4-wire 8-bit serial data interface. - 8080- /8080 - system (WRX): Serves as a write signal and writes data at the rising edge 4-line system (D/CX): Serves as command or parameter select. RD	9	CS/SPI CS	Chip select input pin ("Low" enable).	I
11 write signal and writes data at the rising edge.	10	RS/A0(4 WIRE)		I
12 RD read signal and MCU read data at the rising edge. When IM[3]: Low, Serial in/out signal. When IM[3]: High, Serial input signal. I/O Serial output signal. O This signal will reset the device and must be applied to properly initialize the chip. GROUND GROUND 17~32 DB0~DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND GROUND GROUND GROUND GROUND I 10 11 12 13 14 15 16 16 16 17 17 18 18 18 18 18 18 18 18 18 18 18 18 18	11	WR/SPI CSL/SCK)	write signal and writes data at the rising edge 4-line system (D/CX): Serves as command or	I
SPI SDI/SDA When IM[3] : High, Serial input signal. 14 SPI SDO Serial output signal. O 15 RESET This signal will reset the device and must be applied to properly initialize the chip. 16 GND GROUND GROUND 17~32 DB0~DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND 38 IMO Interface selected Pin I 39 IM1 Interface selected Pin I	12	RD	• ` '	Ι
This signal will reset the device and must be applied to properly initialize the chip. 16 GND GROUND 17~32 DB0~DB15 data bus I/O 33 A LED ANODE I LED CATHODE I STANDO C	13	SPI SDI/SDA		I/O
15 RESET to properly initialize the chip. I 16 GND GROUND GROUND 17~32 DB0~DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND 38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	14	SPI SDO	Serial output signal.	0
17~32 DB0~DB15 data bus I/O 33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND 38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	15	RESET		Ι
33 A LED ANODE I 34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND 38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	16	GND	GROUND	GROUND
34 K LED CATHODE I 35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND 38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	17~32	DB0~DB15	data bus	I/O
35 K LED CATHODE I 36 K LED CATHODE I 37 GND GROUND GROUND 38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	33	A	LED ANODE	I
36KLED CATHODEI37GNDGROUNDGROUND38IM0Interface selected PinI39IM1Interface selected PinI	34	K	LED CATHODE	I
37GNDGROUNDGROUND38IM0Interface selected PinI39IM1Interface selected PinI	35	K	LED CATHODE	I
38 IM0 Interface selected Pin I 39 IM1 Interface selected Pin I	36	K	LED CATHODE	I
39 IM1 Interface selected Pin I	37	GND	GROUND	GROUND
	38	IM0	Interface selected Pin	I
40 IM2 Interface selected Pin I	39	IM1	Interface selected Pin	I
	40	IM2	Interface selected Pin	I

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6. 极限技术参数

Item	Symbol	Unit	Value	Note
Power Supply Voltage 1	VCC, IOVCC	V	-0.3 ~ +4.6	1, 2
Power Supply Voltage 2	VCI – AGND	٧	-0.3 ~ +4.6	1, 3
Power Supply Voltage 3	DDVDH – AGND	٧	-0.3 ~ +6.5	1, 4
Power Supply Voltage 4	AGND - VCL	V	-0.3 ~ +4.6	1
Power Supply Voltage 5	DDVDH - VCL	٧	-0.3 ~ +9.0	1, 5
Power Supply Voltage 7	AGND - VGL	V	-0.3 ~ +13.0	1, 6
Power Supply Voltage 8	VGH- VGL	V	-0.3 ~ +30.0	1
Power Supply Voltage 9	VPP1	V	-0.3 ~ +10.0	1
Power Supply Voltage 10	VPP2	V	-0.3 ~ +10.0	1
Power Supply Voltage 11	VPP3A	V	-10.0 ~ +0.3	1
Input Voltage	Vt	V	-0.3 ~ IOVCC + 0.3	1
Operating Temperature	Topr	°C	-40 ~ +85	1, 7
NVM Write Temperature	Twep	°C	+20 ~ +30	1
NVM Erase Temperature	Теер	°C	+20 ~ +30	1
Storage Temperature	Tstg	$^{\circ}$	-55 ~ +110	1

7. 电气参数

Davamakar	Cumbal		Llmik	Notes		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
TFT Gate ON Voltage	VGH *1)	13	15	17	٧	
TFT Gate OFF Voltage	VGL *2)	-11.7	-9.7	-7.7	V	
TET Common Flortrada Valtaga	VcomH	-	4.25	-	V	
TFT Common Electrode Voltage	VcomL	-	-0.7	-	٧	*3)
TFT Kick-Back Voltage Max.	∆V _p Max	0.6	_	1.8	v	
TFT Kick-Back Voltage Min.	∆V _p Min	0.6	•	1.0	V	

Notes:

- *1) VGH is TFT Gate Operating Voltage.
- *2) VGL is TFT Gate Operating Voltage

The low voltage level of VGL signal must be fluctuated with same phase as Vcom, in case of Cadd (Storage on Gate) structure.

The storage capacitance structure of LH240Q36-SH01 is Cst (Storage on Common)

*3) Vcom must be adjusted to optimize display quality: Cross-talk, Contrast Ratio and etc.

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8. 背光参数

12-1 背光供电方式

12-2LED 白背光特性指标

I	页目	符号	条件	最小值	典型值	最大值	单位	
正向电压		Vf	IF=90mA	2.8	3.2	3.3	V	
反向	自电压	Vr		-	-	-	\ \ \	
正向	Normal	Ipn	6-chip		90	-	m A	
电流	Dimming	Ipd	parallel			5	mA	
反向	自电流	Ir	Vr=5V	-	-	-	uA	
均	匀性		IF=90mA	-	80	-	%	
亮度(覆屏) 主屏			IF=90mA	-	150	-	cd/m ²	
背为	上颜色				白色			

9 极限环境参数

ITEM	SYMBOL	CONDITIONS	CRITERION
OPERATING TEMPERATURE	TOPR	-20 ~ +70	NO DEFECT IN DISPLAYING AND
OPERATING TEMPERATURE	TOPR	-20 ~ +70	OPERATIONAL FUNCTION
STORAGE TEMPERATURE	TSTG	-30 ~ +80	NO DEFECT IN DISPLAYING AND
STORAGE TEMPERATURE	1316	-30 ~ +00	OPERATIONAL FUNCTION
HUMIDITY	-	See Note	WITHOUT CONDENSATION

*NOTE: TEST CONDITION

(1)TEMPERATURE AND HUMIDITY: IF NO SPECIFICATION, TEMP. SET AT $25\pm2^{\circ}$ C, HUMIDITY SET AT $60\pm5\%$ RH

(2) OPERATING STATE: SAMPLES SUBJECT TO THE TESTS SHALL BE IN "OPERATING" CONDITION

10. 光电参数

2	0		Values		111	
Parameter	Symbol	Min	Тур	Max	Unit	Notes
*1) Threshold Voltage	Vsat	4.2	4.3	4.4	V	Fi- 0
-7 I nresnoid voitage	Vth	2.0	2.1	2.2	V	Fig.2
*1) Transmittance	T(%)	-	4.7	-	%	Fig.1
*1) Contrast Ratio	C/R	-	500	-		
*1) Response Time	Tr+Tf	-	35	50	msec	Fig.3, Fig.5
	Rx	0.637	0.657	0.677		
	Ry	0.300	0.320	0.340		
	Gx	0.267	0.287	0.307		
*2) CIE Color Coordinate	Gy	0.571	0.591	0.611		
CIE Color Coordinate	Bx	0.120	0.140	0.160		
	Ву	0.060	0.080	0.100		
	Wx	0.290	0.310	0.330		
	Wy	0.307	0.327	0.347		
	⊝I	-	80	-		
*1) Viewing Angle	⊖r	-	80	-	D	C/R>10
viewing Angle	⊖u	-	80	-	Degree	Fig.4
	⊖d	-	80	-		

Notes: 1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels

Contrast Ratio =

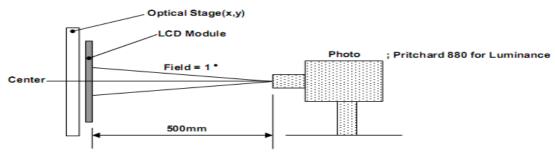
Surface Luminance with all black pixels

- Surface luminance is the center point across the TFT-LCD surface 500 mm from the surface with all pixels displaying white. For more information see FIG 1.
- Response time is the time required for the display to transition from white to black(Rise Time, Tr) and from black to white(Falling Time, Tf). For additional information see FIG 3.
- 4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the TFT-LCD surface. For more information see FIG 4.
- 5. Optimum contrast is obtained by adjusting the TFT-LCD Threshold voltage(Vth & Vsat)

FIG. 1 Optical Characteristic Measurement Equipment and Method

Pritchard 880 System

[Test Equipment Set Up]



- Measuring Condition;
 - -Measuring surroundings: Dark Room
 - -Measuring temperature : Ta=25°C
 - -Adjust operating voltage to get optimum contrast at the center of the display.
 - -Measured value at the center point of LCD panel after more than 30 minutes while backlight turning on.

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FIG. 2 The definition of Vth and Vsat

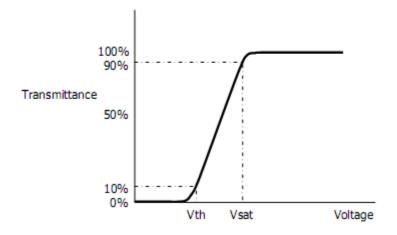
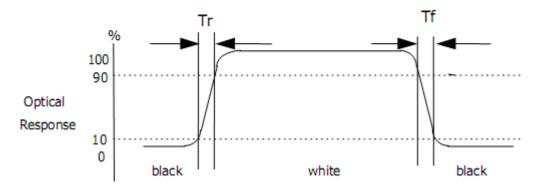


FIG. 3 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



* Voltage conditions for Response time

Vgate: 22V DC Vdata: 0V~4.5V DC Vcom: 0V (Ground)

FIG. 4 The definition of viewing angle

<dimension of viewing angle range>

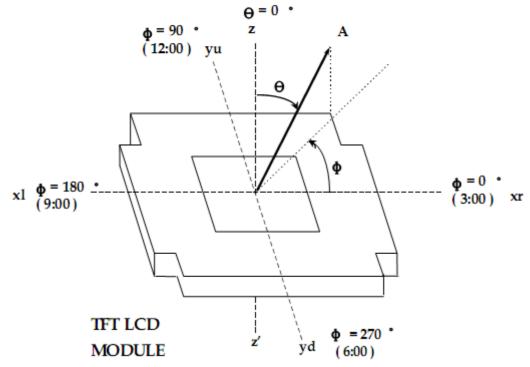
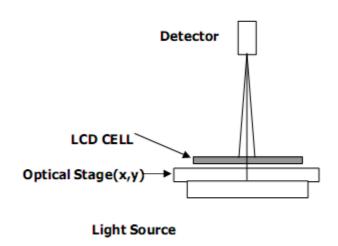
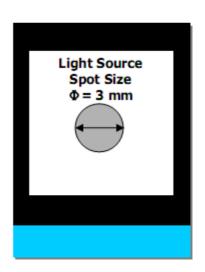


FIG. 5 Response Time Measurement Equipment and Method

DMS 803 System





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11. 可靠性测试

Absolute Maximum Conditions

No.	Parameter	Condition
1	Operating Temperature	-20 ~ 70 ℃
2	Storage Temperature	-30 ~ 80 ℃

Reliability test conditions (Polarizer characteristics null)

No.	Test Items	Test Condition	
1	High Temperature Storage	T = 80°C 240hr	
2	Low Temperature Storage	T = -30℃ for 240hr	
3	High Temperature Operating	T = 70°C for 240hr	Module
4	Low Temperature Operating	T = -20℃ for 240hr (But no condensation of dew)	(Without Contamination)
5	High Temp. and High Humidity Operating	T = 60°C /90% for 240hr (But no condensation dew)	
6	Thermal Shock	-30 ~ 80 ℃, 100cycle	
7	Packing Shock	1coner, 3edge, 6face / 76cmDrop	
8	Packing Vibration	Random Truck 0.57Grms Z direction 1hr.	Packing

※ 1) No.1∼ No.6 : No guarantee for panel, only for module with the above test conditions.

2) No.7~ No.8: Refer to 7-1) Packing Ass'y on page 14.

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12 . LCM 检验标准

(1)外观检验项目及标准(非工作状态)

1 尺寸総出产品规格 1 1 1 1 X Y Z K/8 不进入 A 区 T X Y Z K/8 不进入 A 区 不计 3 引脚部位缺口 X Y Z K/8 L/3 不计 4 边缘突起	次序	项目	判断标准	AQL
X Y Z K/8 不进入A区 T X Y Z K/8 不进入A区 不计 3、引脚部位缺口 X Y Z K/8 L/3 不计 4、边缘突起 A A A	1	尺寸状况	尺寸超出产品规格	1
K/8 L/3			1、一般缺口 X Y Z X Y Z B X Y Z K/8 不进入A区 不计 3、引脚部位缺口 X Y Z K/8 L/3 不计 4、边缘突起 X Y	

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						<u> </u>		 允许缺陷数						
						D		A/B 区	C区					
				×		D<0.2		不限						
					C	0.2 D<0.3		2	779					
2	TF 4T	△/T+1 <i>17/</i> 7	Y		C	0.3 D 0.5		1	一不限	0.50				
3 点状缸	缺陷	-	-		D>0.5		0		2.50					
			X:长径 Y:短径 D:平均		Y)/2									
								/ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	<i>A</i> ¥b					
			1	Ī	长度	宽度	Ę	允许缺陷						
				L	7.21	w o	00	A/B 区	C 🗵					
			*	不计 L 3		02	不计 2							
	线状缺陷		→ •	→ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			W 0.05 W 0.05		不限					
4			线状缸	线状	线状缸	线状	线状	线状缺陷			L 2.	5 —		2
				,										
				₩:宽度 片(刮伤、	污点、加	玉痕)有限度村	¥本则以 							
				<u> </u>		D		允许缺陷数						
	偏 [.]		X			D		∧ / D 🔽						
_	气泡/水纹/皱	光片		X				A/B区	C 🗵					
5		光片 水纹/皱		×	0	D 0.2		不限	C区	2.50				
5			Y	X		D 0.2 .2 D 0.5		不限 3 个	C 区 - - 不限	2.50				
5		水纹/皱	Y	X		D 0.2		不限 3 个 2 个		2.50				
5		水纹/皱	Y	X		D 0.2 .2 D 0.5 .5 D 1.0		不限 3 个		2.50				
6	ź	水纹/皱	1、丝[0 孔:按照	D 0.2 .2 D 0.5 .5 D 1.0		不限 3 个 2 个 0 个		2.50				
	外至	水纹/皱 纹	1、丝[2、丝[① 孔:按照 印宽度	D 0.2 .2 D 0.5 .5 D 1.0 D>1.0 R字符变形针孔		不限 3 个 2 个 0 个						
6	外望 打胶 (水纹/皱 纹 丝印	1、丝E 2、丝E 打胶面 1、PCI	印宽度:丝 积必须盖住 B 板烧焦、	① 孔:按照 印宽度 E ITO 引 版本不符	D 0.2 .2 D 0.5 .5 D 1.0 D>1.0 R字符变形针孔	,可判 (裂痕、 [!]	不限 3 个 2 个 0 个 断。 DK。	- 不限	2.50				
6 7	外约 打胶(PCB 和	水纹/皱 纹 丝印 〔硅胶〕	1、丝E 2、丝E 打胶面 1、PCI 2、PCI 1、元件 2、焊链	印宽度:丝积必须盖住 积必须盖住 B 板烧焦、 B 板金手指 牛装配上下、 易点尽可能;	孔:按照印宽度 ITO 引版本可有编译	D 0.2 .2 D 0.5 .5 D 1.0 D>1.0 (字符变形针孔) 1/2 标准宽度 线 5、线路剥离、 流化、腐蚀、胶 流化、腐蚀、胶	,可判(裂痕、 ¹ 状物、l k体宽度	不限 3 个 2 个 0 个 断。 DK。 导电过孔堵不 新裂现象	一 不限 一 不限 一	2.50				

标准为	文本	产品规]格书	型号		SX 0351	<u> 1V006</u>	页数	14	
(2)显	示功能	检验项目	及标准(]	作状态)						
				E	 断路	不	允许			
1	电性	能缺陷		短路	/大电流	不:	允许		1.0	
				视知	角错误	不:	允许			
			1、针孔缸	井口			1+116.40	14		
			<i>F</i>	≜ ±		宽度	接收规			
2	뒤	字划	B		В	W < 0.4 W 0.4	D 0.2 & D D 0.25 & D		2.50	
2	凸起	2/缺口	W	A A	*[D 0.1 则忽略不计		2.00	
				<u> </u>	<u>F</u>	宽度	接收规	格		
		.l . 		. <i>D</i>		W < 0.4	C、D、G			
3		小图案 且细				W 0.4	C、D、G		1.0	
				: 图案设计	尺寸	C、D:差异	₽尺寸 G=	E-F		
						D —	允许缺陷数			
	点状缺陷			l X			A/B ⊠	C区		
						< 0.1	不限	_		
						D<0.2	2	一不限		
4		点状缺陷		0.		D 0.25	0	_	2.50	
			径 D=(X+Y) 污迹在字[)/2	笔段缺陷"判					
			→ - 1	- w	长度	宽度	允许缺陷 A/B区	3数 C区		
			L		,	不计	W 0.02	不计		
				_	L 3	W 0.03	2	──		
5	线制	犬缺陷	L: 长度	W: 宽	L 2.5	0.03 < W 0			2.50	
	度			度			W > 0.05	按点状缺陷	9判定	

13. LCM 的使用

16-1 晶显示模块

- LCD 是由玻璃和偏光片组成,在搬运过程中,请注意以下事项:
- (1) 请保证使用和存储是在规定的温度范围之内。高温高湿会导致偏光性能降级,产生气泡或者偏光片剥落。
- (2) LCM 表面的偏光片质软容易划伤,不要用硬度大于 HB 铅笔芯的任何物品(玻璃,镊子等)接触,挤压或者摩擦外露的偏光片
- (3) 用来粘合底/面偏光片和反射片的有机粘合剂会被一些化学物质,如丙酮,甲苯,乙醇和异链烷烃破坏,建议用 N-己烷进行清洁。
- (4) 显示屏表面有脏污,请吹拂同时用干的软布擦拭表面。如果脏污比较严重,建议用脱脂棉或者其他的 柔软材料,如擦拭用软皮,浸湿异丙醇或酒精之后进行擦拭其表面。不要用力擦洗以避免损伤显示 屏表面。
- (5) 不可使用以下溶剂:水,酮,芳香烃。其溶剂可能损坏偏光片。
- (6) 避免与油和脂肪接触。
- (7) 因低温而产生的表面浓缩和端子的连接,会损坏,染污或者弄脏偏光片。产品在低温测试之后,需要先在容器之内烘干之后才可以与室温接触。
- (8) 不要放置或贴附任何东西在屏幕上,以免留下痕迹。
- (9) 不要裸手触摸显示器。这样会染污显示区域,并且降低接线端子之间的绝缘性(对于偏光片一些化妆品是确定的)
- (10) 采取措施尽量减小电极的腐蚀。水滴,凝结的潮气或者高湿环境下的电流会加速电极的腐蚀。
- (11) 因为玻璃是易碎的,搬运过程中容易产生缺口(特别是边缘)。请避免跌落或震动。

16-2 组装 LCM 的注意事项

因为 LCM 是高精密度的组装和调试,请避免因过度的撞击,或者进行任何的变更或者修改。

- (1) 不要更改或者改变金属框架的突起形状。
- (2) 不要在线路印刷板上另外钻孔,修改形状或者改变其上的元器件的位置。
- (3) 不要损坏或者修改线路印刷板上的图案。
- (4) 绝对不要更改斑马条(导电橡胶)或者热压连接器。
- (5) 除了焊接接口,不要用烙铁进行任何的修改或者变更。
- (6) 不要跌落,弯曲或者扭转 LCM。特别注意不可用力拉或者扭转 I/O 口或背光的排线。
- (7) 为了防止 FPC 破裂,请特别注意 FPC 的可弯曲部分,覆盖层的边缘,镀金区域的表面,焊接区域或者通孔区域。

16-3 静电放电控制

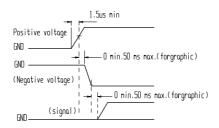
因为模块使用 CMOS LSI,像平常的 CMOS IC 一样需要注意静电的防护。

- (1) 手持 LCM 时,请确保身体是接地的。为了尽量减少因静电而产生的 LCM 功能降级,在运输模块时小心操作避免接触到:印刷电路板的外露区域,元件的电极。
- (2) 从包装袋中取出 LCM 或者与其他装置装配时,请确保模块与你的身体的电势一致。
- (3) 焊接 LCM 的接线端时,请确保烙铁的交流电源没有漏电。
- (4) 使用电动螺丝批装配 LCM 时,电动螺丝批须接地以尽可能减小马达换向器产生火花而引起的电磁波 辐射
- (5) 尽量使你的工衣,工作台达到地电位。
- (6) 为了减少静电产生需要注意工作环境的空气不可太干燥。建议相对湿度为 50%-60%。

标准文本	产品规格书	型号	<u>SX035HV006</u>	页数	16
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16-4 操作注意事项:

- (1) 液晶的视角会随着驱动电压(VO)的变化而变化,调节 VO 使之显示为最好的对比度。
- (2) 极限值以上的驱动电压会缩短 LCD 的使用寿命。
- (3) 如果 LCM 长时间的显示一个图案,图案可能残存(似鬼影)或者其暗影不规则的显示。隔段时间后使用即可恢复正常。请注意这种现象不会影响显示。
- (4) 在低于操作温度范围的温度下工作会造成响应时间延长。但是,这并不意味着 LCD 不能工作。它会在温度恢复至规定范围后恢复正常。
- (5) 如果在工作过程中显示区域被猛烈的挤压,显示会异常。但是,关电后重新启动后会恢复正常。
- (6) 端子的凝结水汽会导致电化学反应,破坏线路的接线端。所以,必须在低于 40°C,50% RH 环境条件下中使用。
- (7) 电源打开,在正极/负极电压稳定之后,输入信号。



16-5 储存

如果 LCD 需储存几年,以下的预防事项是必要的。

- (1) 保存于密封的聚乙烯袋内。适当的密封就不需要干燥剂。
- (2) 存储于黑暗的环境中。不要暴露于日光或者荧光灯的直射下,保持温度在0℃ 到35℃之间。
- (3) 偏光片的表面不可与其他物体接触。(建议存于运输用的容器中)
- (4) 环境条件
 - 不要在 70°C 的环境中放置超过 160 小时。
 - 不要在-20°C 的环境中放置超过 48 小时。

16-6 安全

- (1) 建议将损坏的或者不要的 LCD 压成碎片,用溶剂,如丙酮,乙醇清洗后烧毁。
- (2) 如果从破损的玻璃中泄漏的液晶不慎粘到手,请用肥皂,清水彻底清洗。

16-7 权利限制

除非经过上线科技公司和客户的认可,在交货之日起一年之内,上线科技公司会按照自己承诺的检验标准对 LCD 功能性的缺陷进行修改。外观上缺陷的产品必须在交货之日起30天之内退回上线科技公司.以上日期依运输文件为准。上线科技公司的权力范围仅限于对以上项目的修复和/或者替换。上线科技公司不会对由此引起的或者并发的其他事件负责。

16-8 权利范围之内的退货

如有违反以上所提到的预防注意事项而造成的不良将不予以保证。典型例子如下:

- 破坏 LCD 玻璃。
- 任何形式的线路修改,包括增加电器元件。

模块的修改必须基于客户相互的文件协议。模块退回时,必须要有详细的不良描述。客户安装的 连接器或者排线必须完全地取掉,不能损坏 LCM 的 PCB 孔,连接器和接线端。

规格书技术资料备注:由于液晶产品技术不断改进,本规格书有未能把所有技术资料体现,请客户务必在确定产品时,以样品质量和结构为准,以免造成生产批量出现不必要的损失,谢谢支持和配合