



Development Board

Design around Timex SCLD
PSG on TMX ports & 80K RAM on board
RGB Video output
Minor corrections to /IORQGE & Kempston Interface added
fatia.eu
Sheet: /
File: 2020.03 TC2048 Board.sch
Title: Timmy - Timex Computer 2048 Clone
Size: A2 Date: 2021-08-03 Rev: 0.2
KiCad E.D.A. kicad (5.1.9-0-10.14) Id: 1/1

Change log:
rev. 0.2 - Corrected edge connector PCB layout
BT audio added
ESP-WIFI added
rev. 0.12 - Outstanding corrections to:
U12 connections
U3 ROMSEL connection
SW1 footprint
R38 & R39 values
components added:
C105 & R32
Comments added
rev. 0.11 - keyboard connections corrected
Internal speaker added
80K switching corrected; PCB manufactured
rev. 0.1 - first draft