



Development Board	
Design around Timex SCLD	
PSG on TMX ports & 80K RAM on board	
RGB Video output	
Minor corrections to /IORQGE & Kempston Interface added	
fatia.eu	
Sheet: /	File: 2020.03 TC2048 Board.sch
Title: Timmy - Timex Computer 2048 Clone	
Size: A2	Date: 2021-08-03
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Change log:

- rev. 0.2 - Corrected edge connector PCB layout
- BT audio added
- PSG IOA connector added
- rev. 0.12 - Outstanding corrections to:
 - Upper RAM /RAS & /WE connections
 - U12 connections
 - U3 ROMSEL connection
 - SW1 footprint
 - R38 & R39 values
 - audio connections
- Components added:
 - C105 & R32
- Comments added
- rev. 0.11 - keyboard connections corrected
- Internal speaker added
- 80K switching corrected; PCB manufactured
- rev. 0.1 - first draft