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| **EC392** | **Digital System Design Lab** | **0L:0T:2P** | **1 credits** |

1. Introduction to Digital Electronics Lab- Nomenclature of Digital Ics, Specifications, Study of the Data Sheet, Concept of Vcc and Ground, Verification of the Truth Tables of Logic Gates using TTL ICs.
2. Implementation of the Given Boolean Function using Logic Gates in Both Sop and Pos Forms.
3. Verification of State Tables of Rs, J-k, T and D Flip-Flops using NAND & NOR Gates
4. Implementation and Verification of Decoder/De-Multiplexer and Encoder using Logic Gates.
5. Implementation of 4x1 Multiplexer using Logic Gates.
6. Implementation of 4-Bit Parallel Adder Using 7483 IC.
7. Design , and Verify the 4- Bit Synchronous Counter
8. Design, and Verify the 4-Bit Asynchronous Counter.
9. Simulation of MOS Inverter with different loads using PSPICE software
10. Simulation of CMOS Inverter for different parameters *Kn*, *Kp* as a design variable in suitable circuit simulator software.
11. Design of a 4-bit Multiplexer using VHDL\Verilog
12. Design of a decade counter using VHDL\Verilog.
13. Design of a 3-input NAND gate and its simulation using suitable logic simulator

**Book List**

1. Douglas L.Perry, "VHDL: Programming by Example", McGraw-Hill, 2002.
2. Charles H. Roth, Lizy Kurian John, "Digital systems design using VHDL", Thomson, 2008.