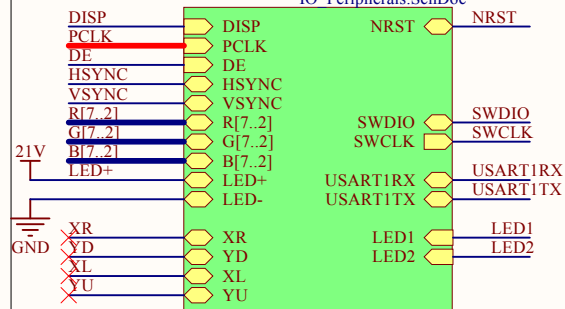
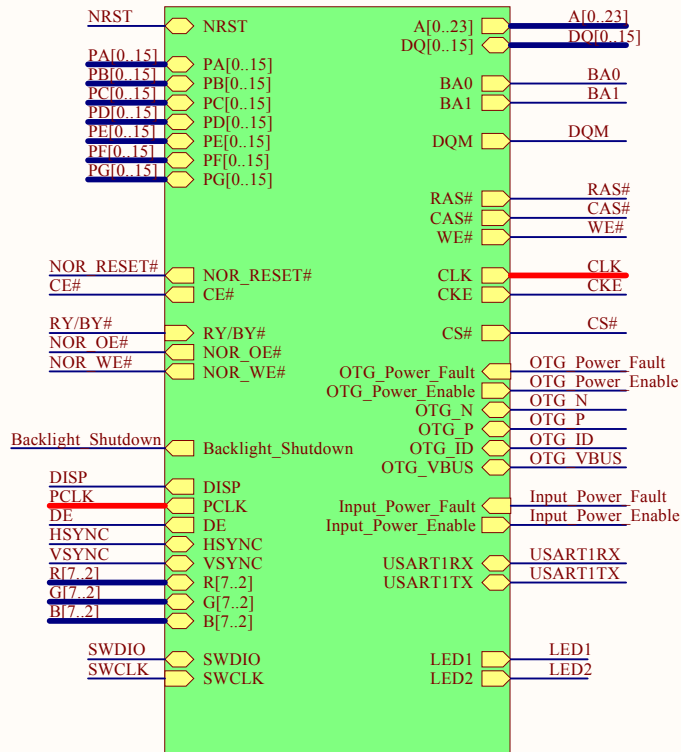
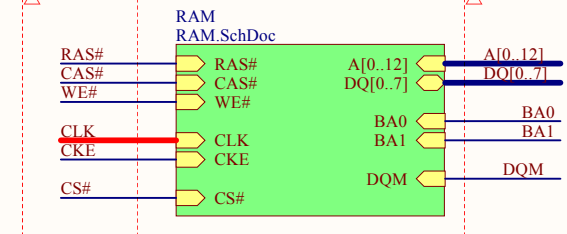
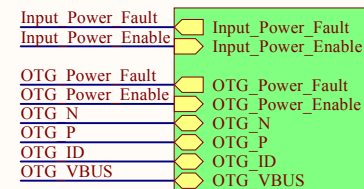


IO_Peripherals
IO_Peripherals.SchDocMCU
STM32F429ZI.SchDoc

Memories

Power
Power.SchDocUSB
USB.SchDoc

1

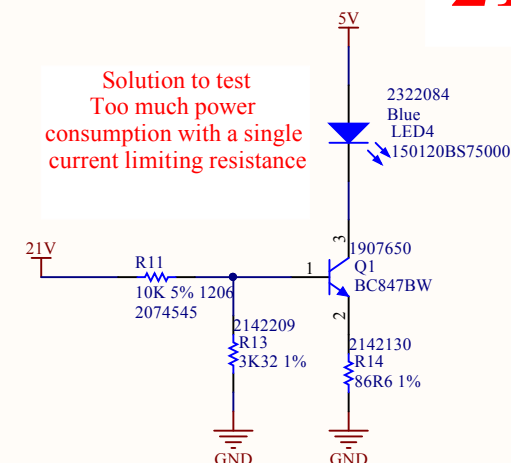
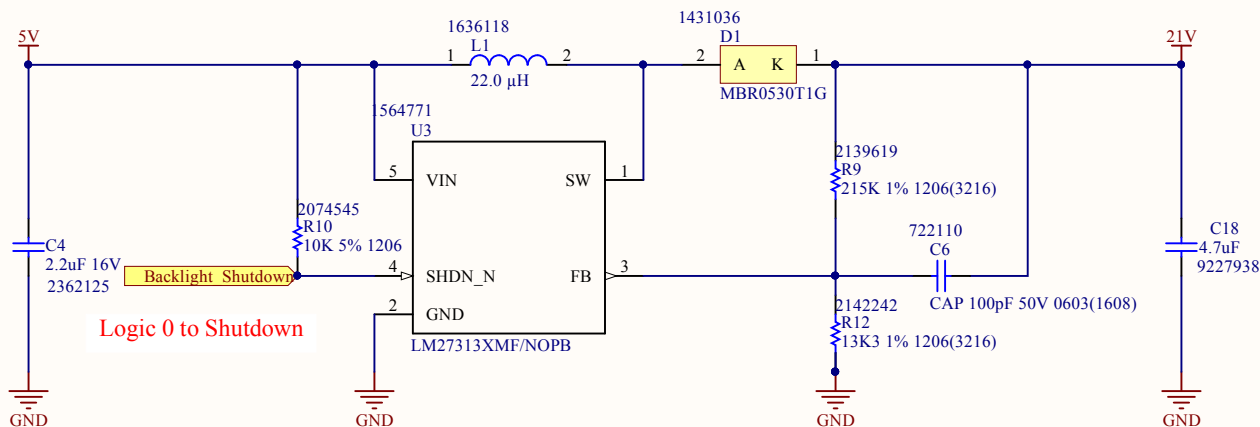
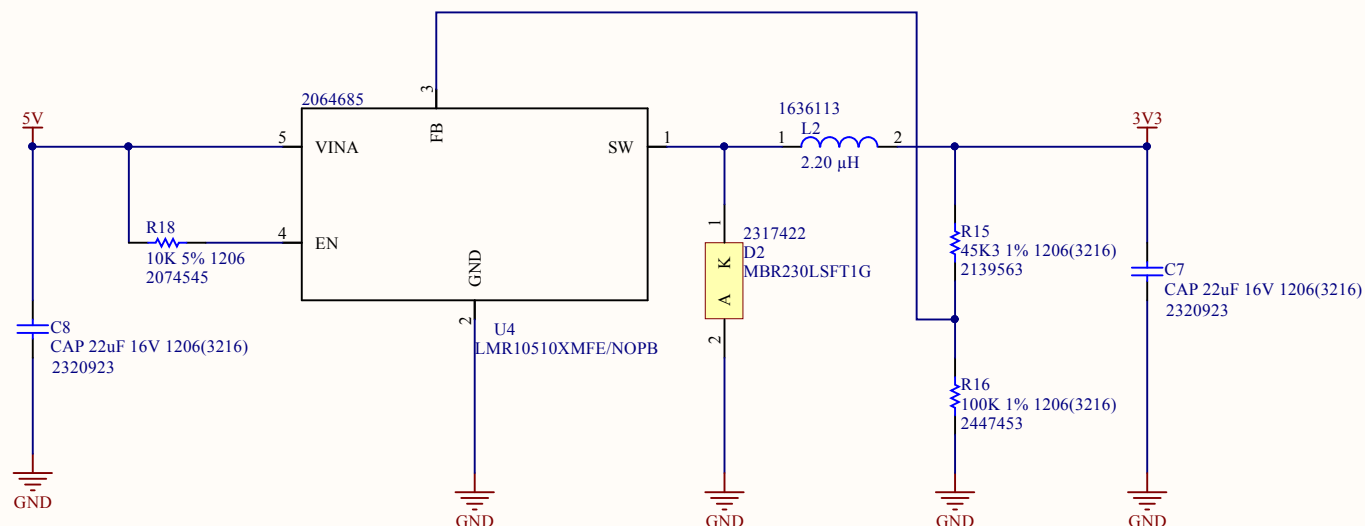
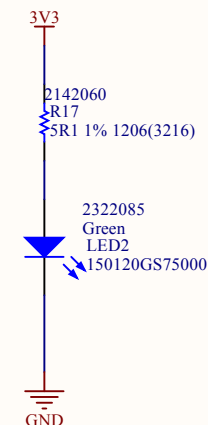
2

3

4

21V

Solution to test
Too much power
consumption with a single
current limiting resistance

**3V3**

Title Power		Drawn By: Badr BOUSLIKHIN		<div>INSA Lyon Departement GE</div> <div>INSA</div> <div>INSTITUT NATIONAL DES SCIENCES APPLIQUÉES LYON</div>
Size: A4	Number: *	Revision: 1.0		
Date: 06/06/2015	Time: 17:46:51	Sheet 2 of 7		
File: C:\Users\Badr\Documents\prt-ge-s2-2015-svn\trunk\Board\02_Schema BoM Layout\PRT_STM32F429ZI_Eval-Board\Power.SchDoc				

1

2

3

4

No ERC placed here targeting the duplicate names error

A

B

C

D

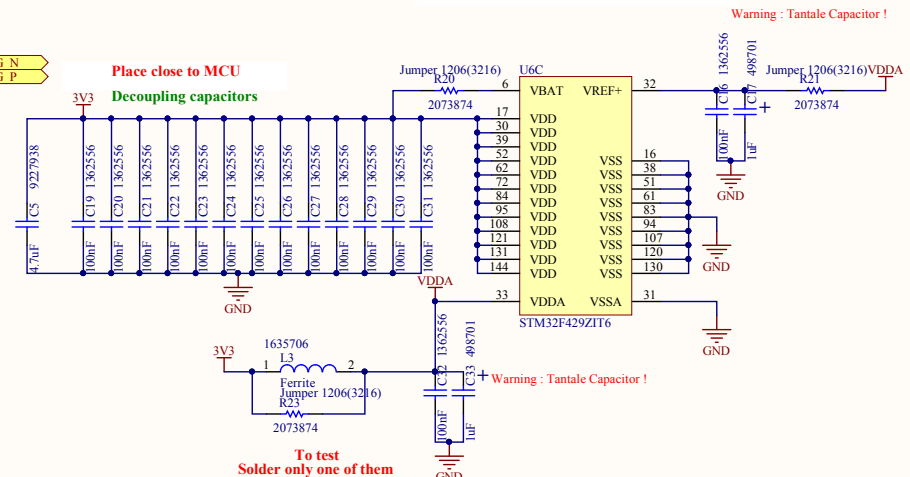
A

B

C

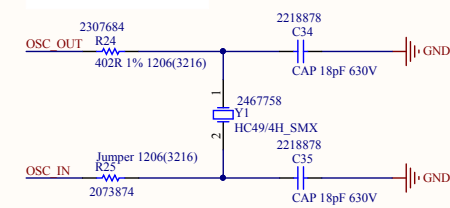
D

MCU Power

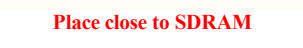
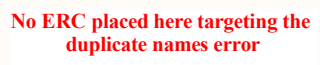



HSE XTAL

402 Ohm value to tune 5 to 6 times ESR value of the Quartz

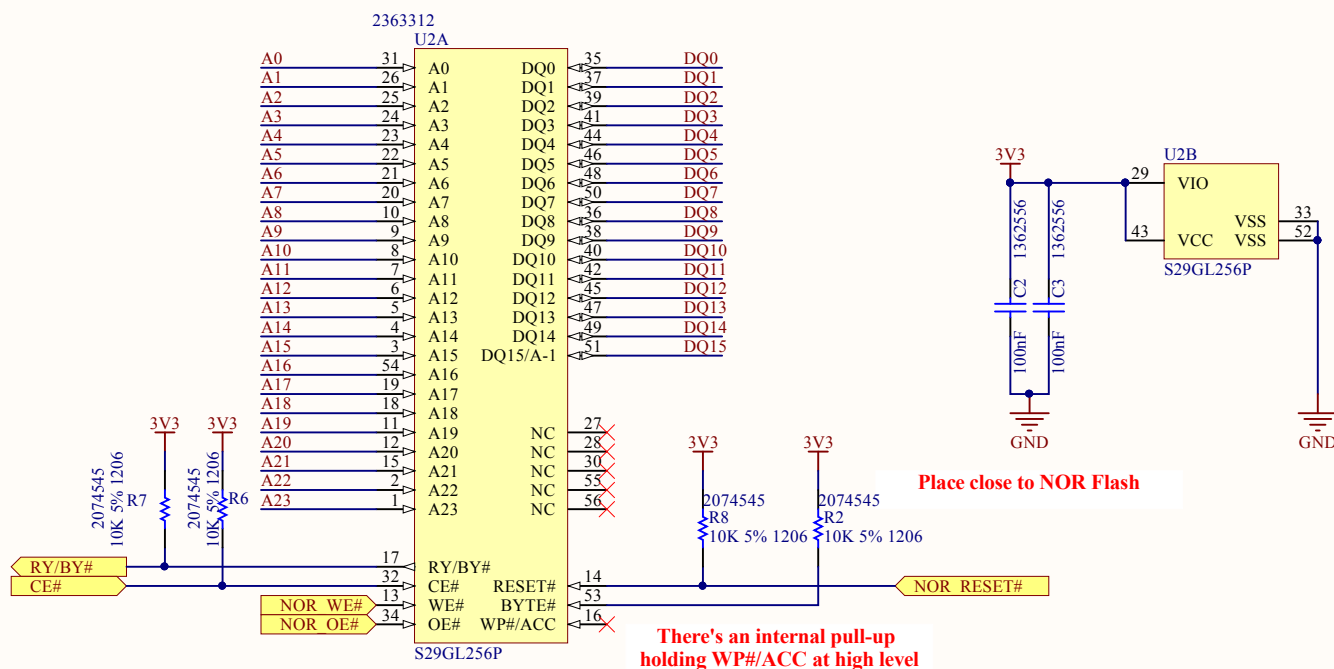


SDRAM/NOR Shared Signals
BA0/A14
BA1/A15

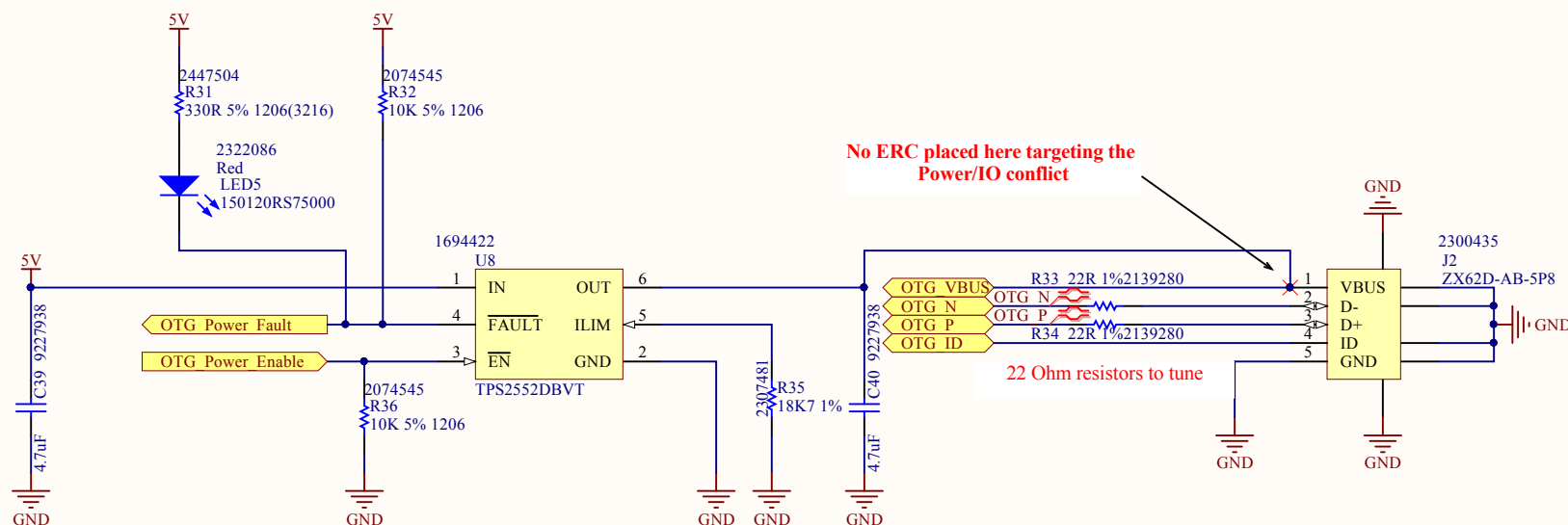
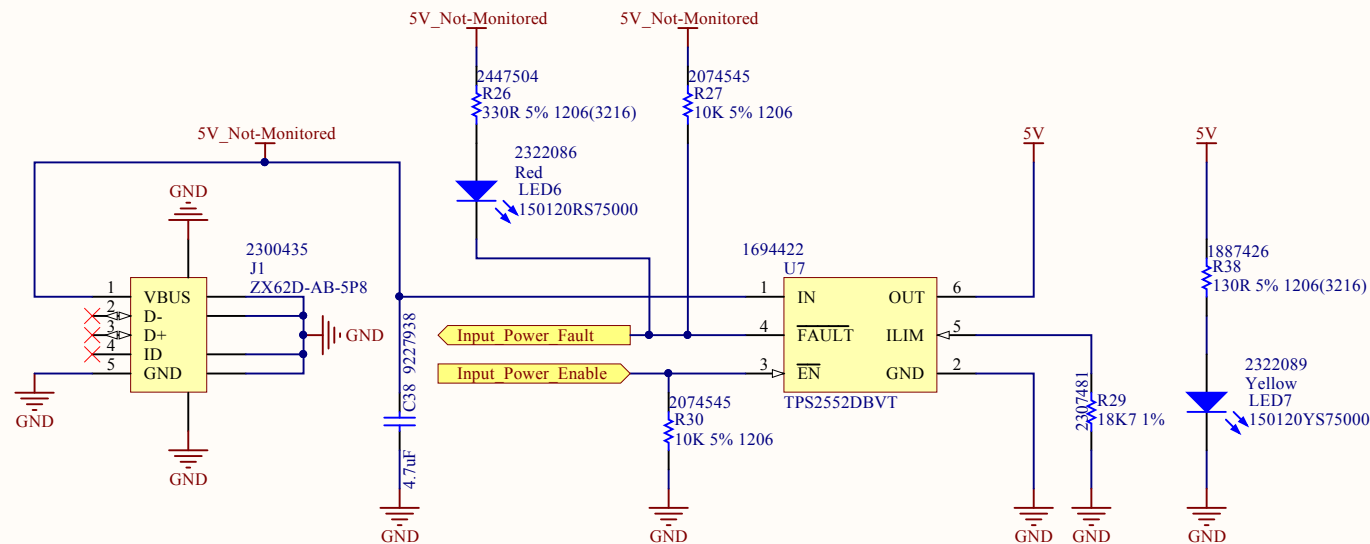



Title <i>RAM</i>		Drawn By: Badr BOUSLIKHIN		INSA Lyon Departement GE  INSTITUT NATIONAL DES SCIENCES APPLIQUÉES LYON
Size: A4	Number: *	Revision: L0		
Date: 06/06/2015	Time: 17:46:52	Sheet 4 of 7		
File: C:\Users\Badr\Documents\prt-ge-s2-2015-svn\trunk\Board_02_Schema_BoM_Layout\PRТ_STM32F429Z1_Eval-Board\RAM_SchDoc				

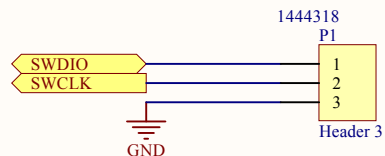
A[0..23] A[0..23]
DQ[0..15] DQ[0..15]



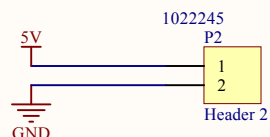
USB OTG FS



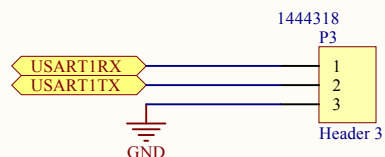
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Size: A4	Number: *	Revision: 1.0		
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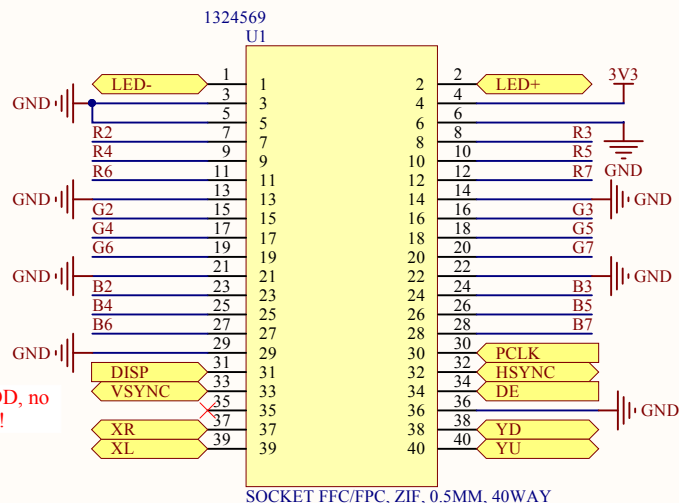
SW Debug port



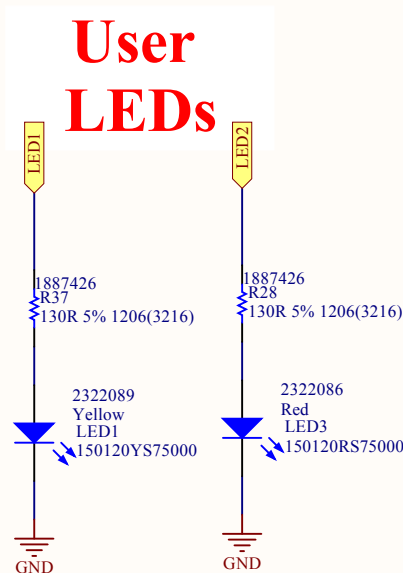
Alternate Power Input



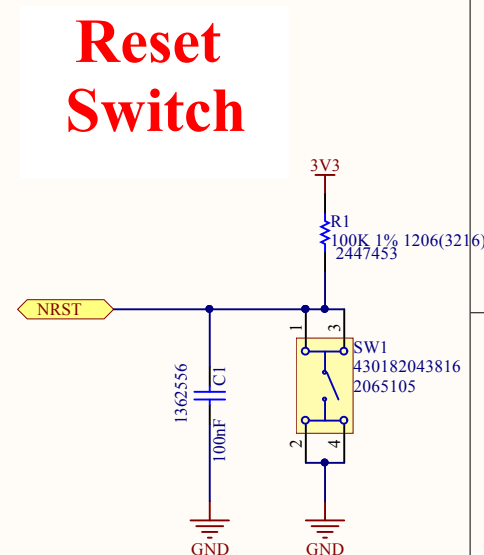
USART1 interface



LCD Interface



User LEDs



Reset Switch

Boot mode selection pins		Boot mode	Aliasing
BOOT1	BOOT0		
x	0	Main Flash memory	Main Flash memory is selected as the boot space
0	1	System memory	System memory is selected as the boot space
1	1	Embedded SRAM	Embedded SRAM is selected as the boot space