

Process Control: Part II- Model Predictive Control (EE6225, AY2019/20, S1)

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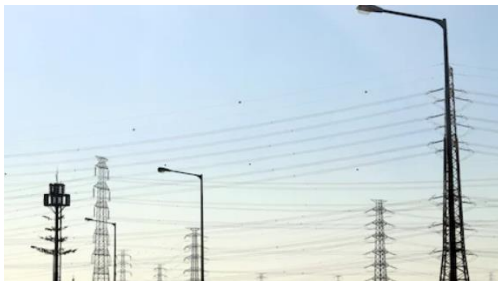




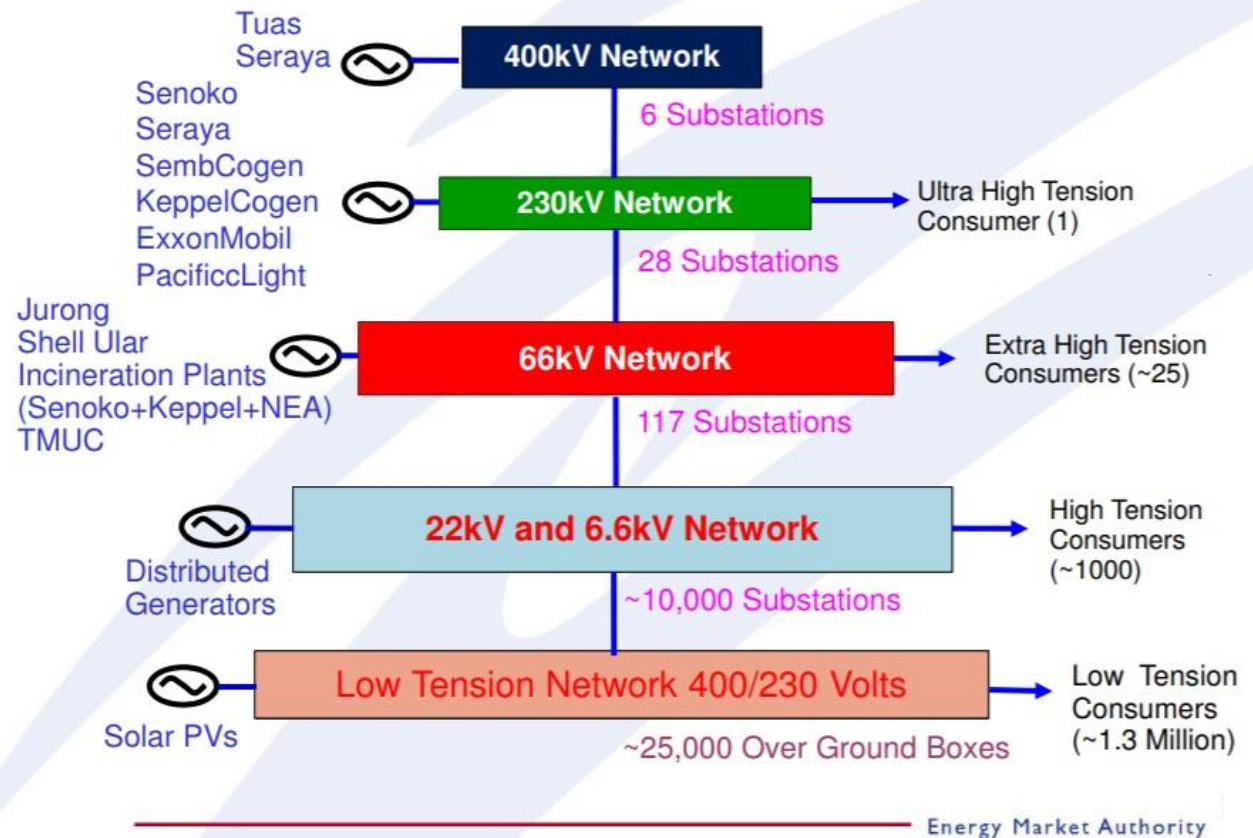


Transmission System



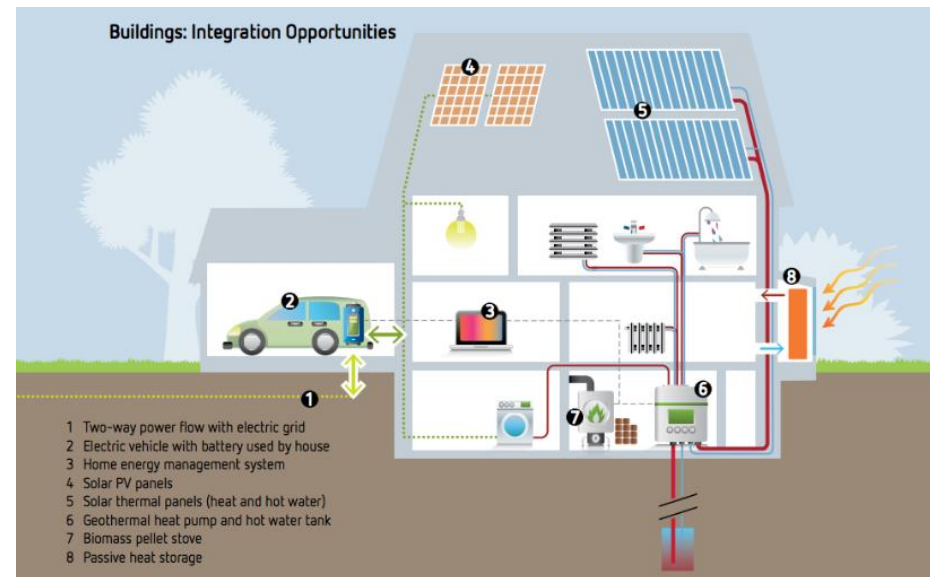
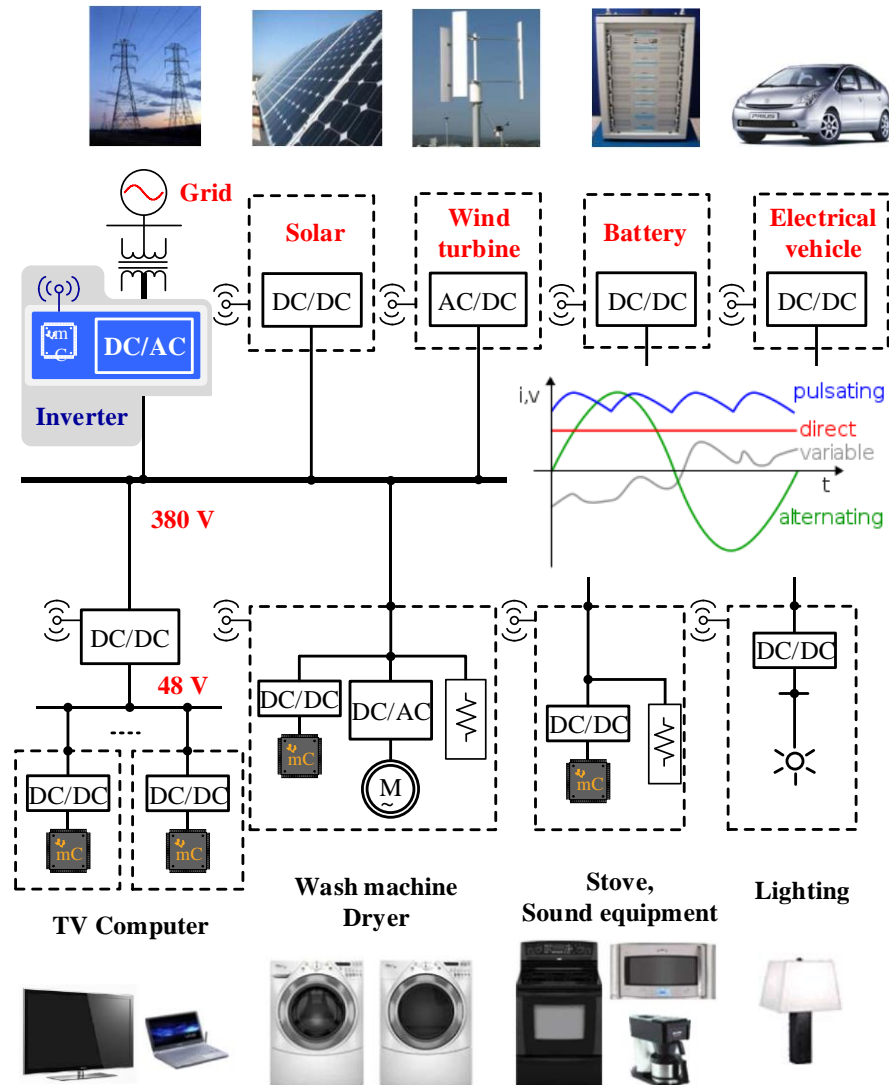


Power System Overview



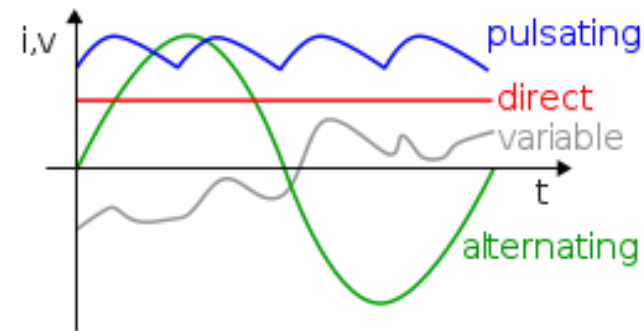


Future renewable energy sources-based building



OPERATION PRINCIPLE OF THE POWER CONVERTERS

[31/10/2019]

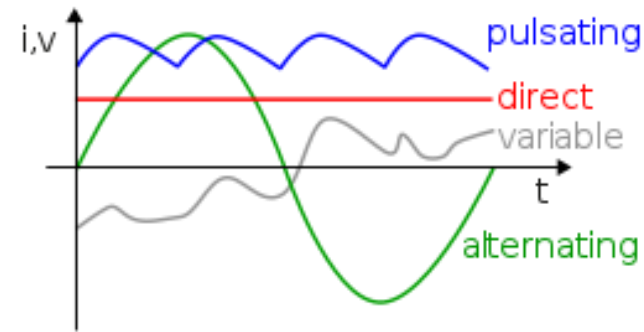


- Operation principle of the DC/DC converter
- Operation principle of the DC/AC inverter



OPERATION PRINCIPLE OF THE POWER CONVERTERS

[31/10/2019]



- Operation principle of the DC/DC converter
- Operation principle of the DC/AC inverter



DC/DC CONVERTER

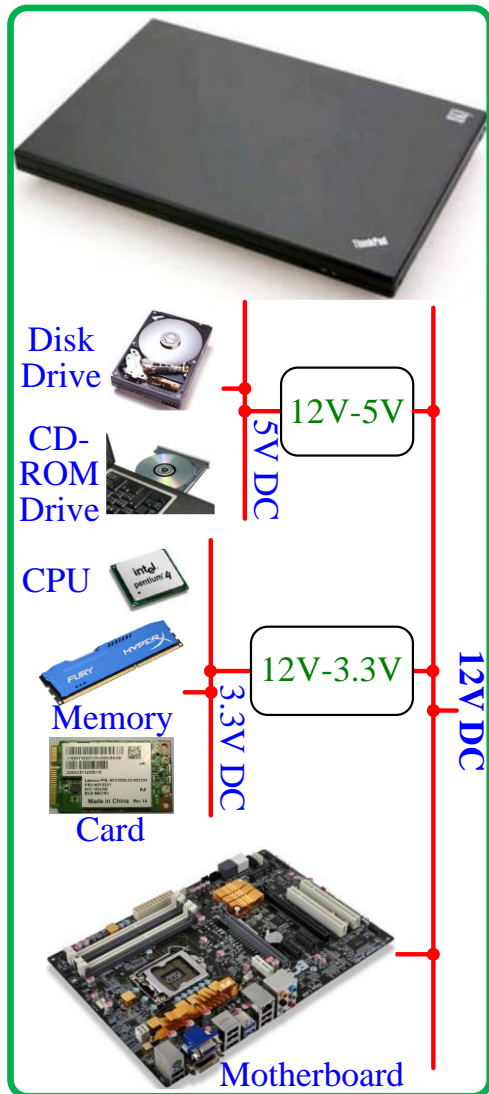
- Buck converter
- Boost converter
- Buck-Boost converter

DC/DC CONVERTER

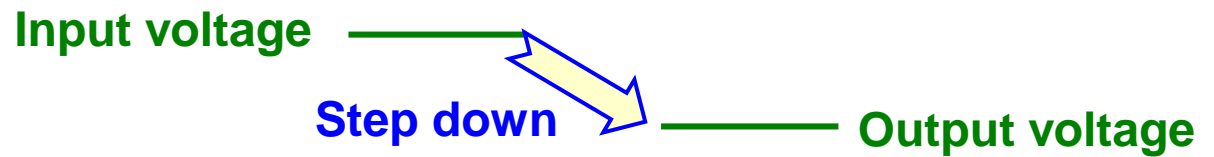
➤ Buck converter

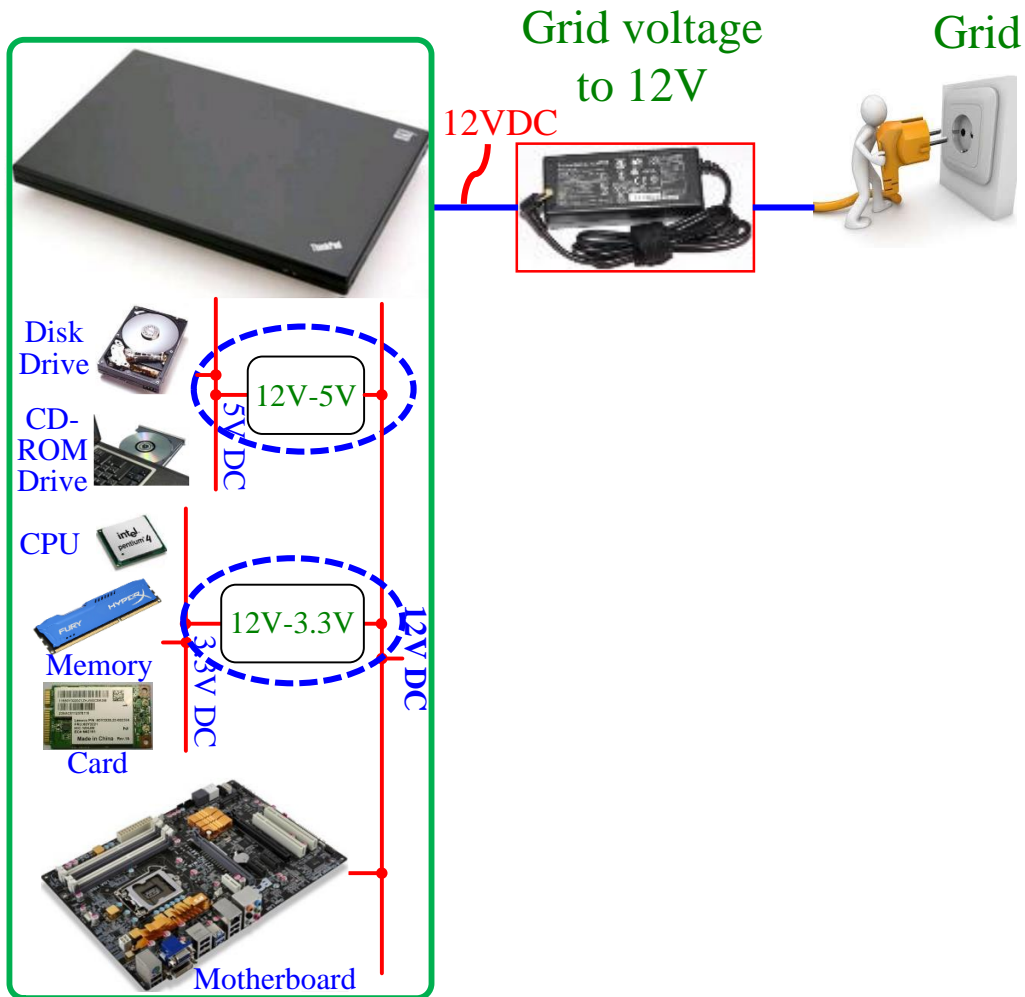
➤ ~~Boost converter~~

➤ ~~Buck Boost converter~~

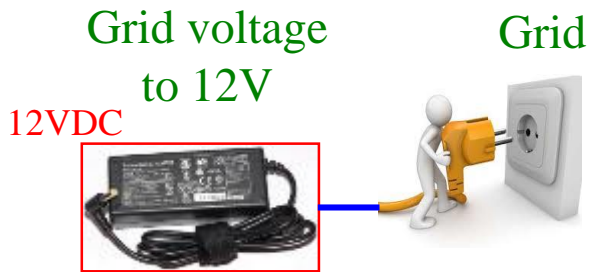
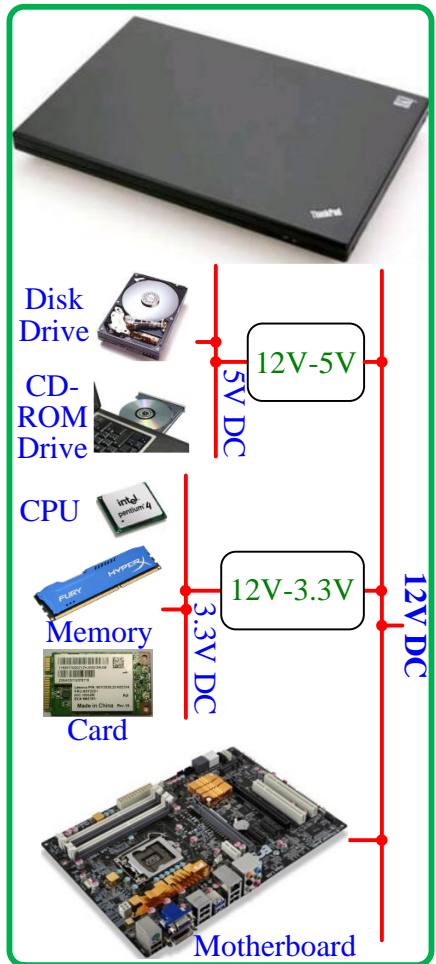


BUCK CONVERTER (STEP DOWN DC/DC CONVERTER)

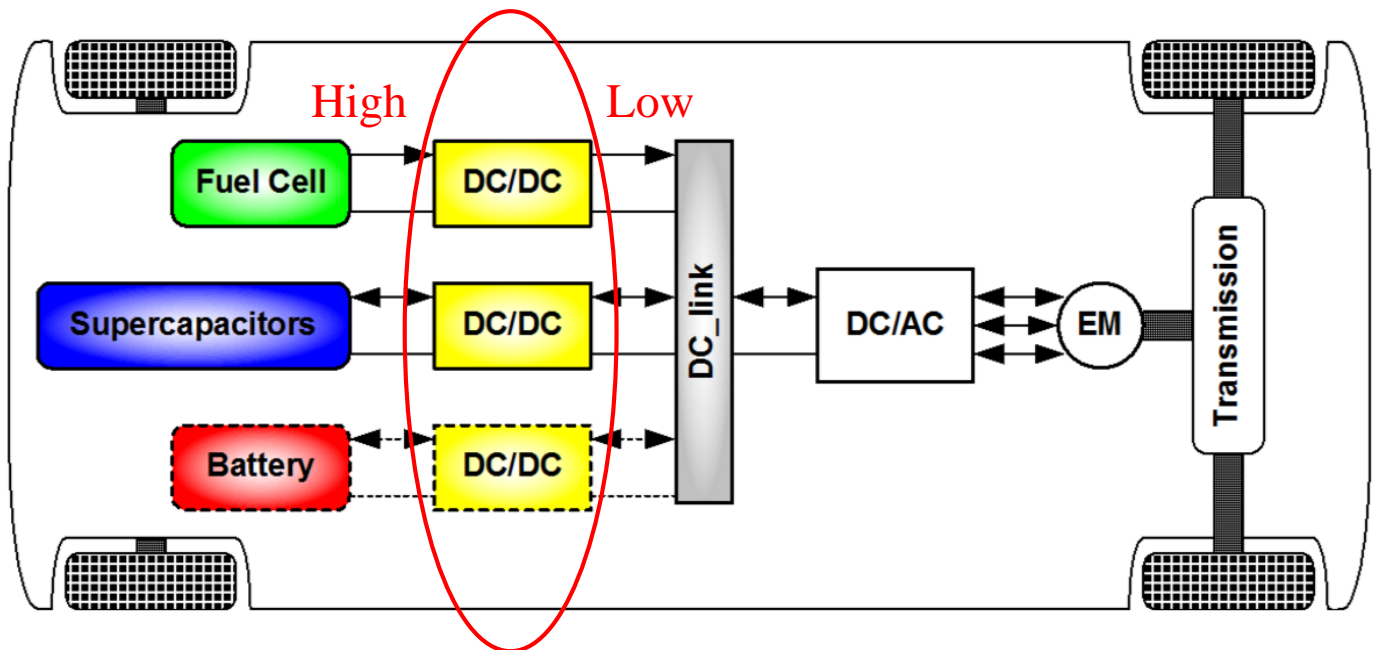
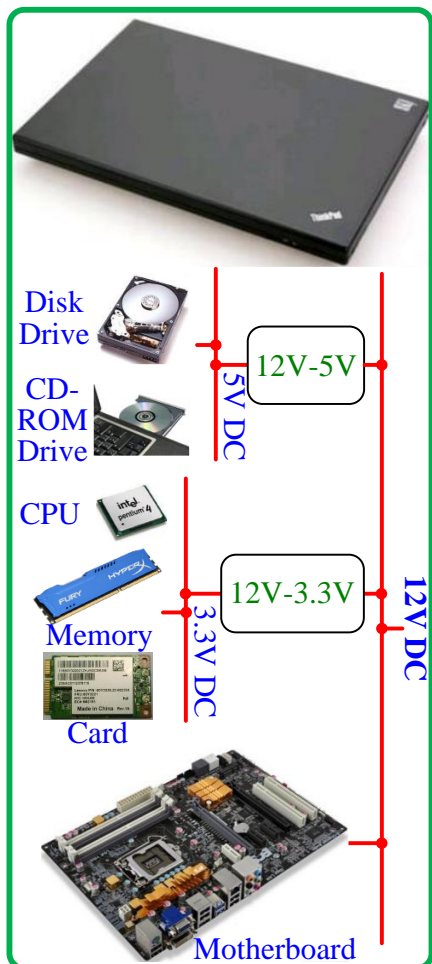


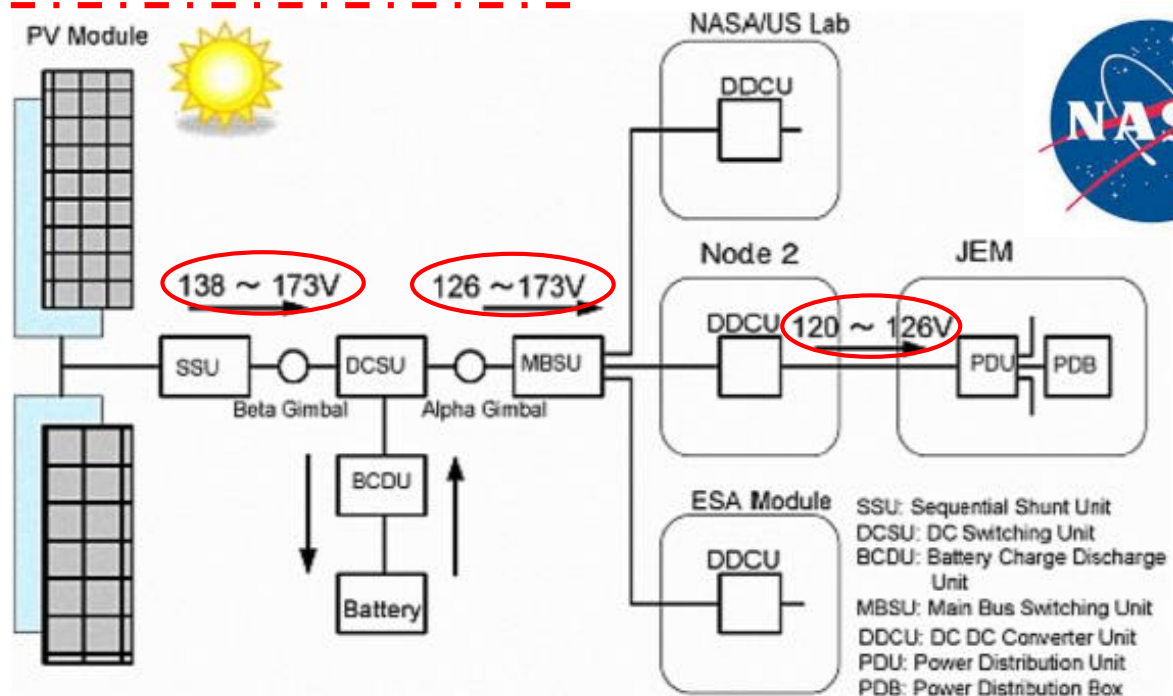
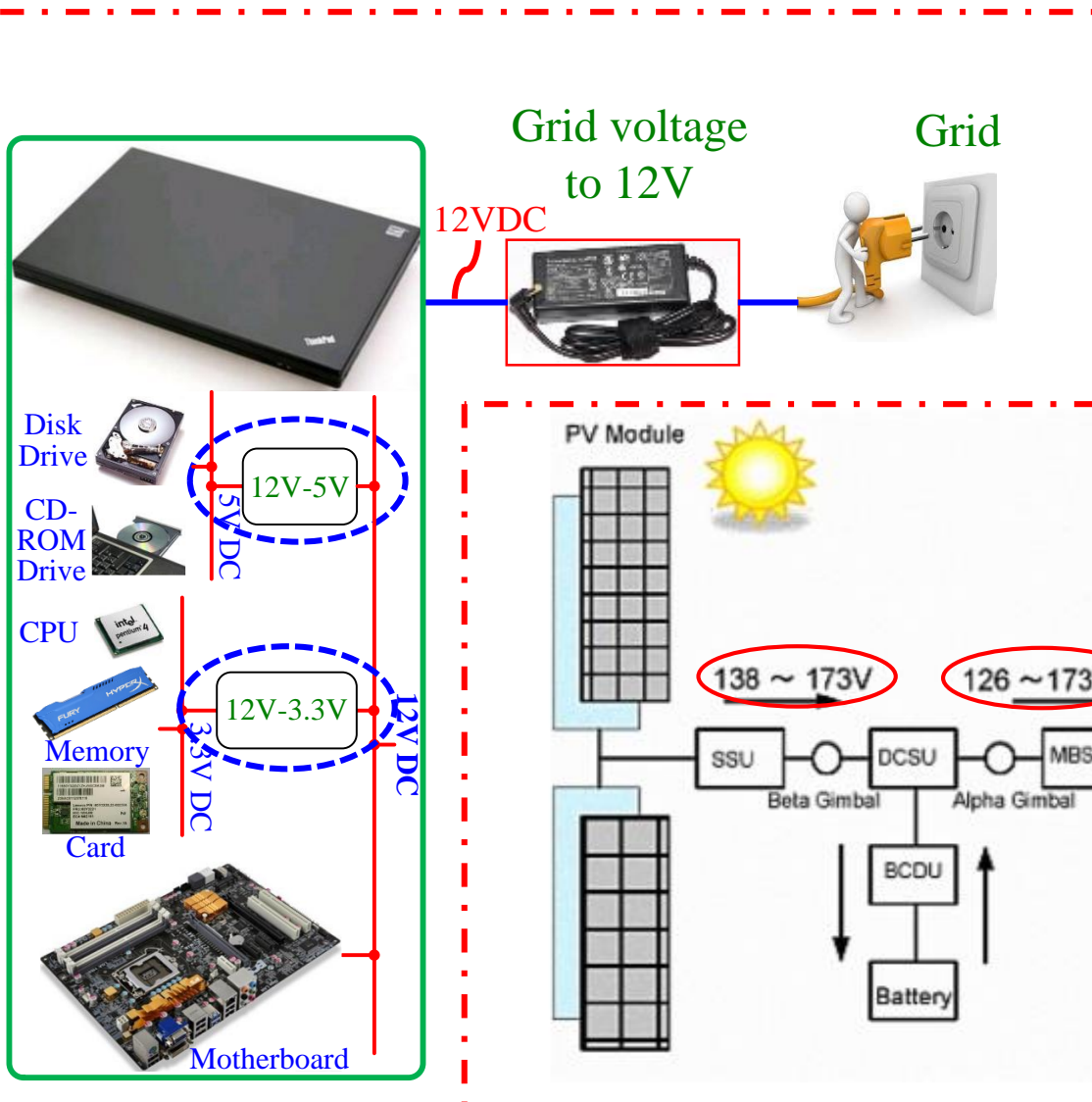


Other applications of step-down DC/DC power supplies: Aircraft

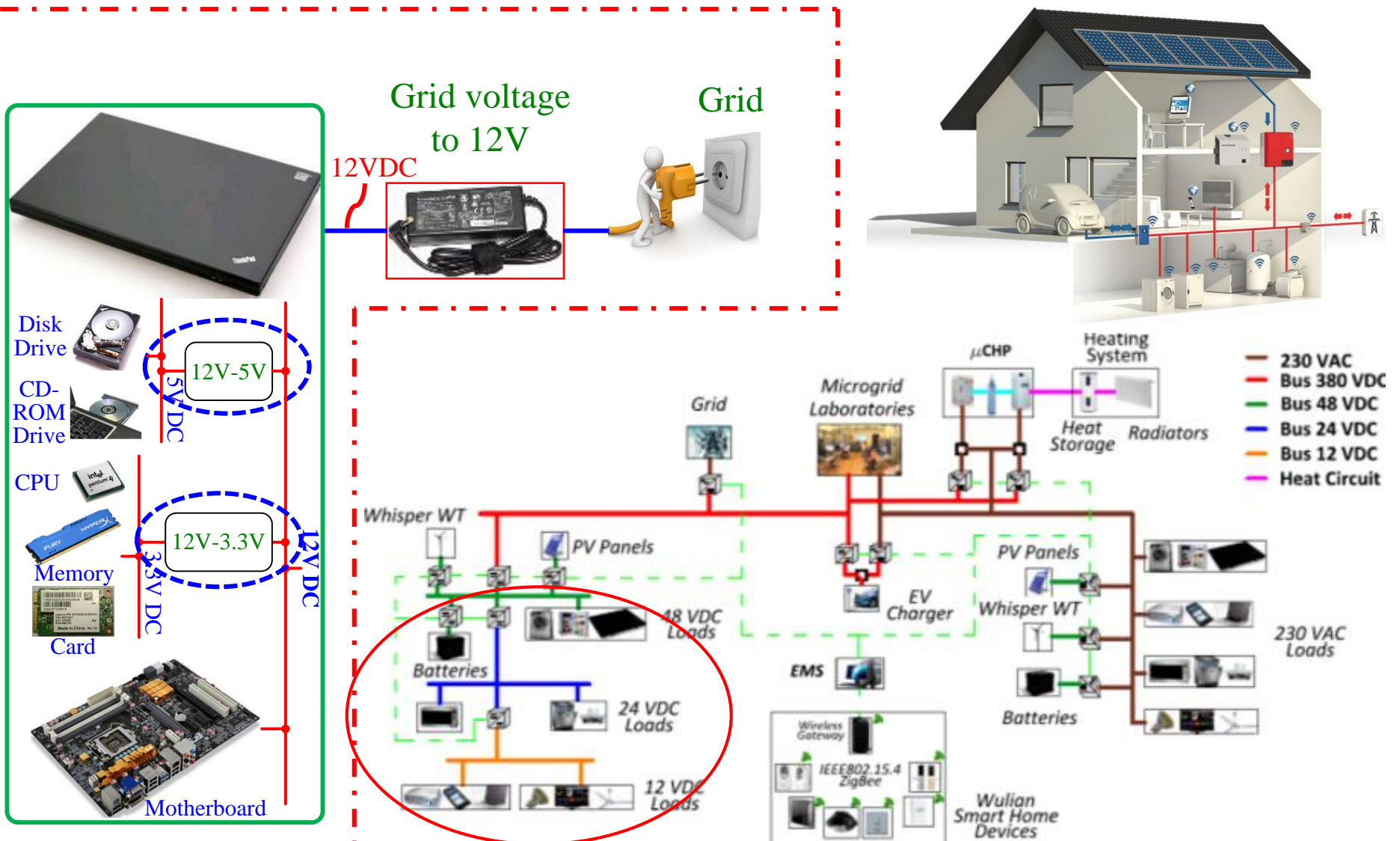


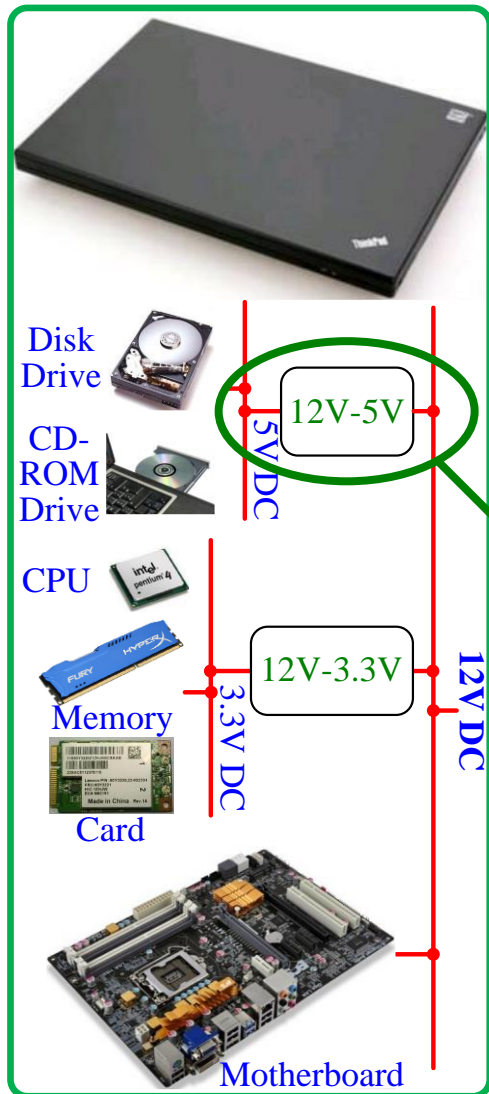
Other applications of step-down DC/DC power supplies: Electric vehicle





Other applications of step down DC/DC power supplies: Renewable energy system in the house



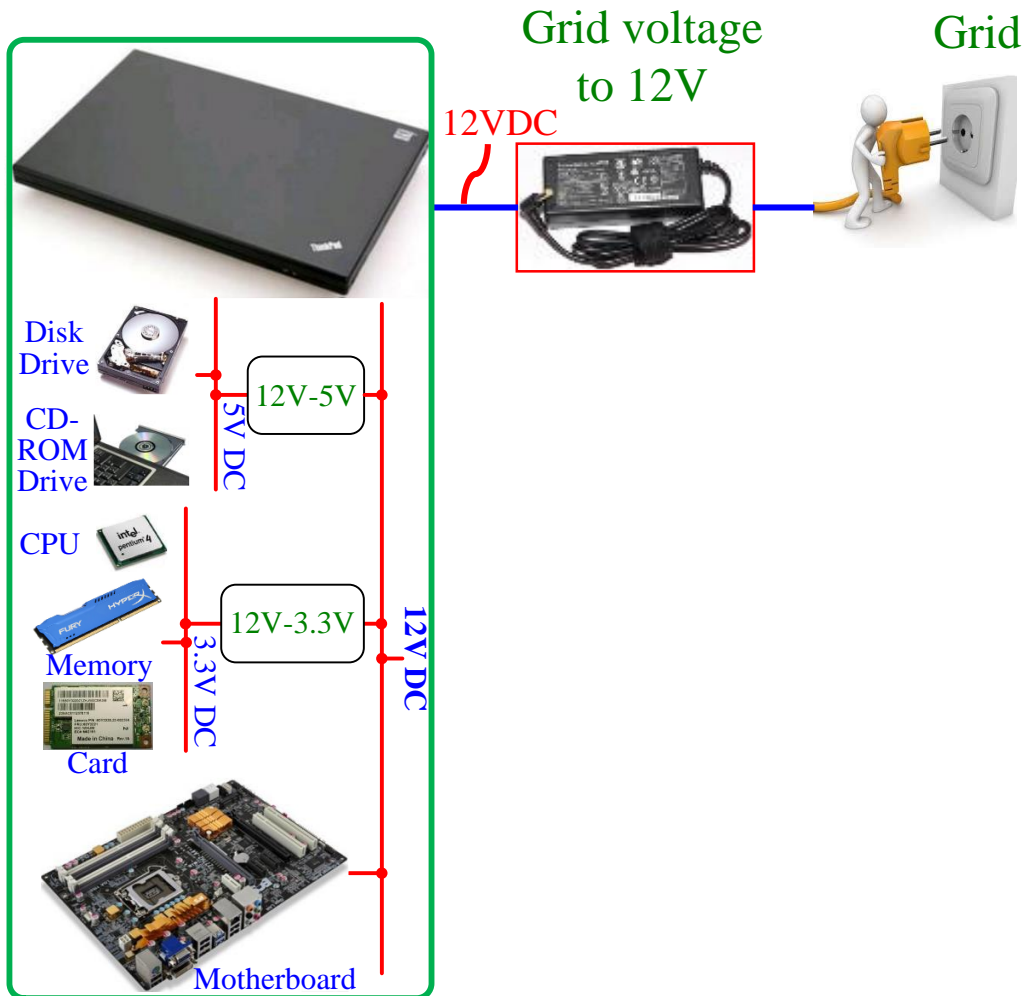


BUCK CONVERTER (STEP DOWN DC/DC CONVERTER)

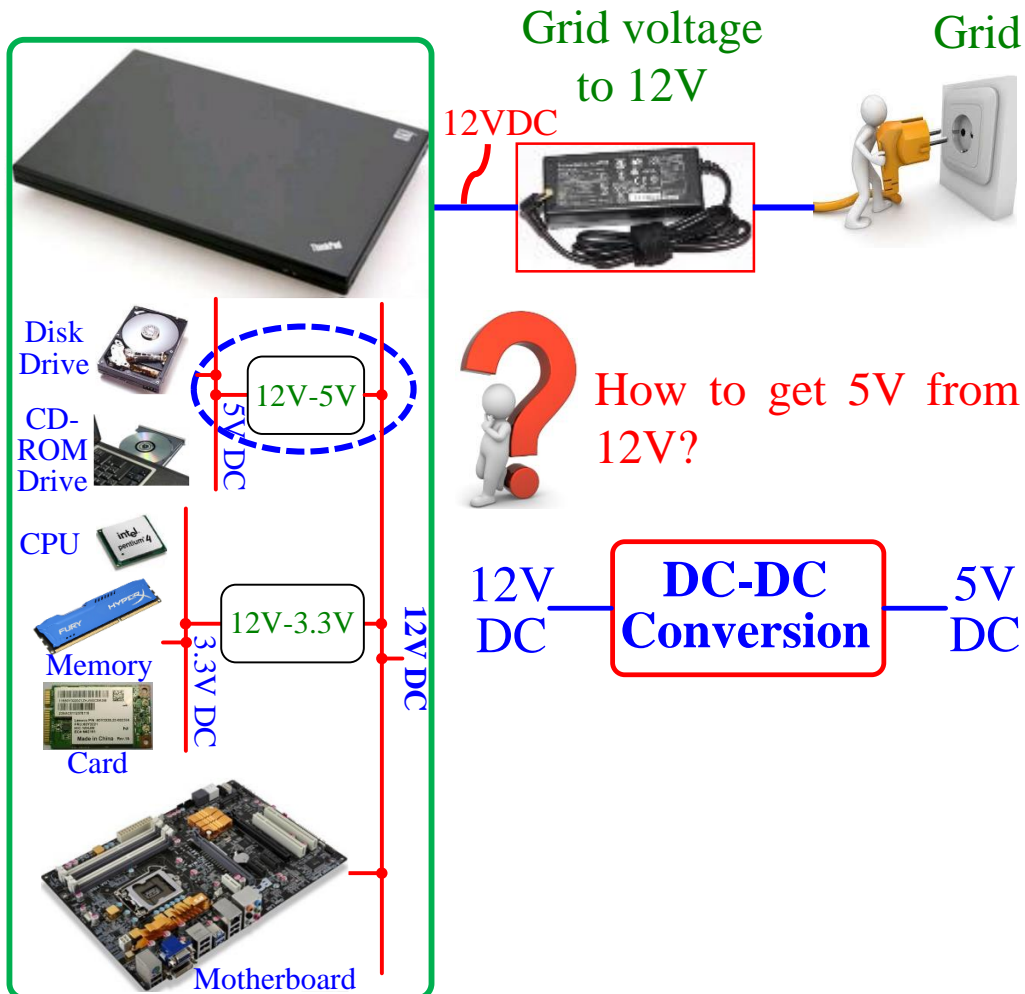
How to design a
12 V to 5 V DC/DC
converter



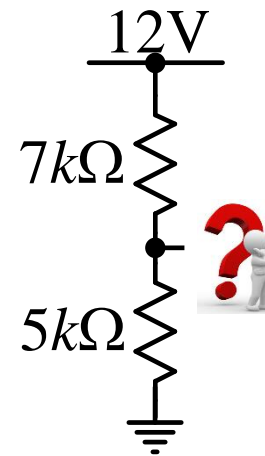
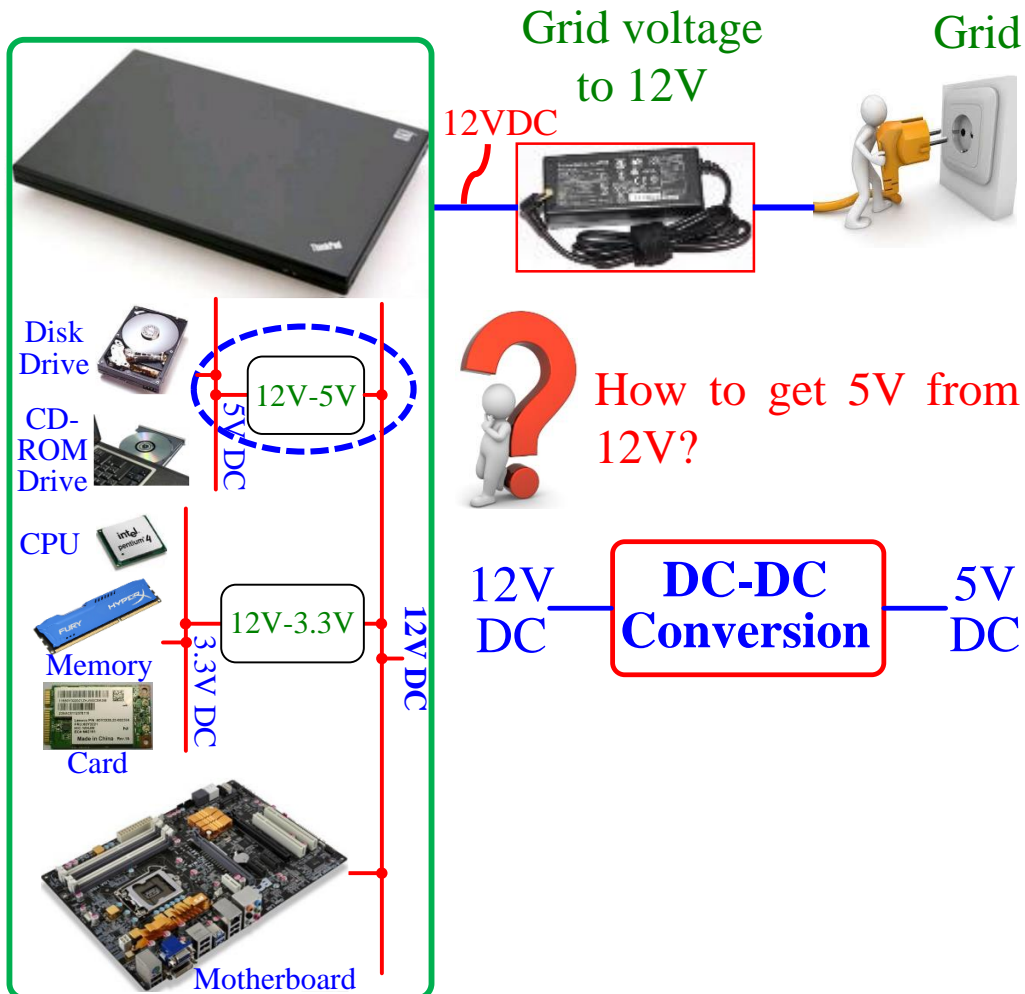
How to design step down DC/DC power supply?



How to design a 12 V – 5 V power supply?

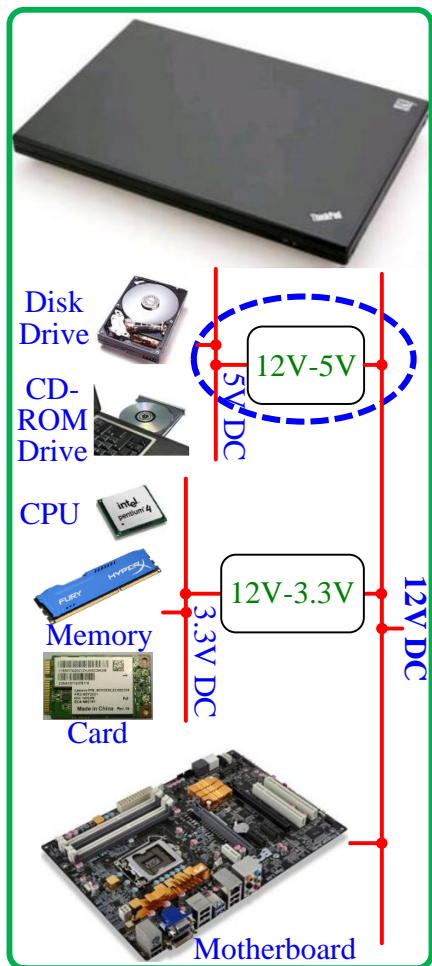


How to design a 12 V – 5 V power supply?



$$\frac{5(k\Omega)}{5(k\Omega) + 7(k\Omega)} \cdot 12V = 5V$$

How to design a 12 V – 5 V power supply?



Grid voltage
to 12V

12VDC



Grid

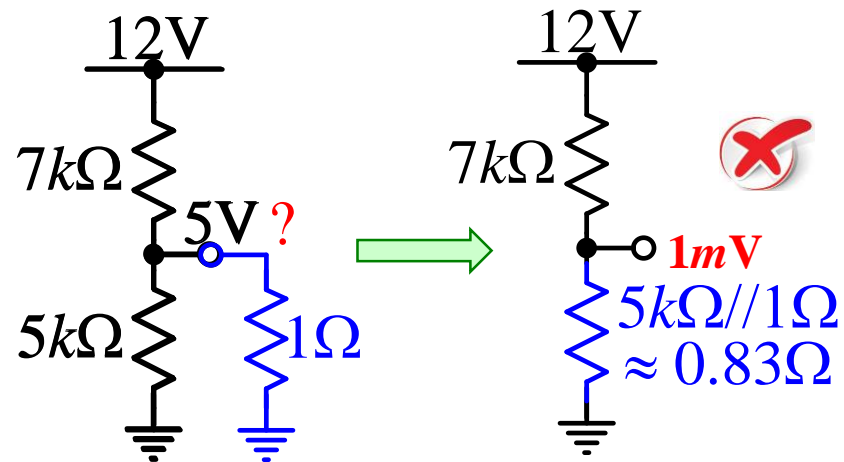


How to get 5V from
12V?

12V
DC

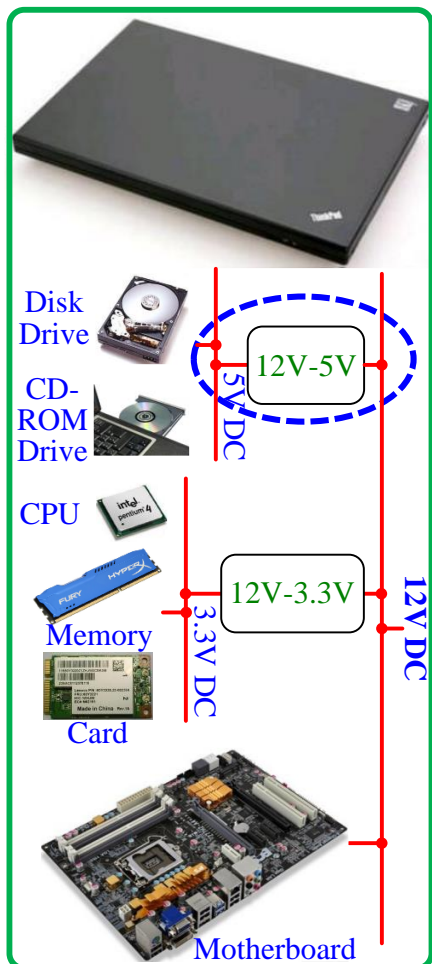
**DC-DC
Conversion**

5V
DC



$$\frac{0.83(k\Omega)}{0.83(k\Omega) + 7(k\Omega)} \cdot 12V = 1mV$$

How to design a 12 V – 5 V power supply?



Grid voltage
to 12V

Grid

12VDC

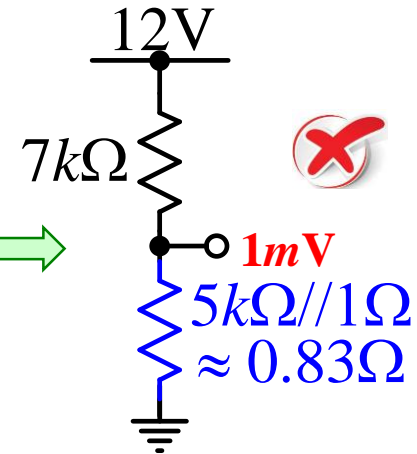
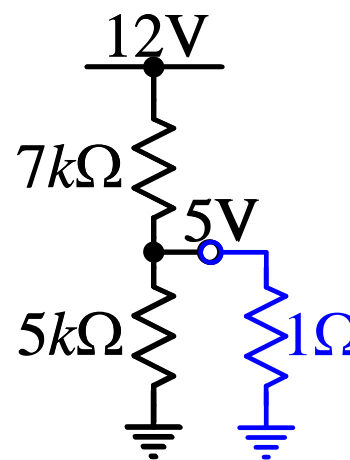


How to get constant 5V
from 12V with load?

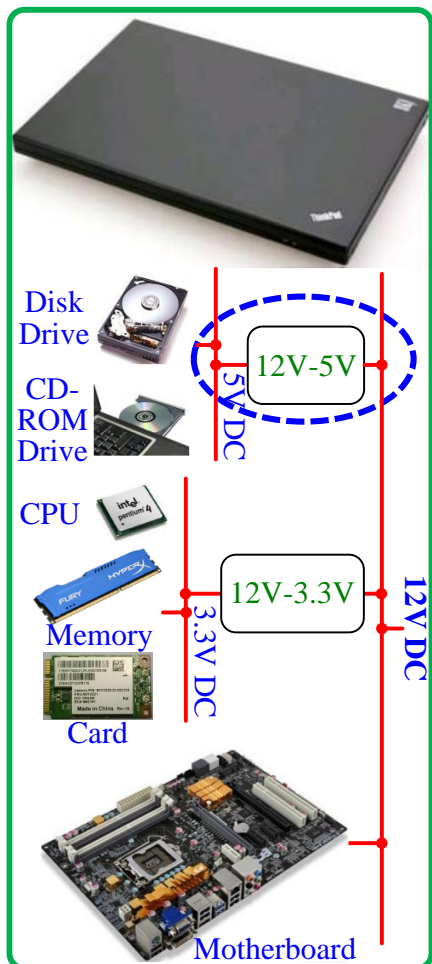
12V
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**DC-DC
Conversion**

5V
DC



How to design a 12 V – 5 V power supply?



Grid voltage
to 12V

Grid

12VDC

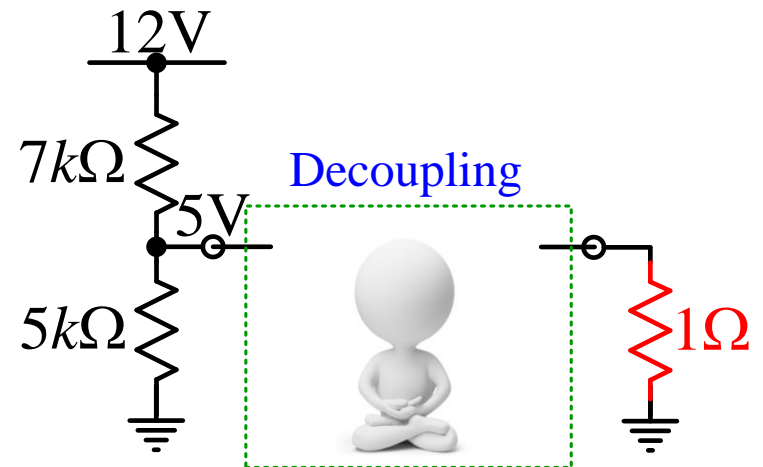


How to get constant 5V
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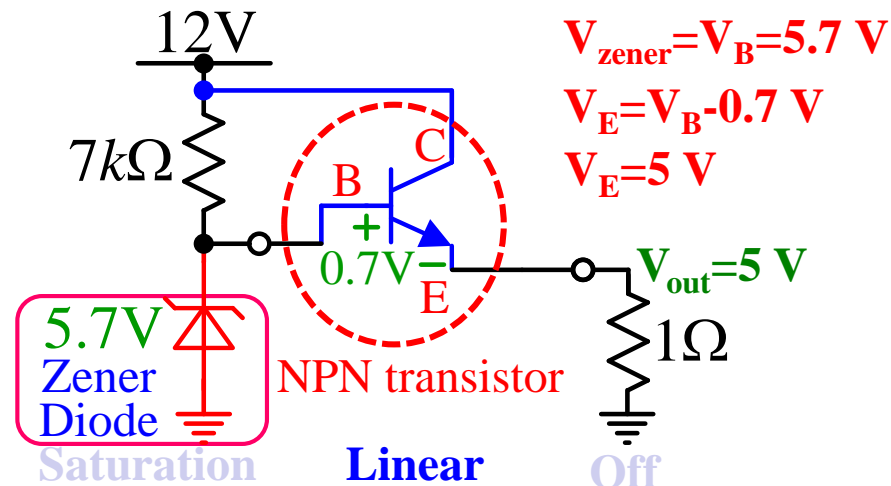
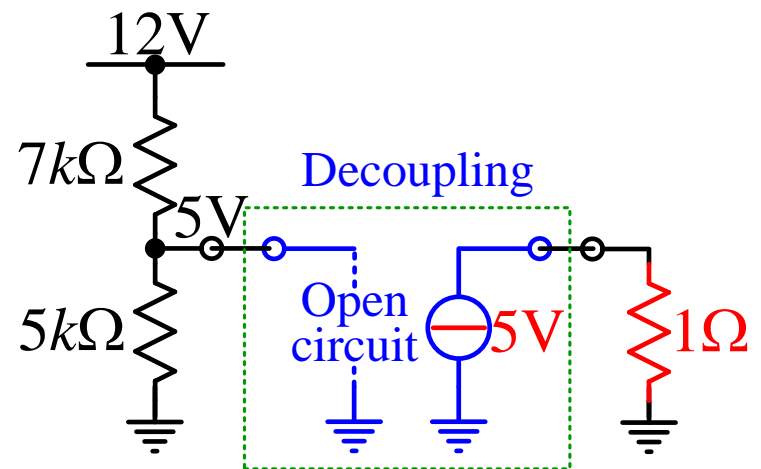
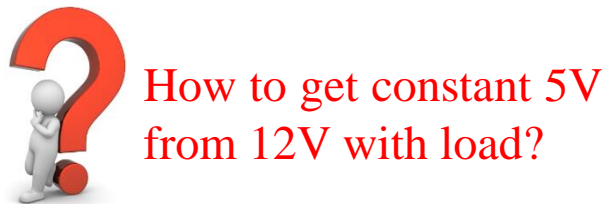
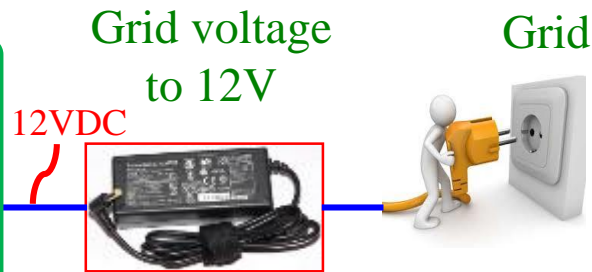
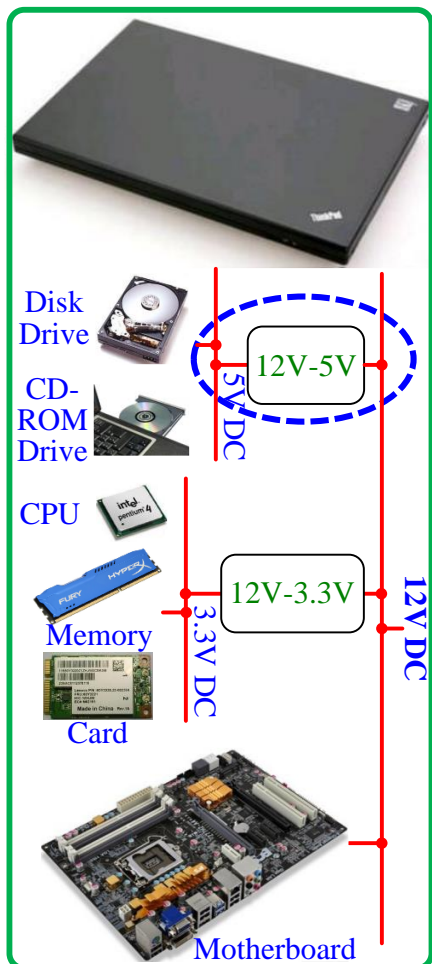
**DC-DC
Conversion**

5V
DC



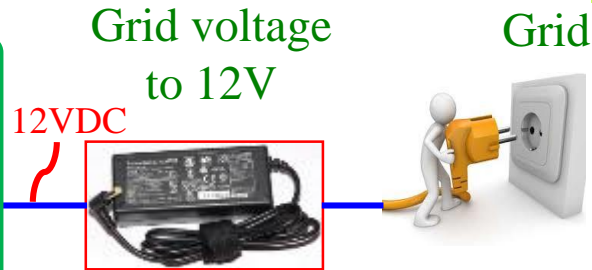
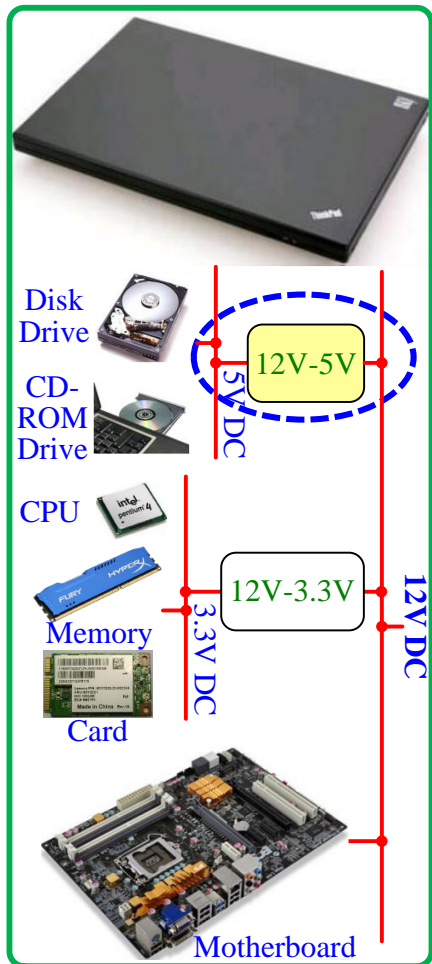


How to design a 12 V – 5 V power supply?

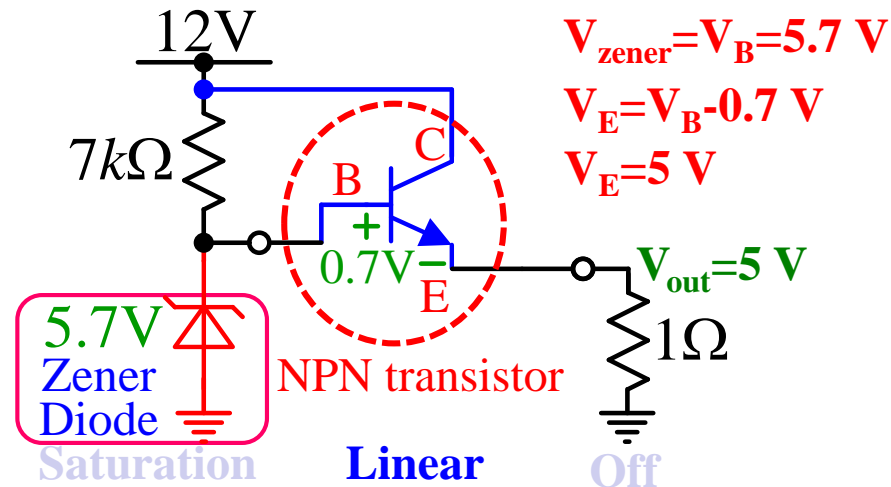
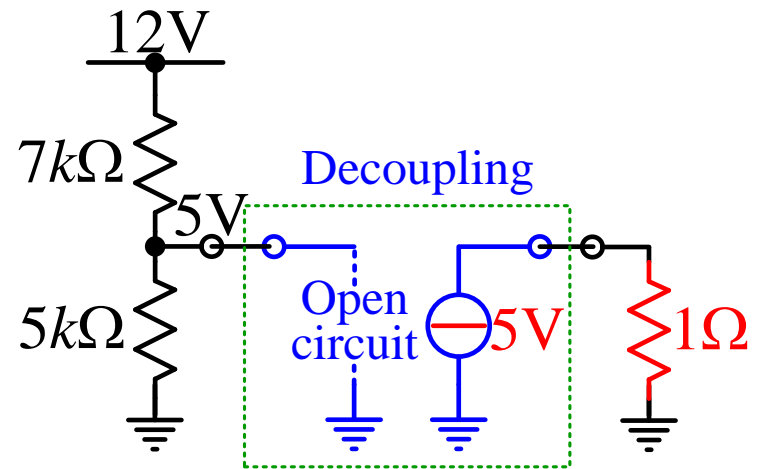


Traditional step down DC/DC power supply

Traditional step down DC/DC power supply

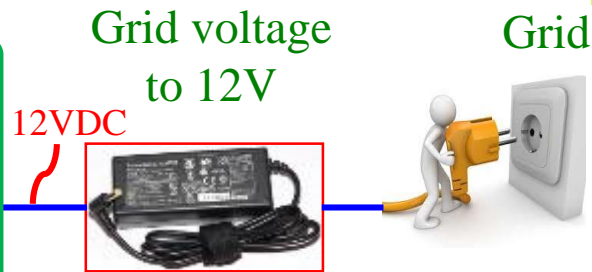
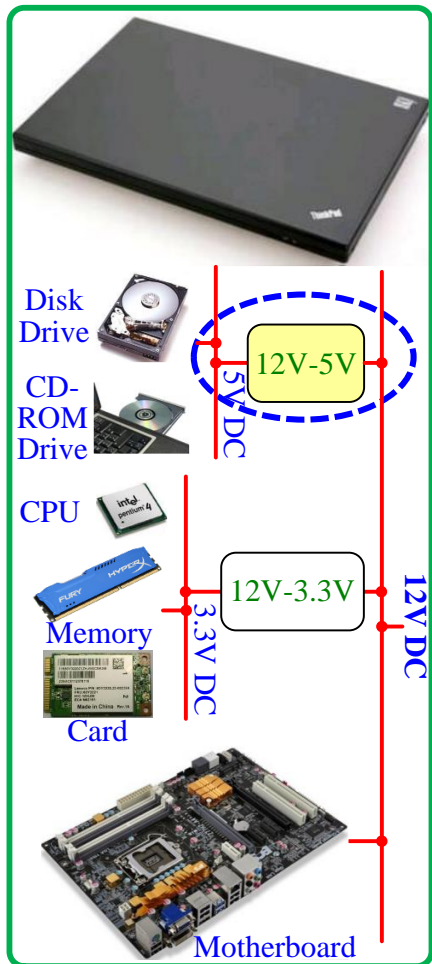


How to get constant 5V from 12V with load?

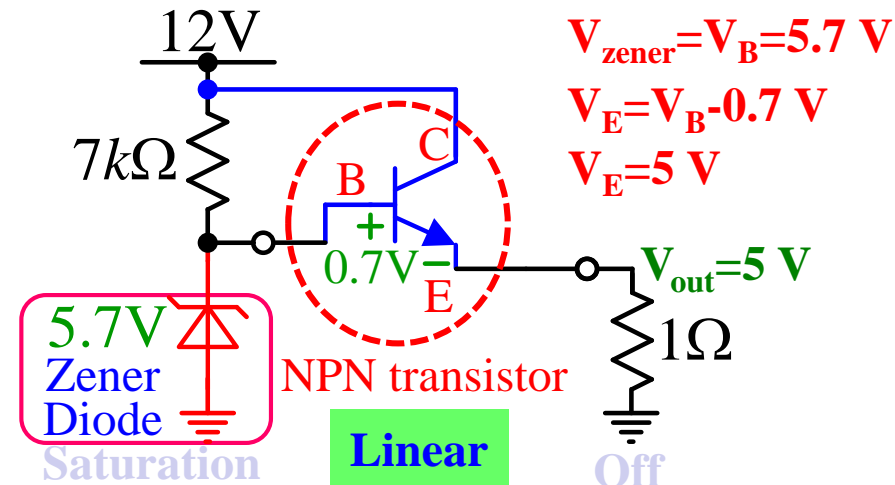
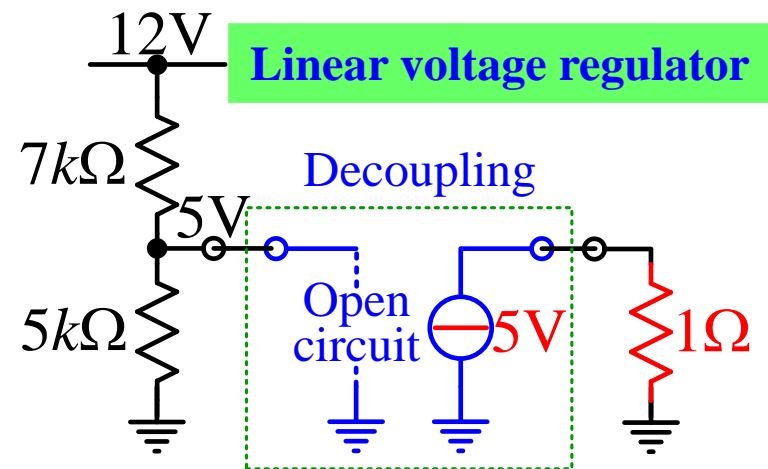


Traditional step down DC/DC power supply: Linear voltage regulator

Traditional step down DC/DC power supply

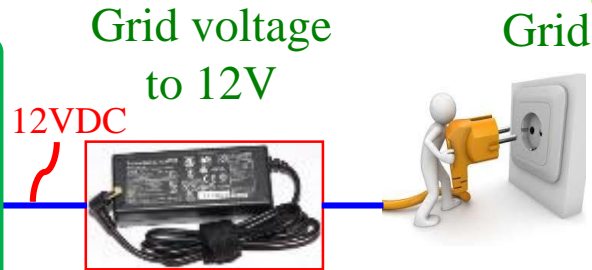
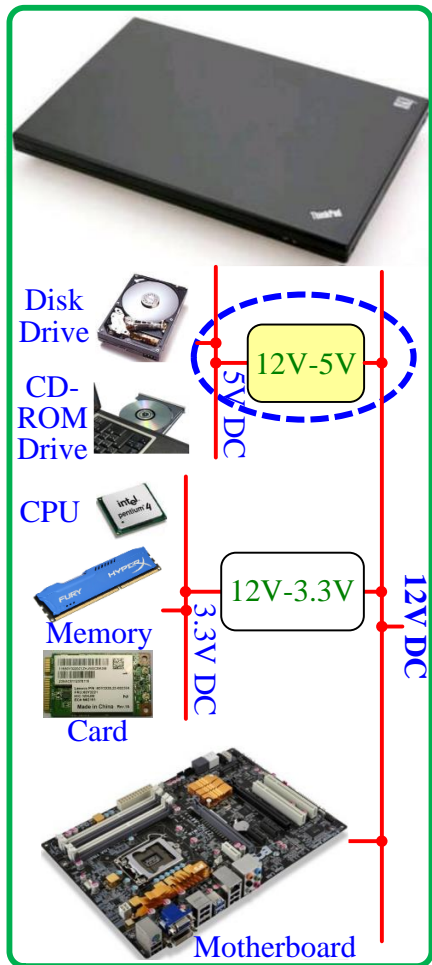


How to get constant 5V from 12V with load?

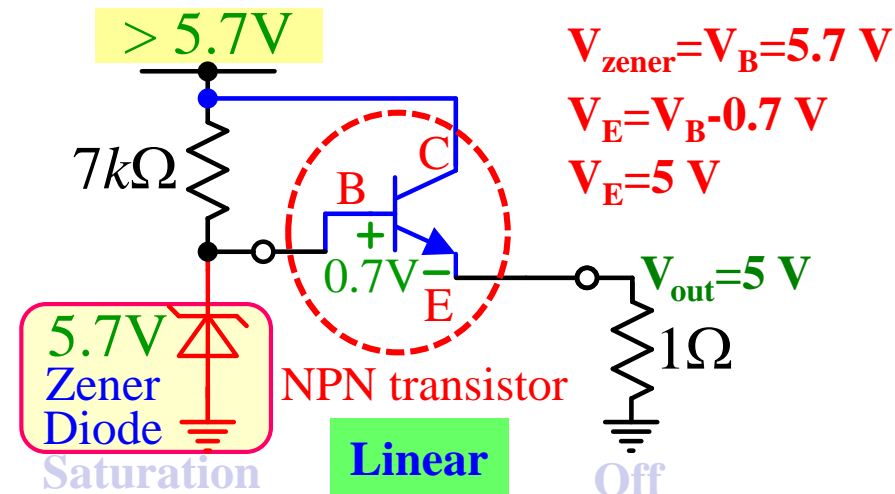
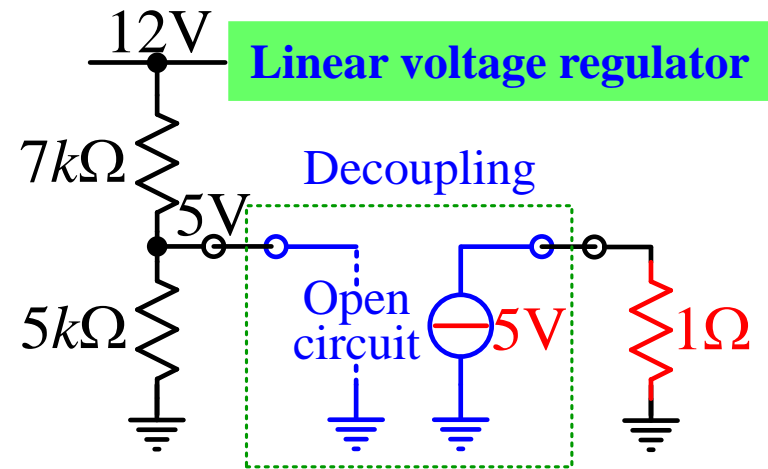
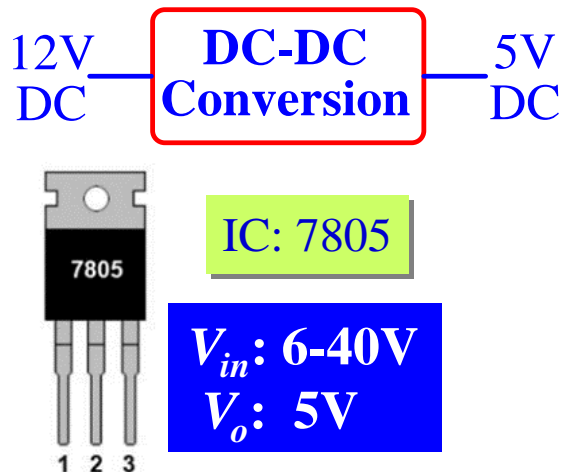


The first linear voltage regulator IC: 7805

Traditional step down DC/DC power supply

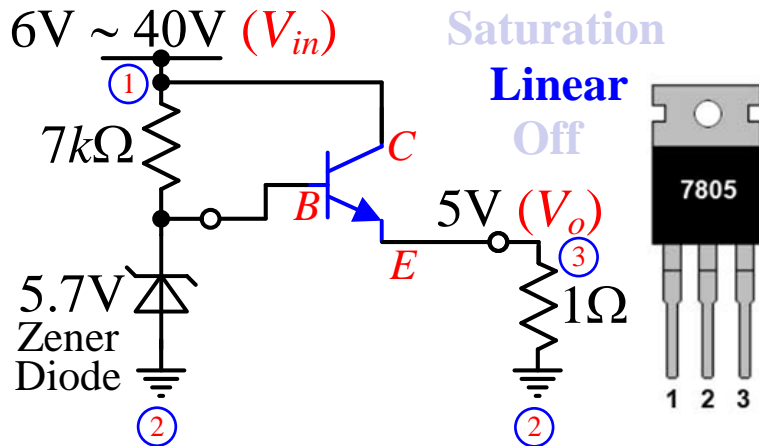


How to get constant 5V from 12V with load?



Advantages of the linear voltage regulator: Take 7805 as an example

Linear voltage regulator 7805



Advantages of Linear voltage regulator 7805

1. Cheap price < \$ 0.1;
2. Simple circuit;
3. Wide input voltage: 6V ~ 40V DC.

No need to control it

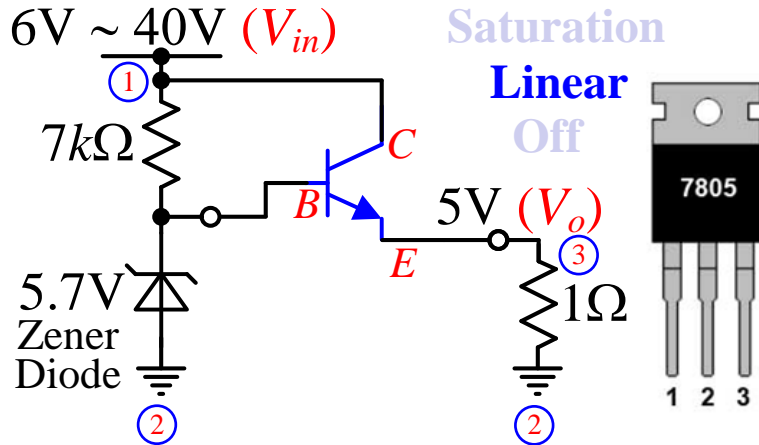
Are there any disadvantages for linear voltage regulator?





Disadvantages of the Linear Voltage Regulator: Efficiency Problem

Linear voltage regulator 7805



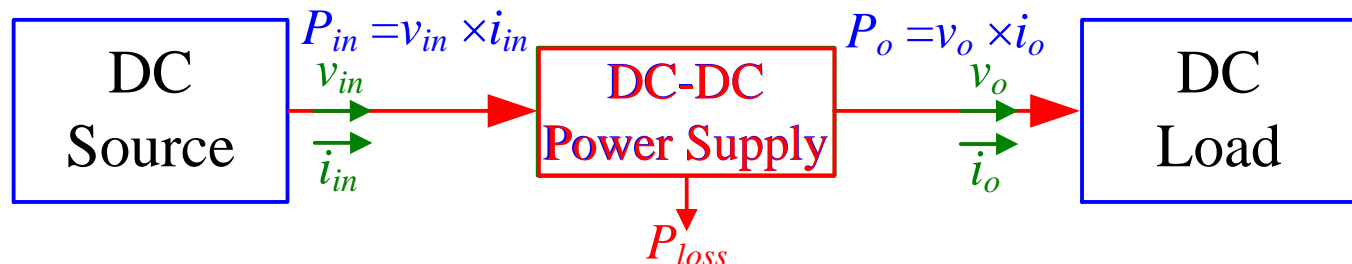
Advantages of Linear voltage regulator 7805

1. Cheap price < \$ 0.1;
2. Simple circuit;
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Efficiency

$$\eta = \frac{p_o}{p_{in}} = \frac{v_o \cdot i_o}{v_{in} \cdot i_{in}}$$

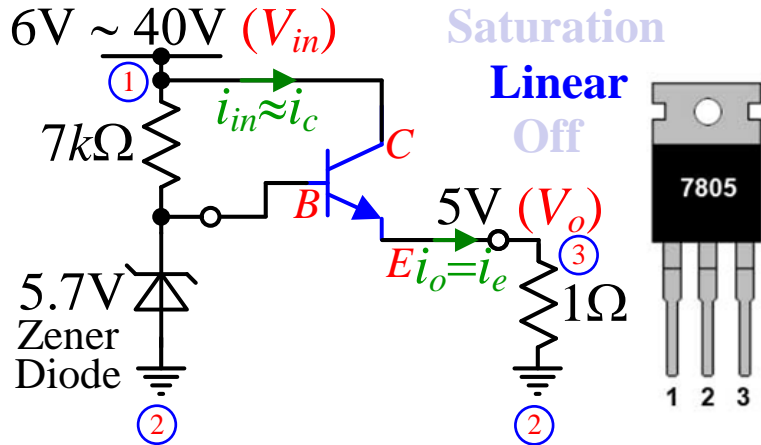
Efficiency
Problem:





Disadvantages of the Linear Voltage Regulator: Efficiency Problem

Linear voltage regulator 7805



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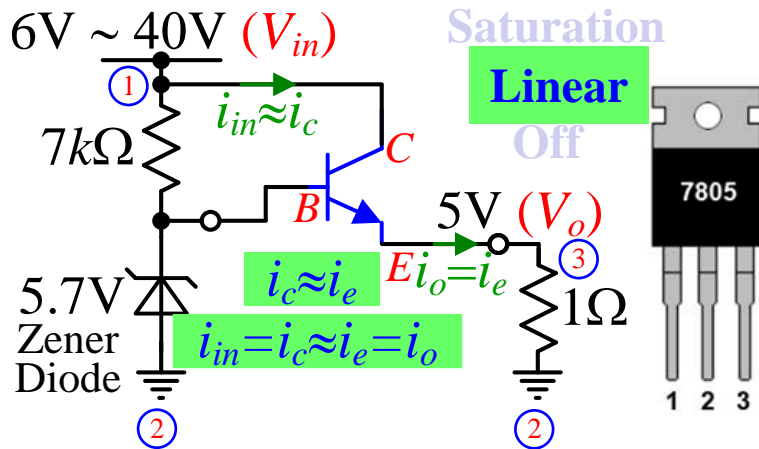
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Disadvantages of the Linear Voltage Regulator: Efficiency Problem

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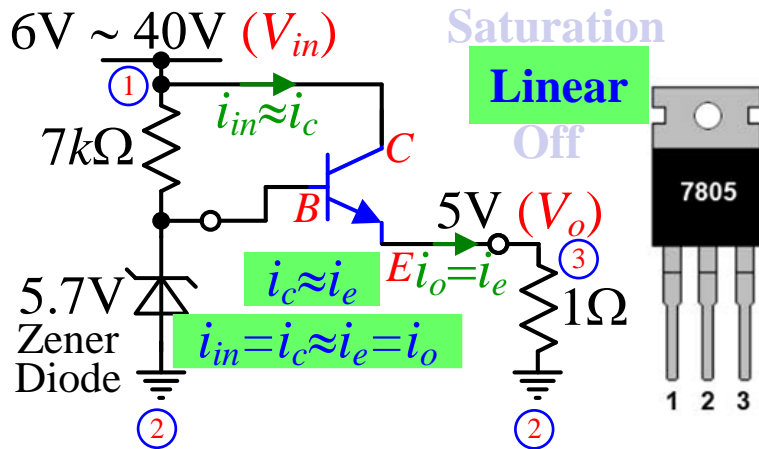
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Disadvantages of the Linear Voltage Regulator: Efficiency Problem

Linear voltage regulator 7805



Efficiency

$$\eta = \frac{p_o}{p_{in}} = \frac{v_o \cdot i_o}{v_{in} \cdot i_{in}} = \frac{v_o \cdot \cancel{i_e}}{v_{in} \cdot \cancel{i_c}} \approx \frac{v_o}{v_{in}}$$

1: If $V_{in} = 6 \text{ V}$:

$$\eta \approx (5 \text{ V}) / (6 \text{ V}) = 83.3\%$$

2: If $V_{in} = 40 \text{ V}$:

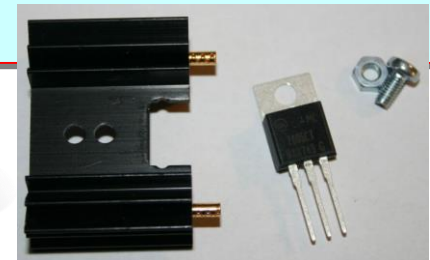
$$\eta \approx (5 \text{ V}) / (40 \text{ V}) = 12.5\%$$

Advantages of Linear voltage regulator 7805

1. Cheap price < \$ 0.1;
2. Simple circuit;
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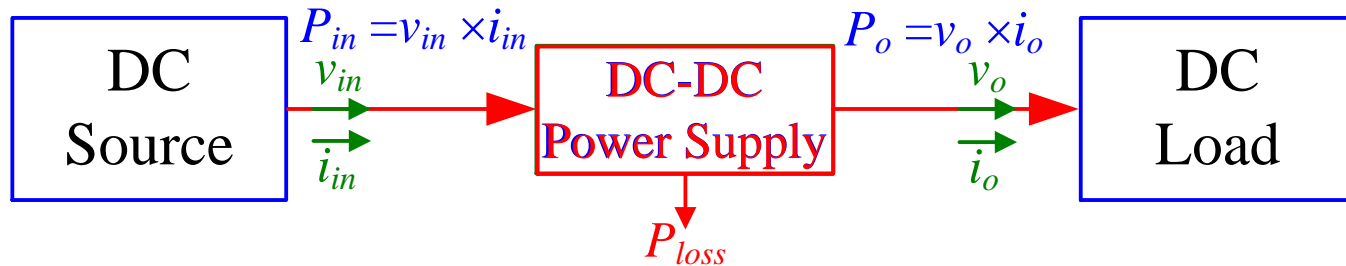
Disadvantages of Linear voltage regulator

1. Efficiency is low if the input voltage is high;
 - 83% @ 6V - 5V;
 - 12.5% @ 40V - 5V;
2. Large power loss → Need large heatsink → Large size.



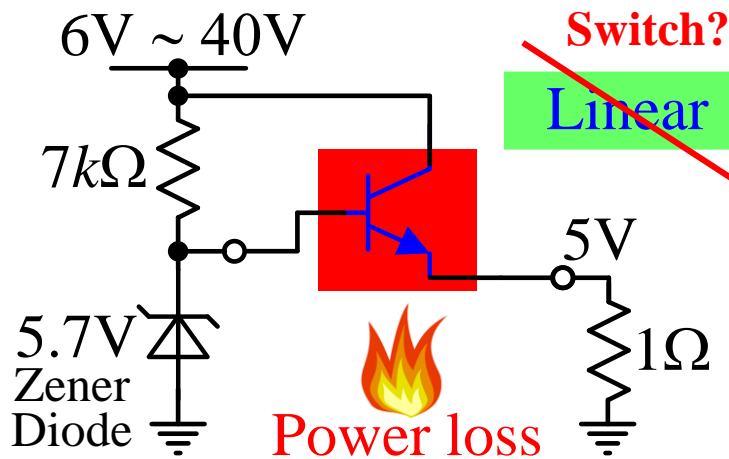
Improved step-down DC/DC power supplies: Switch-Mode Power Supplies (Buck converter)

Efficiency
Problem:



$$p_{in} = p_o + p_{loss} \Rightarrow \eta = \frac{p_o}{p_{in}} = \frac{p_o}{p_o + p_{loss}} \Rightarrow$$

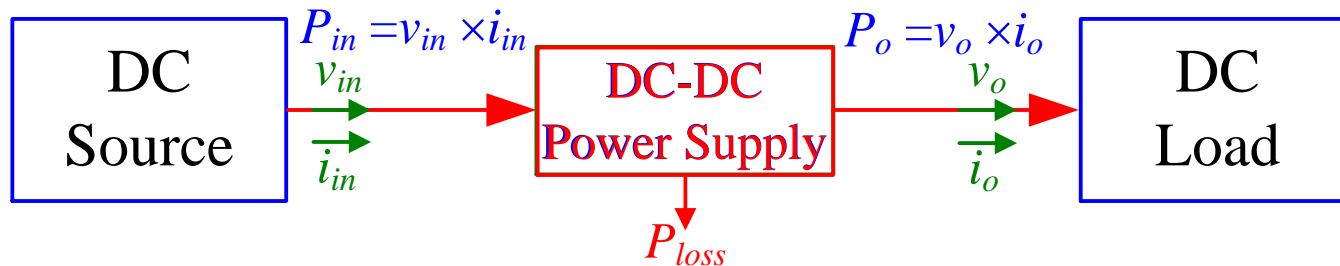
High efficiency requires
low power loss





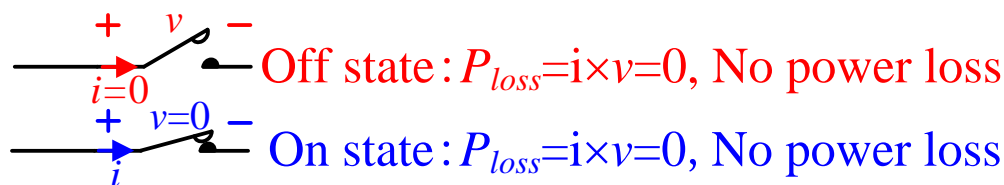
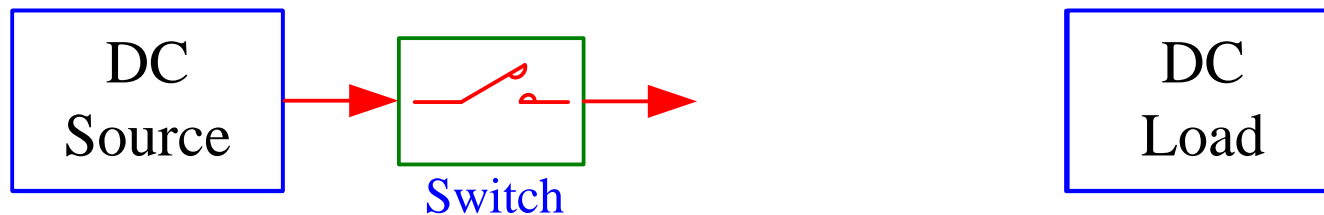
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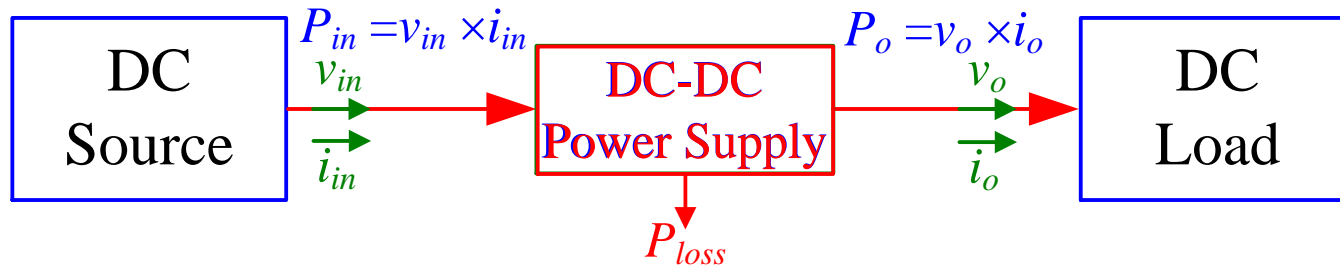
High efficiency requires
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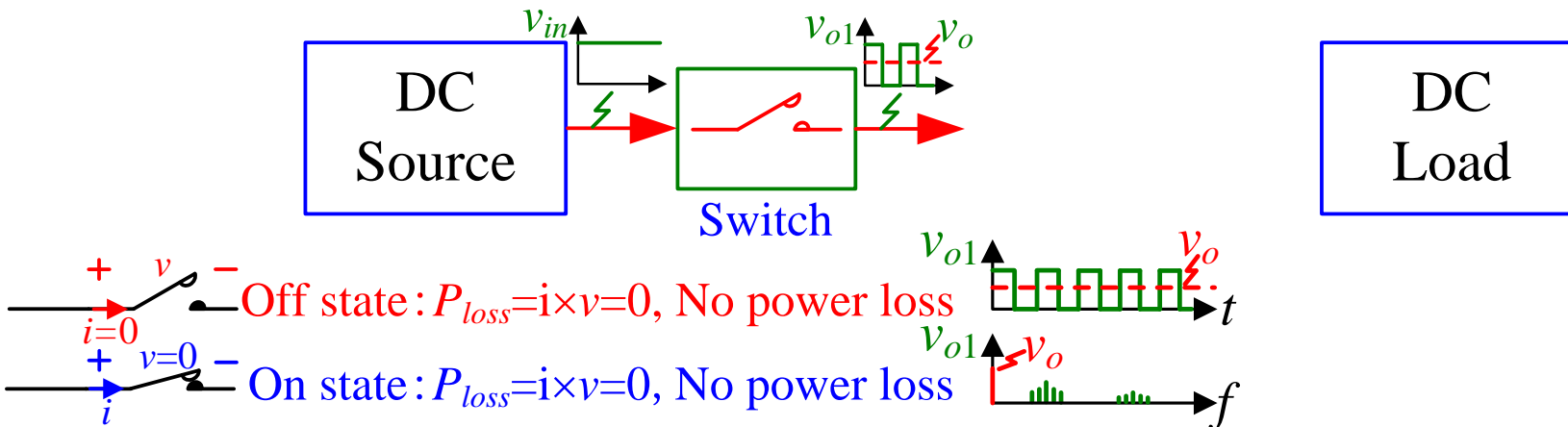
Improved step-down DC/DC power supplies: Switch-Mode Power Supplies (Buck converter)

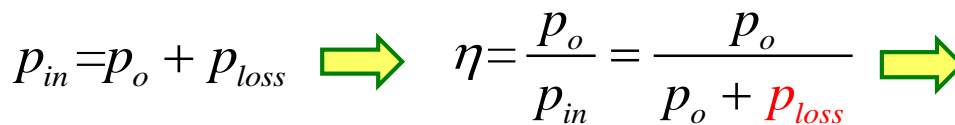
Efficiency
Problem:



$$p_{in} = p_o + p_{loss} \Rightarrow \eta = \frac{p_o}{p_{in}} = \frac{p_o}{p_o + p_{loss}} \Rightarrow$$

High efficiency requires
low power loss





The diagram illustrates a DC-DC converter system. A DC Source is connected to a Switch, which is in turn connected to a DC Load. The input voltage V_{in} is applied to the switch. The switch voltage v and current i are shown. The output voltage v_{O1} is shown as a square wave. The output voltage v_O is shown as a filtered version of v_{O1} . The diagram also shows the switching harmonic spectrum and the filtered output voltage v_{O1} with no harmonics.

Off state: $P_{loss} = i \times v = 0$, No power loss

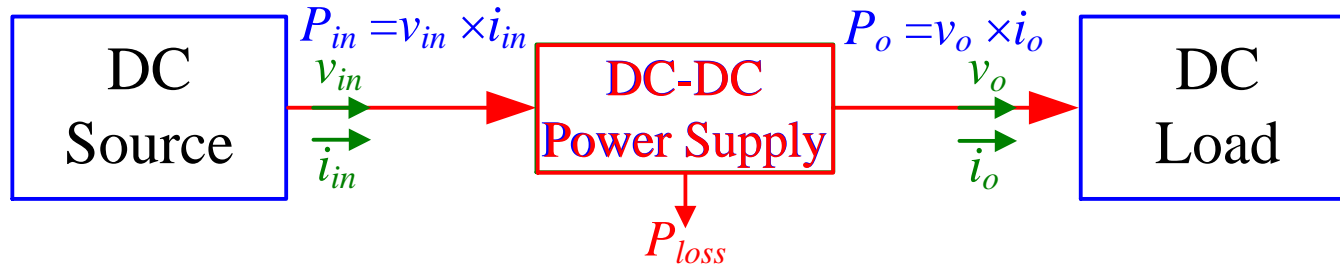
On state: $P_{loss} = i \times v = 0$, No power loss

Switching harmonic

No Harmonic

Improved step-down DC/DC power supplies: Switch-Mode Power Supplies (Buck converter)

Efficiency
Problem:



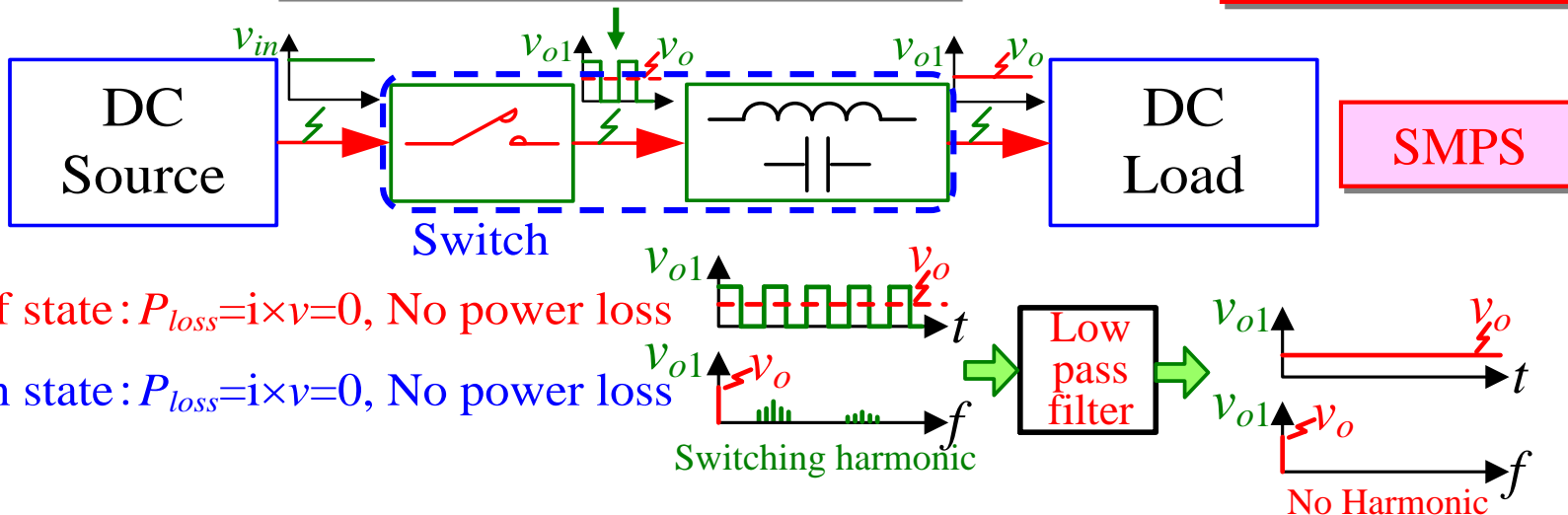
$$p_{in} = p_o + p_{loss} \Rightarrow \eta = \frac{p_o}{p_{in}} = \frac{p_o}{p_o + p_{loss}} \Rightarrow$$

High efficiency requires
low power loss

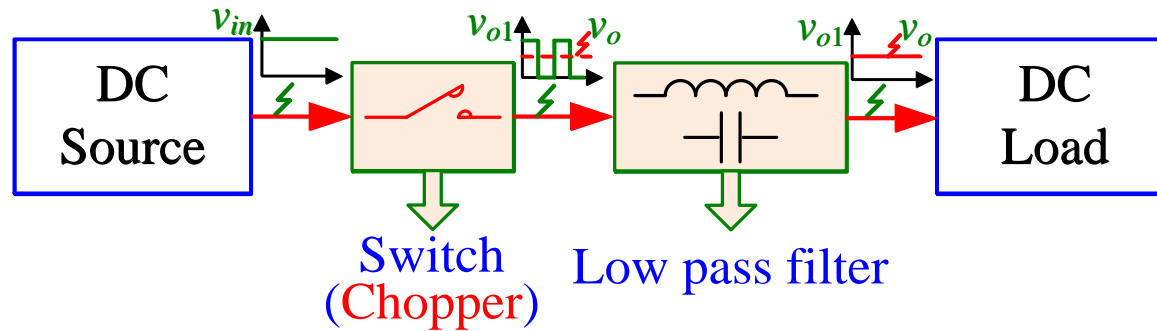
Efficiency

We can control the on/off time

Buck converter

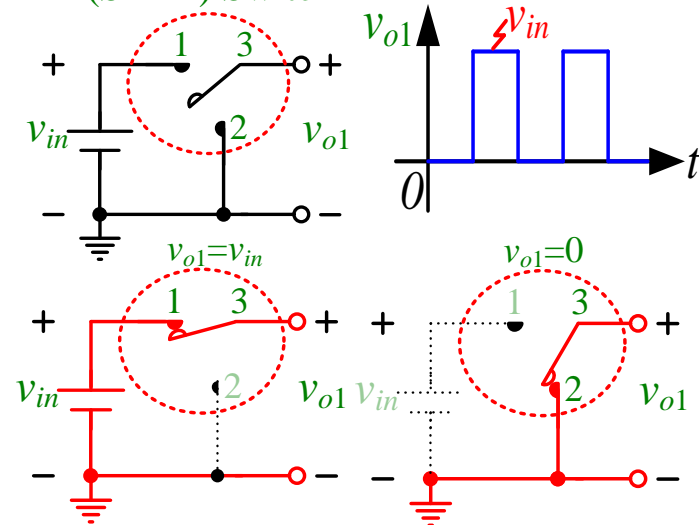


Derive the practical circuit of Buck Converter

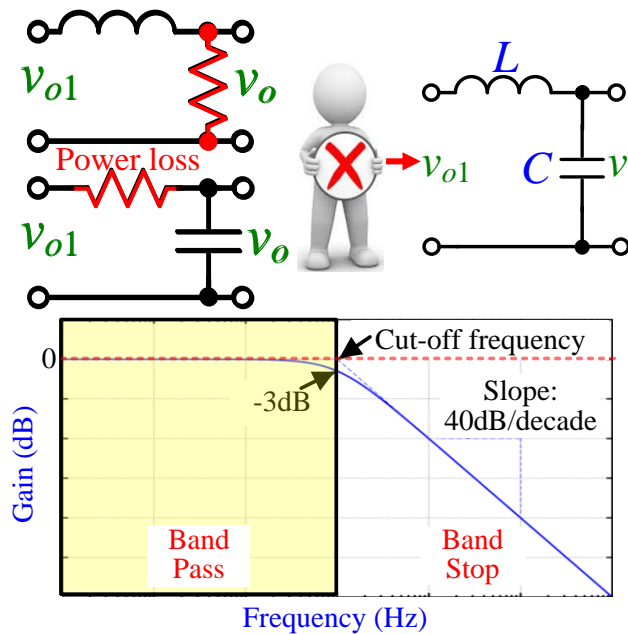


1. Chopper

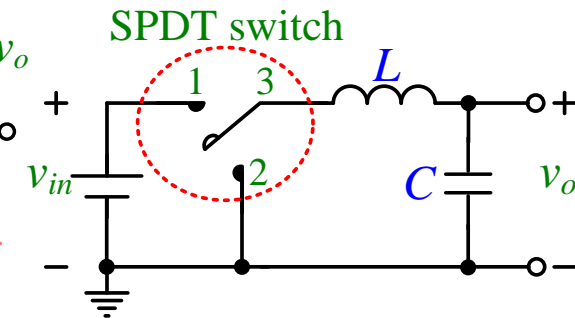
Single-pole-double-throw (SPDT) Switch



2. Low pass filter

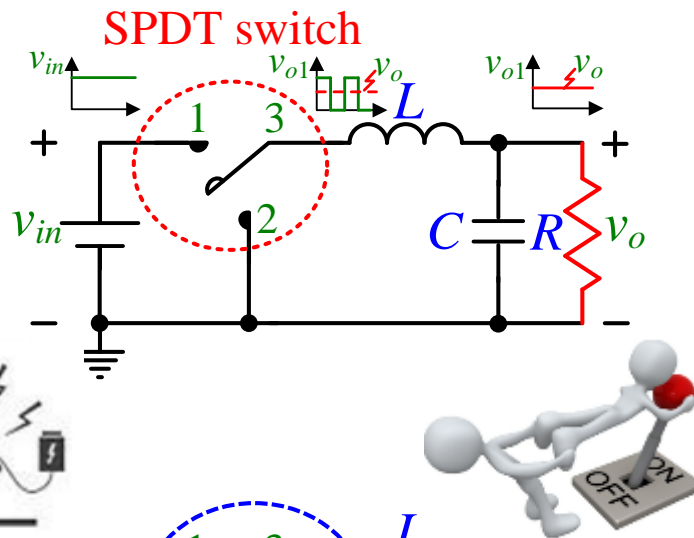


Buck Converter Version 1.0

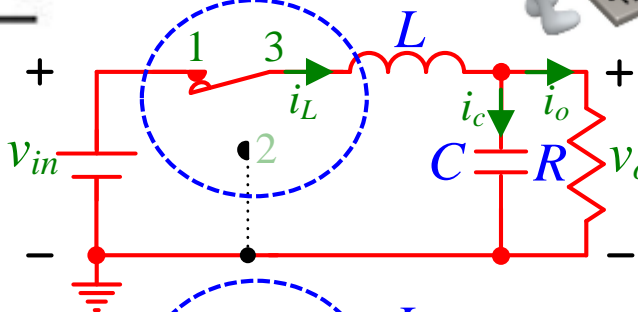


Derive the practical circuit of Buck Converter

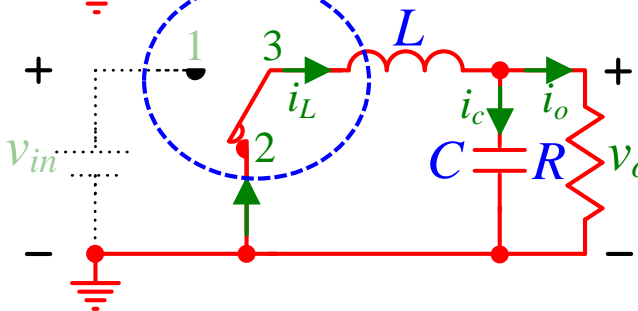
Buck Converter Version 1.0



Mode 1:

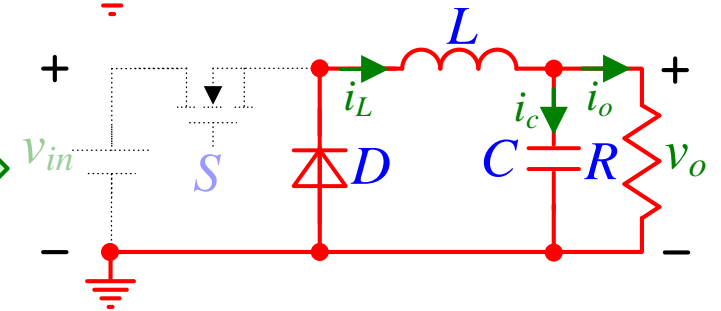
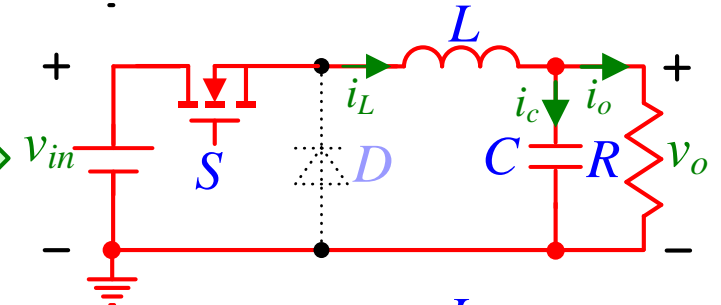
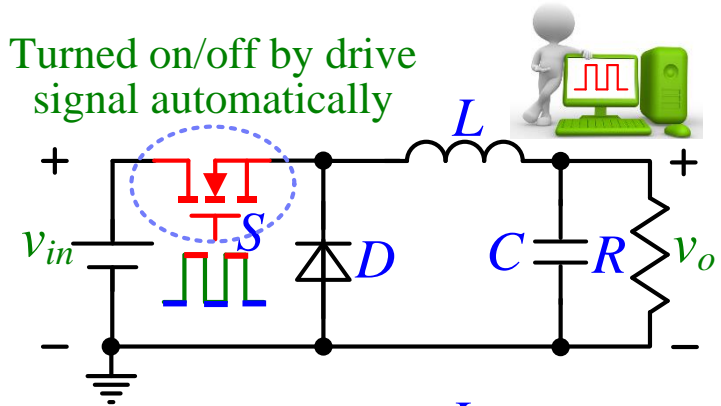


Mode 2:



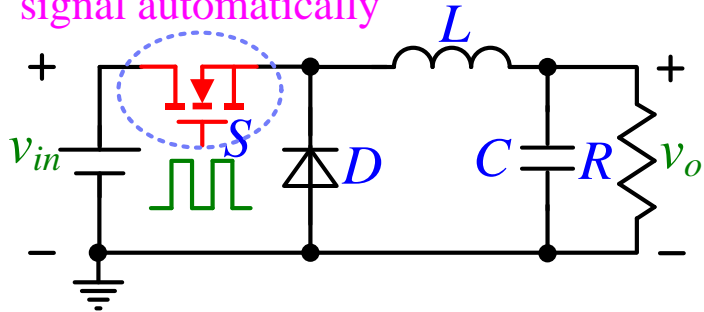
Final version of Buck converter

Turned on/off by drive
signal automatically



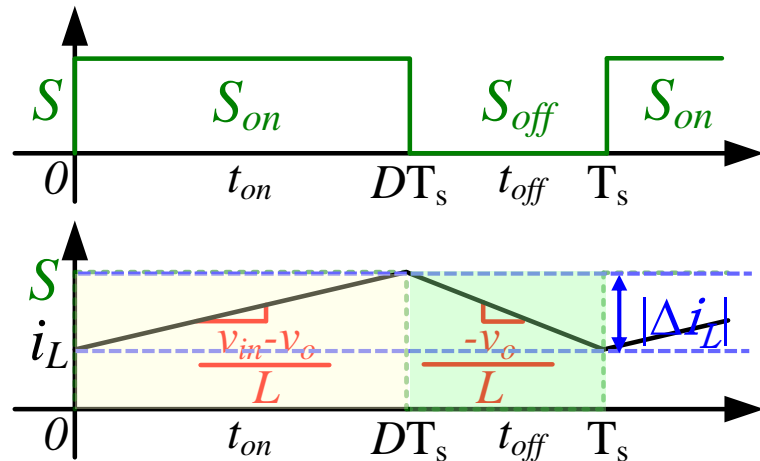
Relationship between v_o and v_{in} : Voltage Transfer Ratio of Buck Converter

Turned on/off by drive signal automatically



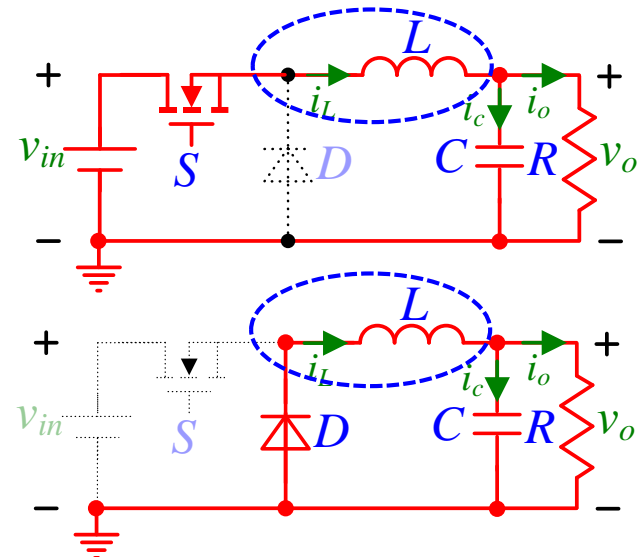
T_s : Switching period t_{on} : Turn on time

$D = \frac{t_{on}}{t_{on} + t_{off}}$: Duty cycle t_{off} : Turn off time



Turn on S:
($t_{on} = DT_s$)

Turn off S:
($t_{off} = T_s - DT_s$)



Derive voltage transfer ratio according to $|\Delta i_L|$:

$$|\Delta i_L \uparrow| = \left(\frac{v_{in} - v_o}{L} \right) \cdot DT_s \quad |\Delta i_L \downarrow| = \left(\frac{v_o}{L} \right) \cdot (1 - D) T_s$$

$$\downarrow |\Delta i_L| = |\Delta i_L \uparrow| = |\Delta i_L \downarrow|$$

$$v_o = v_{in} \cdot D$$

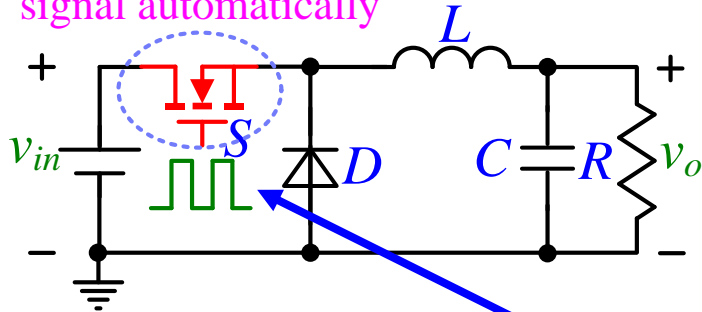
Derivation

Preconditions:

- Buck converter is in steady state
- i_L keeps continuous

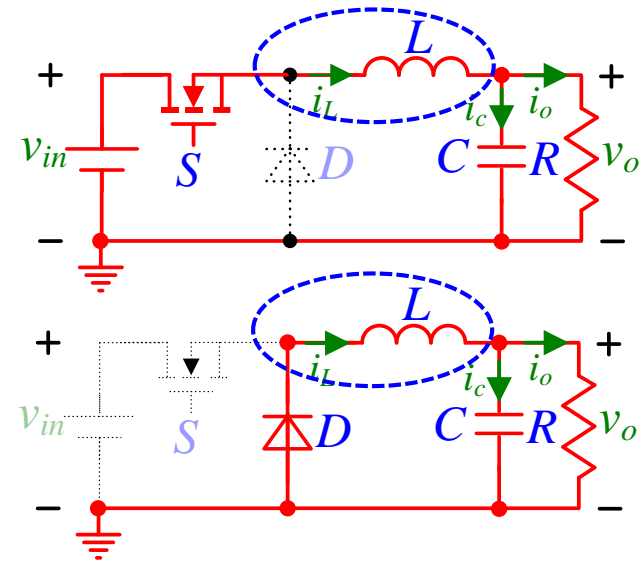
Operation principle of Buck Converter

Turned on/off by drive signal automatically



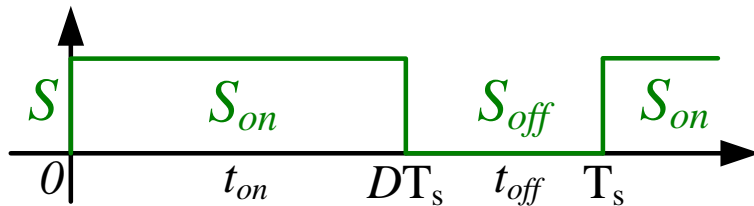
Turn on S:
($t_{on} = DT_s$)

Turn off S:
($t_{off} = T_s - DT_s$)



T_s : Switching period t_{on} : Turn on time

$D = \frac{t_{on}}{t_{on} + t_{off}}$: Duty cycle t_{off} : Turn off time



We can
control the
on/off time

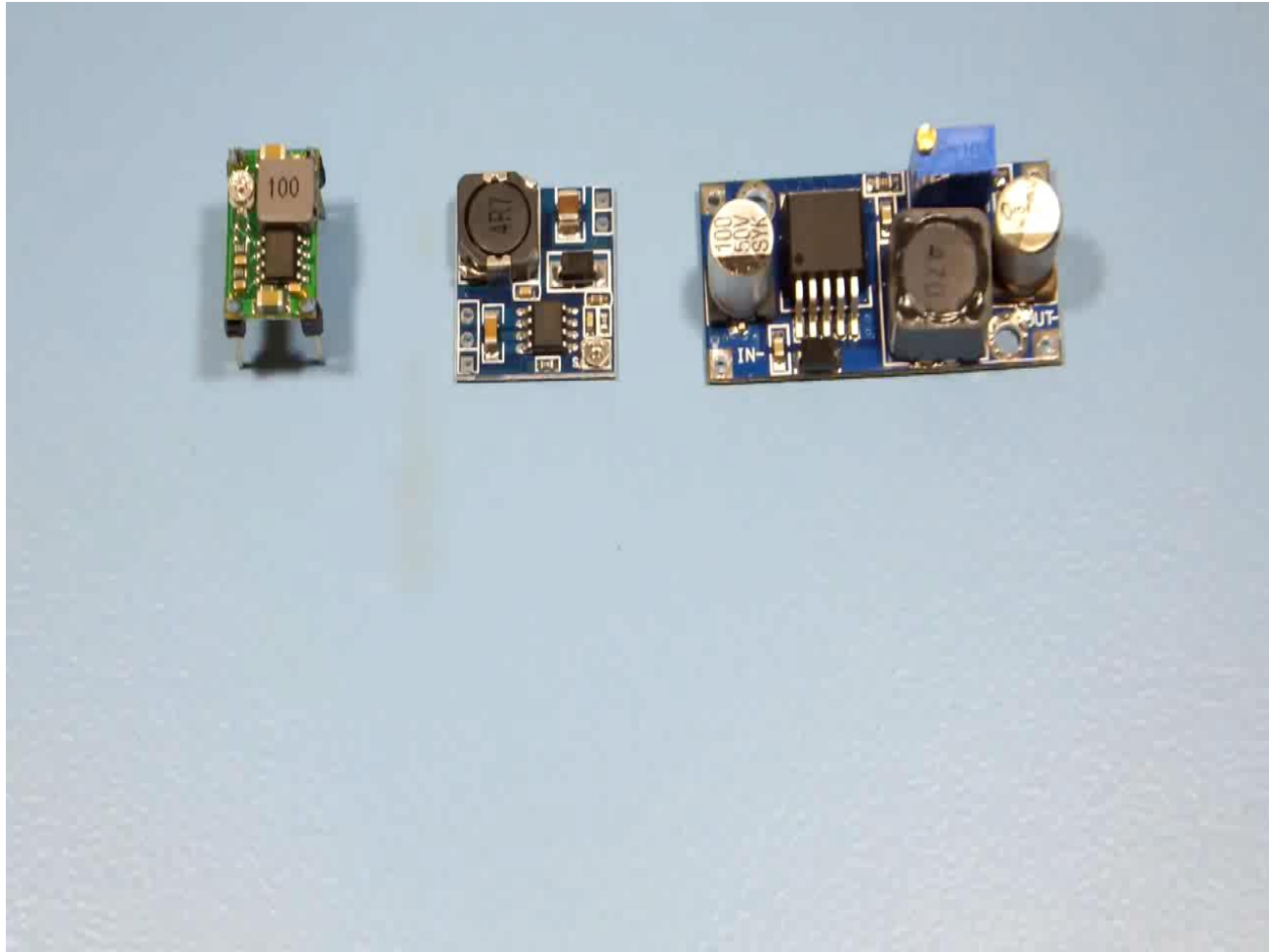
If input voltage is 12 V,

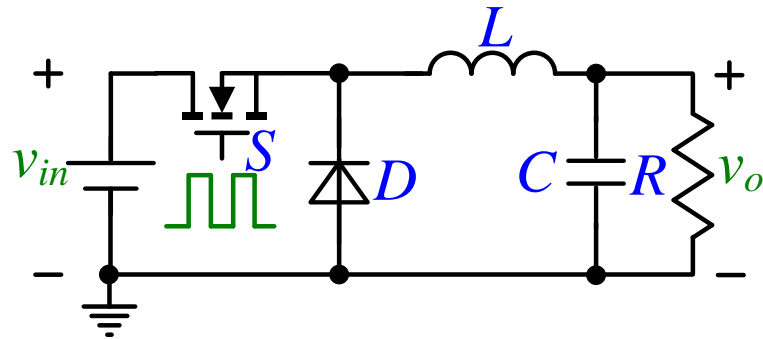
$$v_o = v_{in} \cdot D$$

If output voltage is required to 5 V,

$$D = \frac{5}{12}$$

Comparison between Buck converter and linear voltage regulator





1W = 0.1 \$



uxcell

uxcell New BIG-Size Voltage Converter Regulator
DC/DC DC **48V** Step-Down to DC **12V** 30A 360W **Buck**
Transformer Waterproof

★★★★★ 24 customer reviews | 13 answered questions

Price: **\$36.76** & **FREE** Shipping. [Details](#)

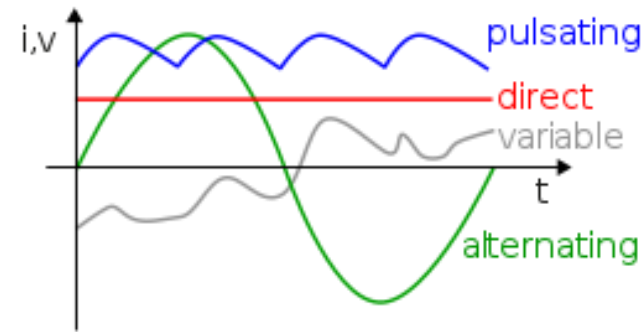
In Stock.

Sold by [uxcell](#) and [Fulfilled by Amazon](#). Gift-wrap available.

Size: 48V to 12V 30A 360W

OPERATION PRINCIPLE OF THE POWER CONVERTERS

[31/10/2019]



- Operation principle of the DC/DC converter
- Operation principle of the DC/AC inverter

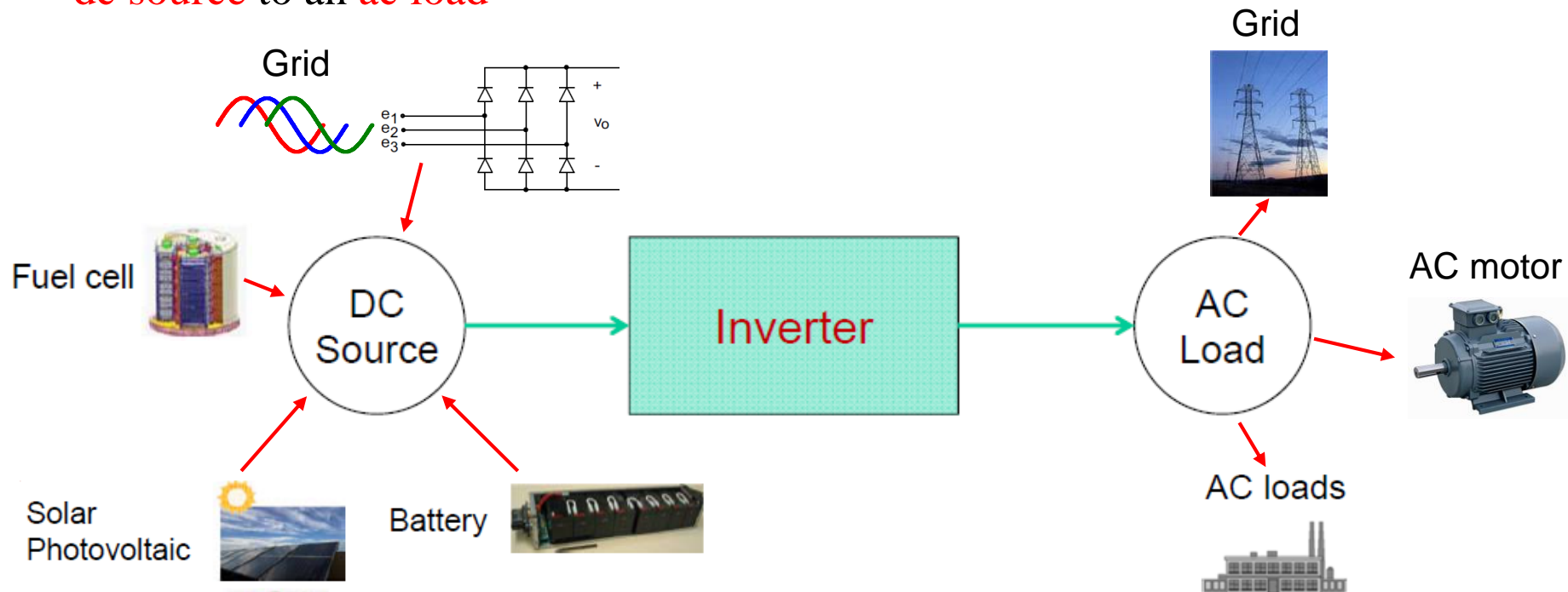
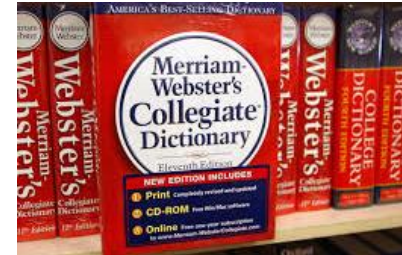


How does inverter work?



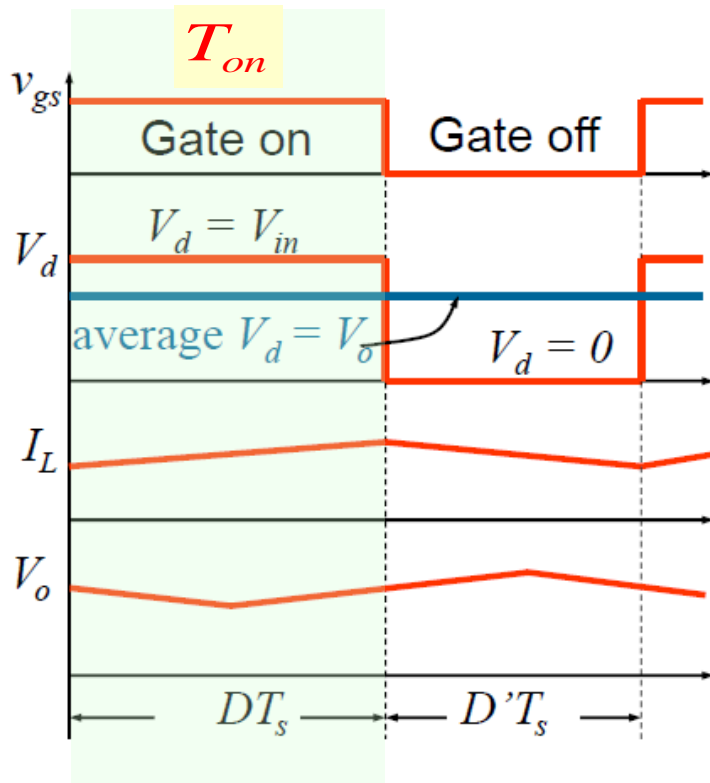
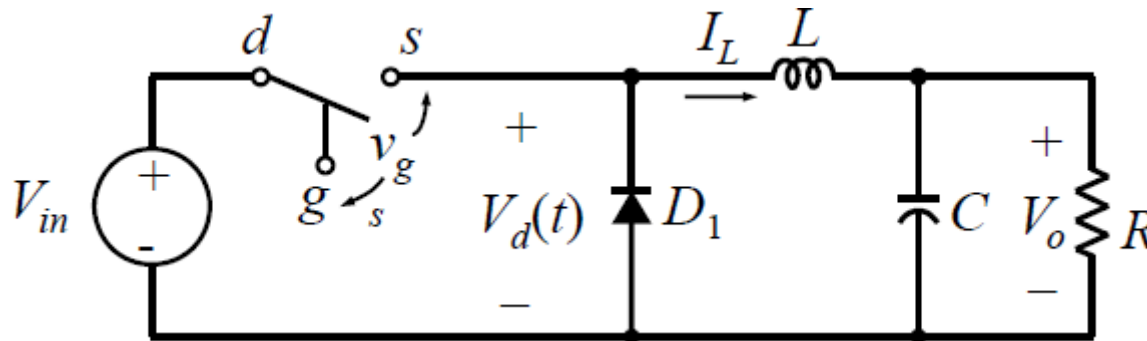
Inverter basics: Definition of the inverter

- A device used to convert direct current into alternating current – Merriam-Webster Dictionary.
- More precisely: inverter is to convert or transfer power from a dc source to an ac load



BEFORE STUDY THE OPERATION OF THE INVERTER,
LET US REVISIT THE DC TO DC CONVERTER.

DC-DC buck converter circuit



Average output voltage:

$$\bar{V}_o = D \cdot V_{in}$$

Where $D = T_{on}/T_s$ is the duty ratio.

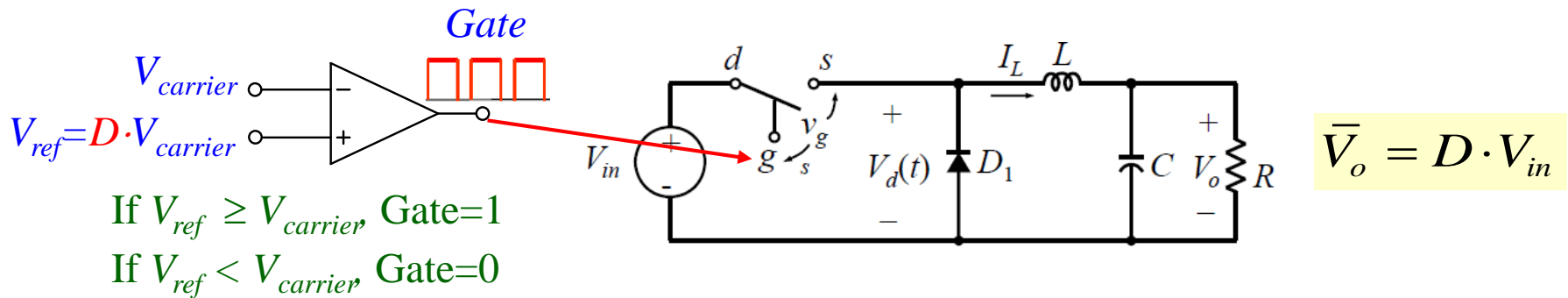
Because $D < 1$, V_o is always less than V_{in}

→ buck converting

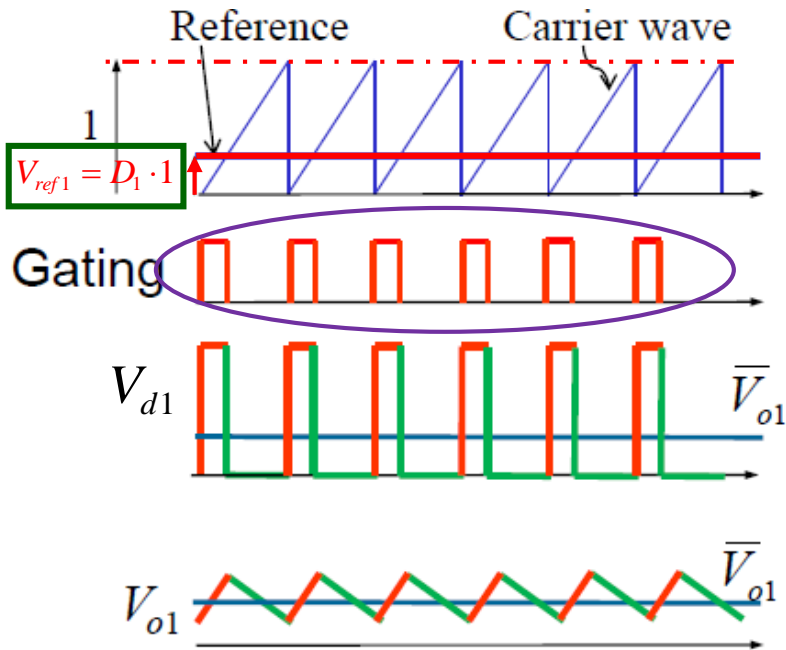
T_s : switching period (unit: s) = $1/f_s$

f_s : switching frequency (unit: Hz)

How the gate signal generated for the switcher?

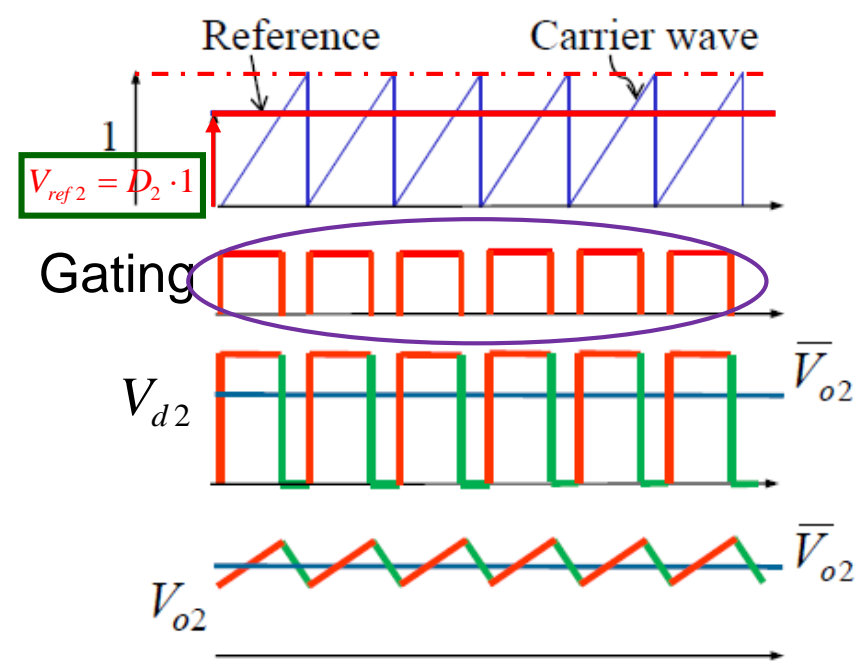


Get a **small** output voltage



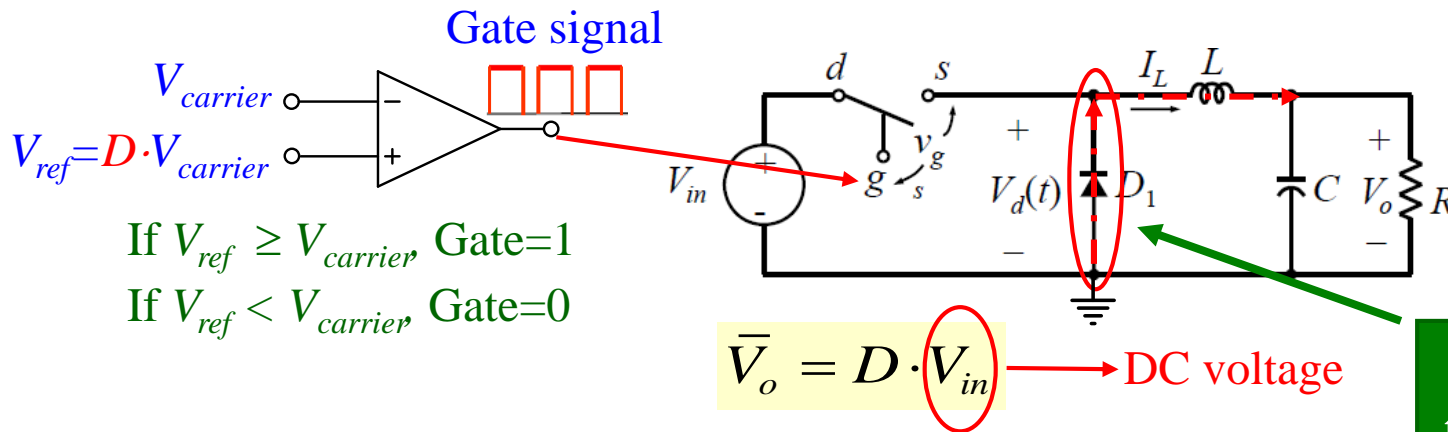
Average output voltage: $\bar{V}_{o1} = D_1 \cdot V_{in}$

Get a **large** output voltage

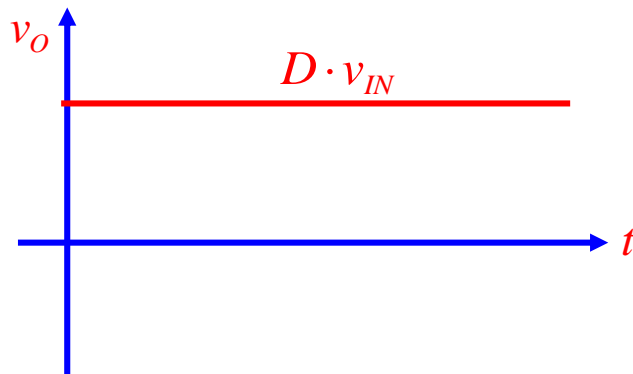


$\bar{V}_{o2} = D_2 \cdot V_{in}$

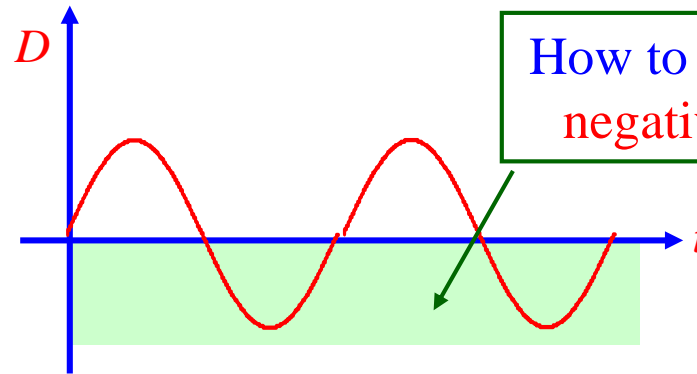
Question: Can we change the duty cycle to a sinusoidal AC waveforms ?



DC to DC converter



DC to AC converter?

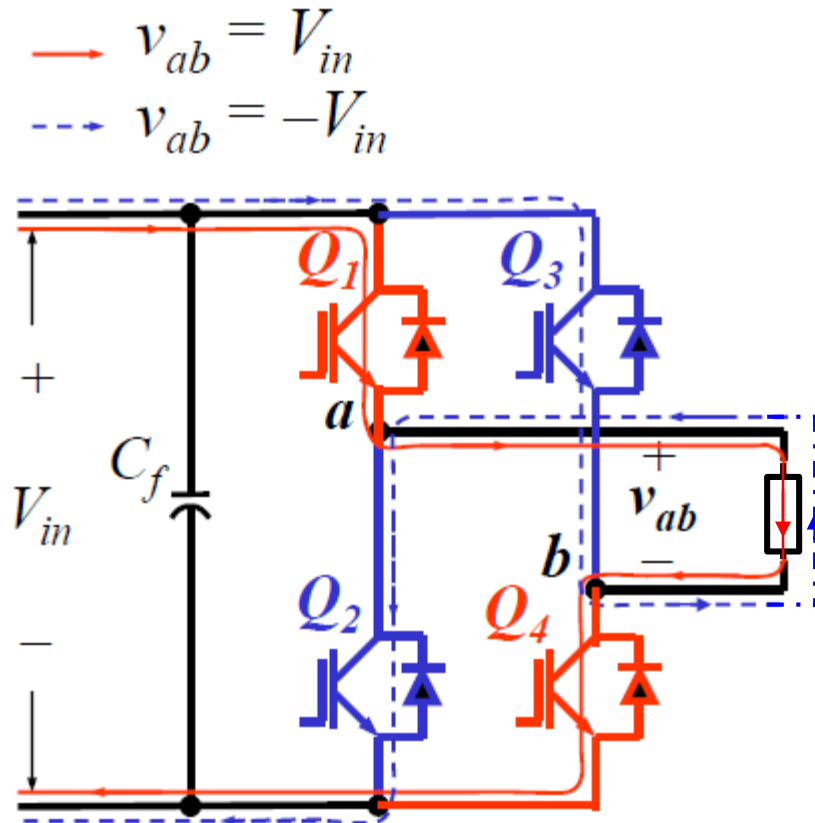


How to generate this negative voltage?

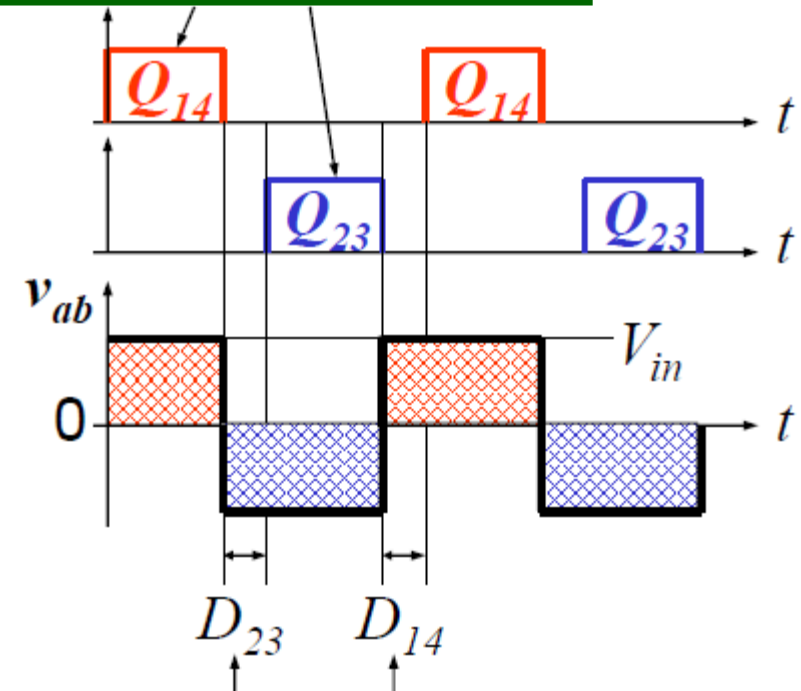
V_o cannot be smaller than 0 V due to the diode block

LET US COME BACK TO THE INVERTER

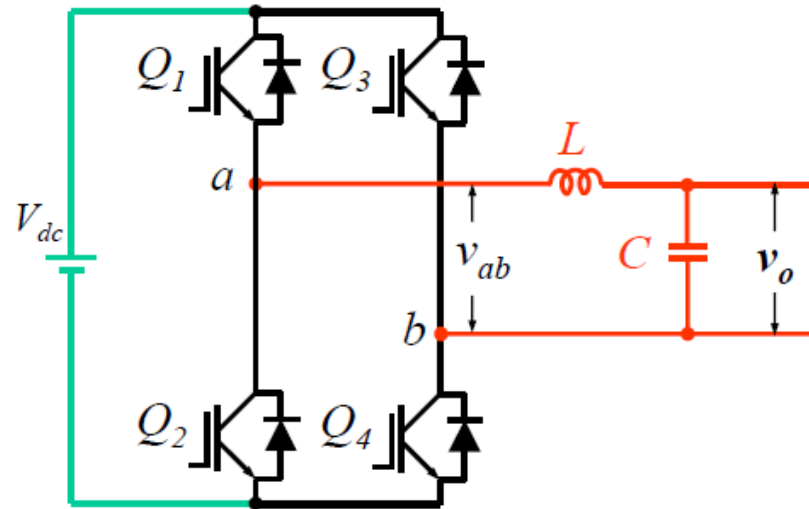
Full bridge circuit can generate both positive and negative voltages



Switches conduct alternately



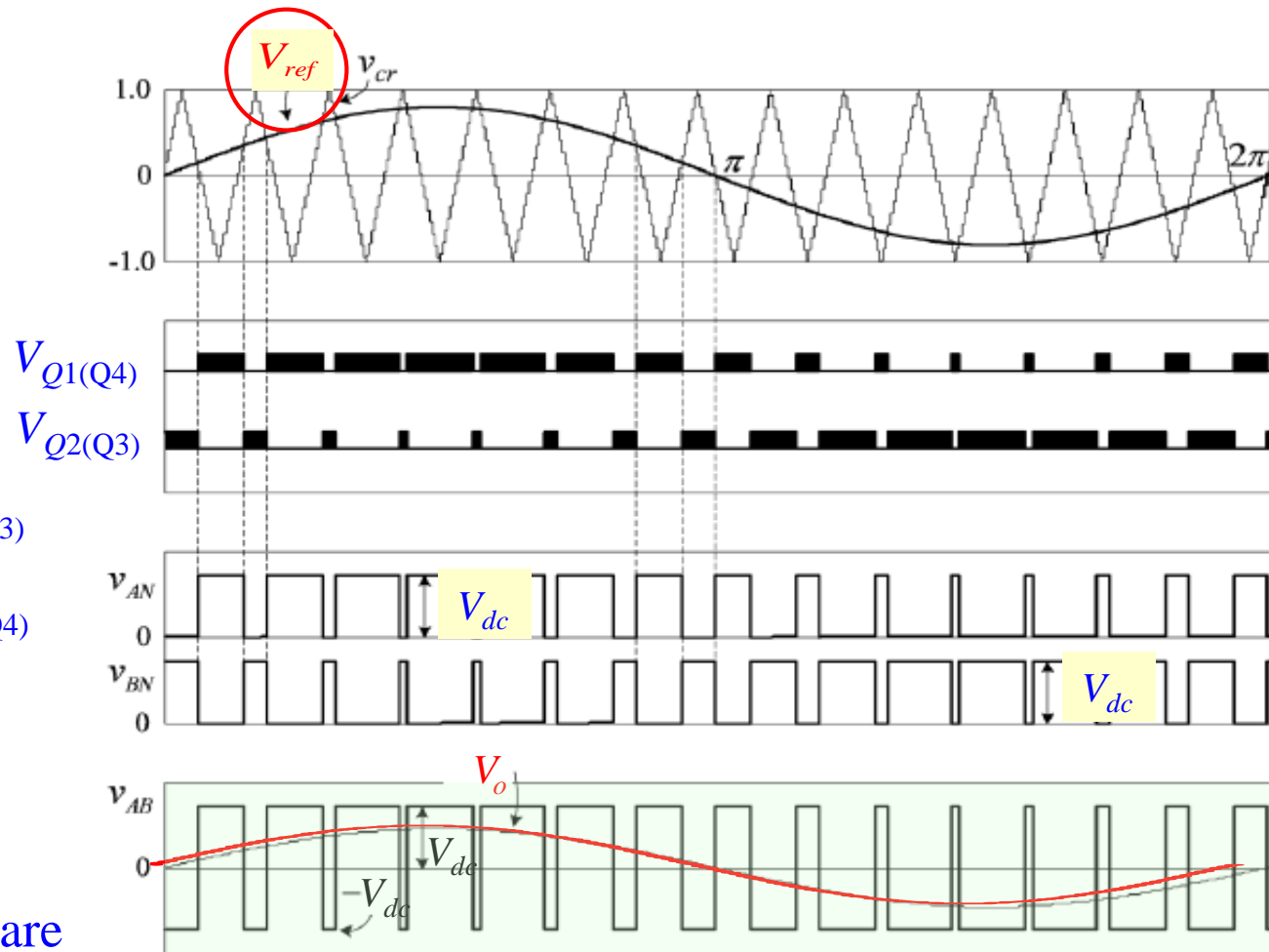
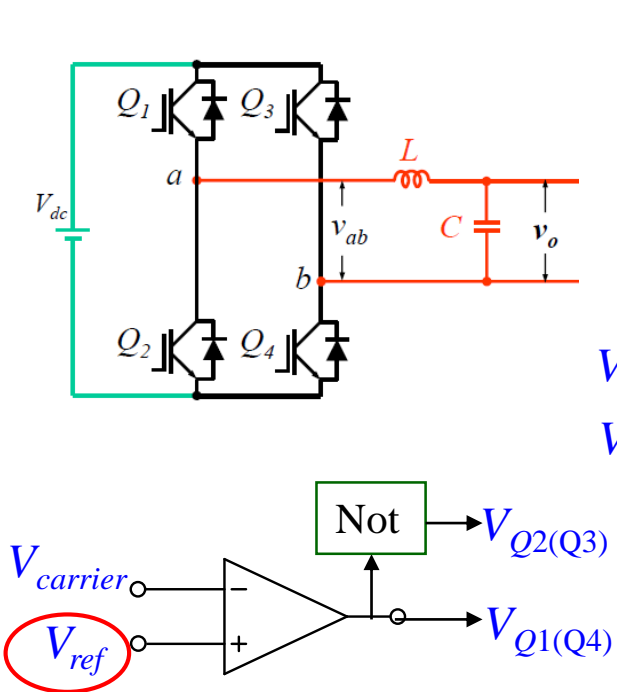
Dead-time, current circulating through anti-paralleled diodes



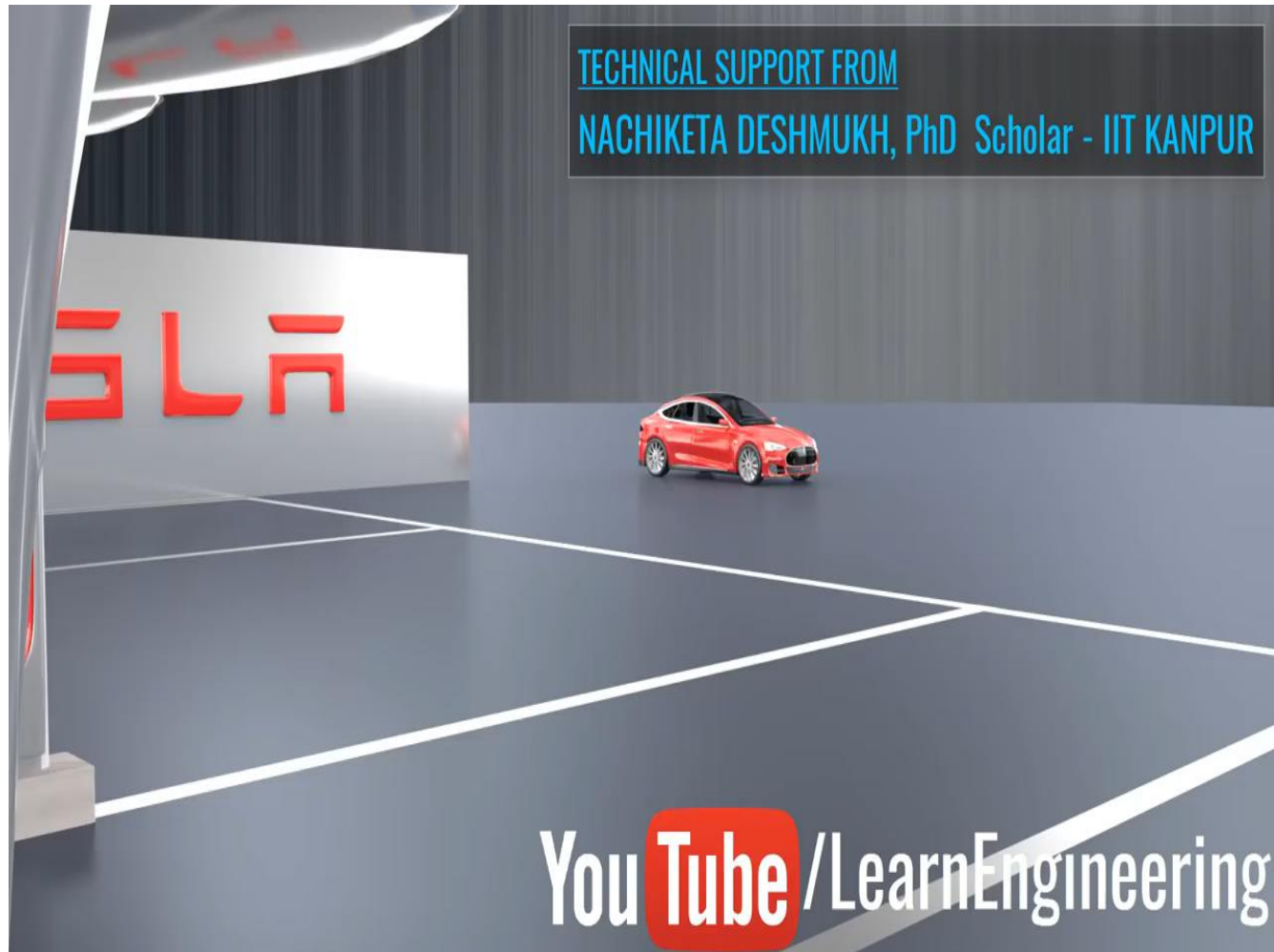
- Q_1 - Q_2 and Q_3 - Q_4 pairs are switched complementarily.
- The total average output voltage $v_o = \text{average}(v_{ab})$.
- Output across a and b looks like a square wave.
- LC circuit to smooth out the waveform to be sinusoidal.



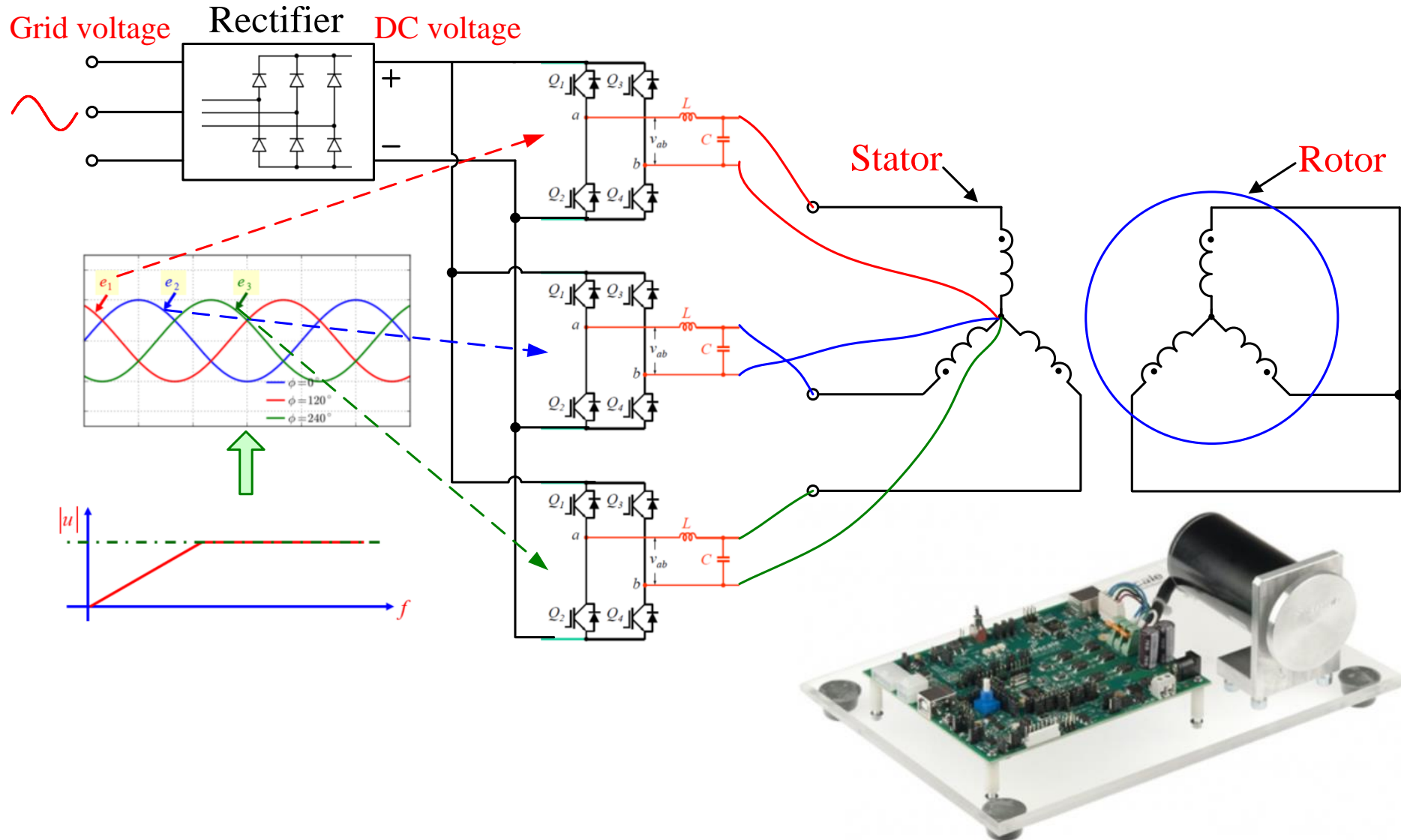
Bipolar modulation of the single phase DC/AC inverter



- $V_{Q1}-V_{Q4}$ are the gating signal for Q_1-Q_4 .
- Q_1-Q_2 and Q_3-Q_4 pairs are switched complementarily.

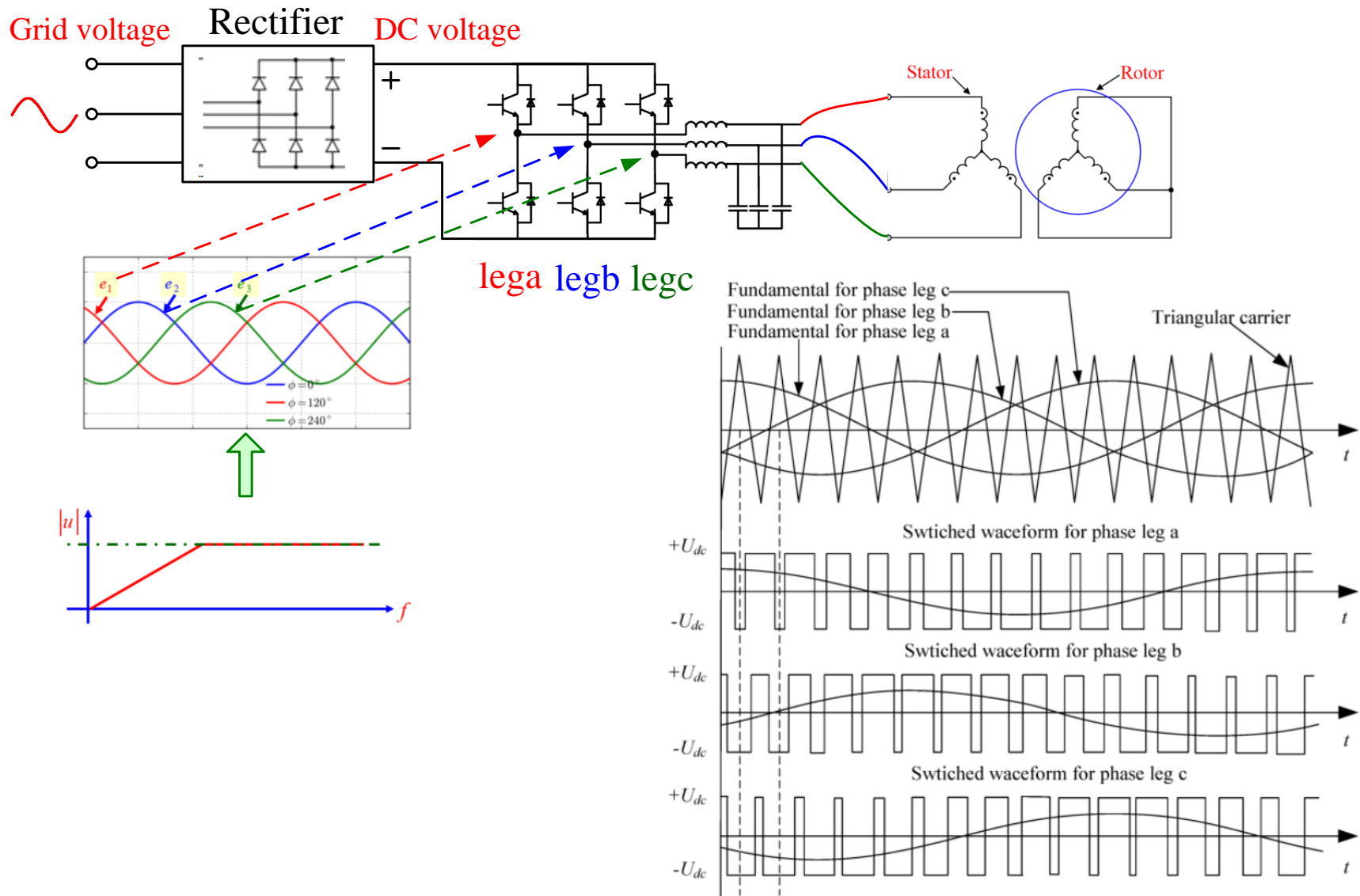


Single phase inverter to three phase inverter





Single phase inverter to three phase inverter



- Understand the operation of DC/DC converters
- Understand the operation of power inverters

Thank you!

Jackzhang@ntu.edu.sg

