

Exercise 7.1

```
...
fromFile "ex1.c";;

Prog
[Fundec
  (None,"main",[(TypI, "n")],
   Block
    [Stmt
      (While
        (Prim2 (">",Access (AccVar "n"),CstI 0),
         Block
          [Stmt (Expr (Prim1 ("printi",Access (AccVar "n"))));
            Stmt
              (Expr
                (Assign
                  (AccVar "n",Prim2 ("-",Access (AccVar "n"),CstI 1)))]));
          Stmt (Expr (Prim1 ("putc",CstI 10)))]])
    ]
  )
]
```

- Declarations:
Fundec
- Statements:
Block, Stmt, While, Expr
- types:
TypI
- expressions:
Prim2, Access, Accvar, CstI, Prim1, Assign

Exercise 7.2

- /MicroC/ex7_2_i.c
- /MicroC/ex7_2_ii.c
- /MicroC/ex7_2_iii.c

Exercise 7.3

- /MicroC/CLex.fsl -> line: 31-32
- /MicroC/CPar.fsy -> line: 17-18 and line: 104-105
- /MicroC/ex7_3_i.c
- /MicroC/ex7_3_ii.c
- /MicroC/ex7_3_iii.c

Exercise 7.4

Added syntax for PreInc and PreDec to Absyn.fs

Modified interp.fs to handle PreIn c and PreDec by modifying the eval function.

Exercise 7.5

- /MicroC/ex7_5_i
- /MicroC/ex7_5_ii
- /MicroC/ex7_5_iii