

Studio Preliminare

Andrea & Ottavia

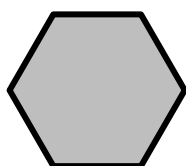
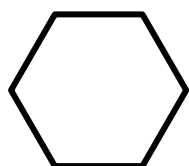
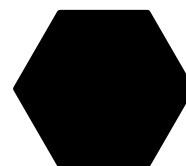
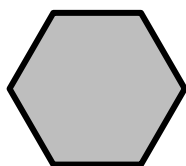
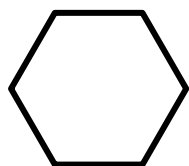
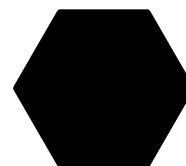
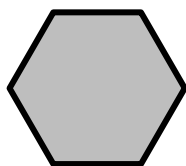
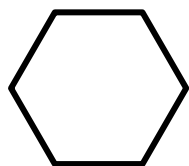
2022-12-09

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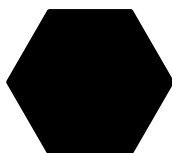
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Blocco di pratica

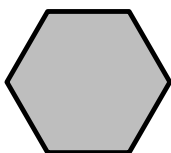
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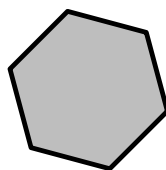
correct



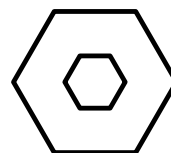
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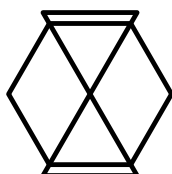
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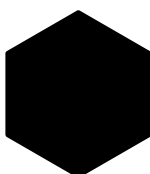
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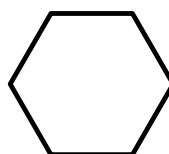
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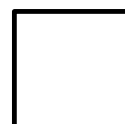
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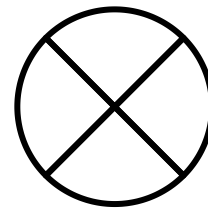
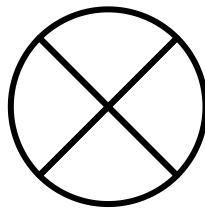
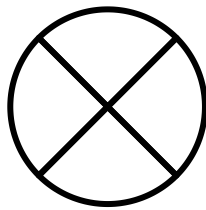
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d.difference



Pratica 2

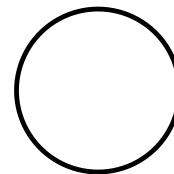
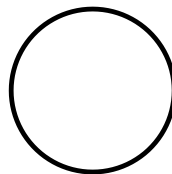
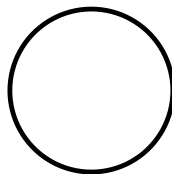


correct

dist1

dist2

dist3

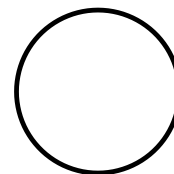
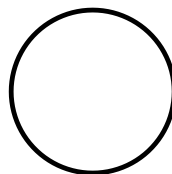
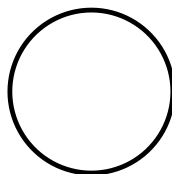
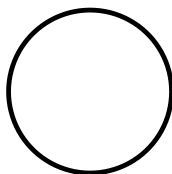


dist4

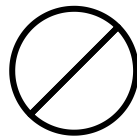
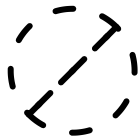
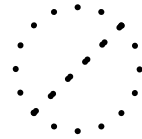
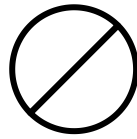
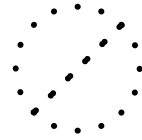
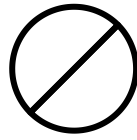
dist5

dist6

dist7



Pratica 3

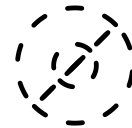
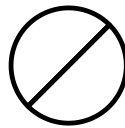
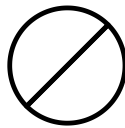
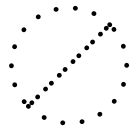


correct

r.diag

wp.copy

wp.matrix

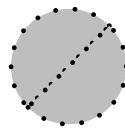
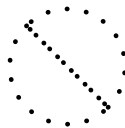
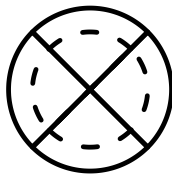


d.union

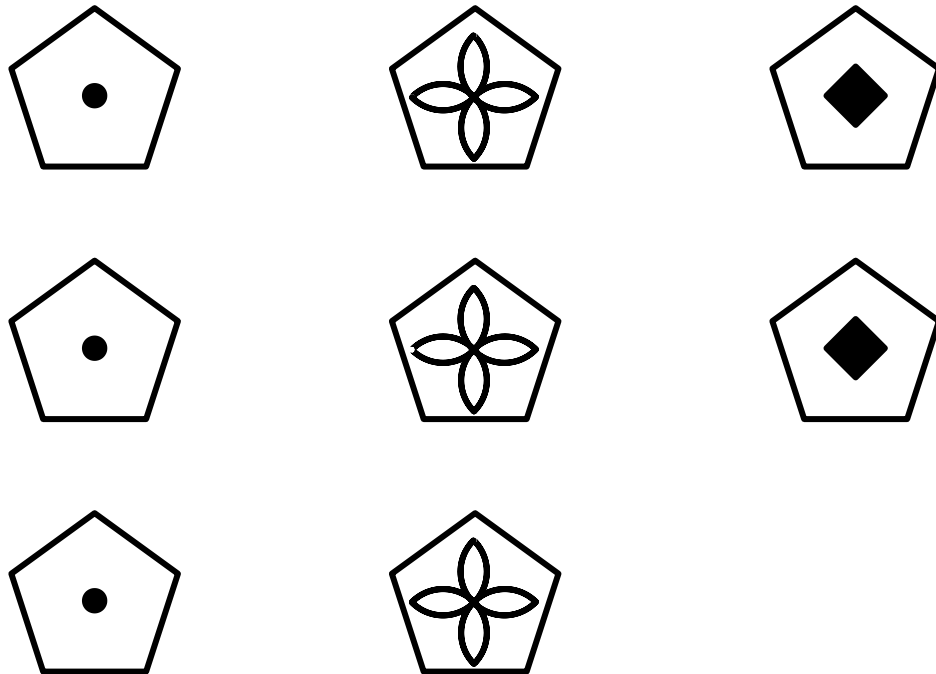
ic.flip

ic.neg

difference



Pratica 4

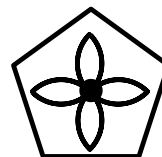
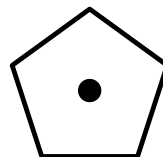
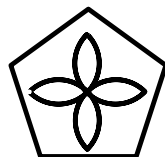
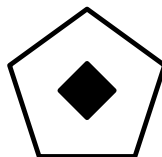


correct

r.diag

wp.copy

wp.matrix

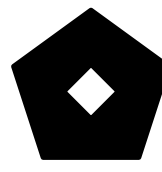
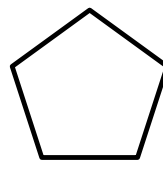
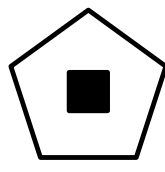
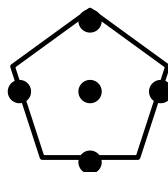


d.union

ic.flip

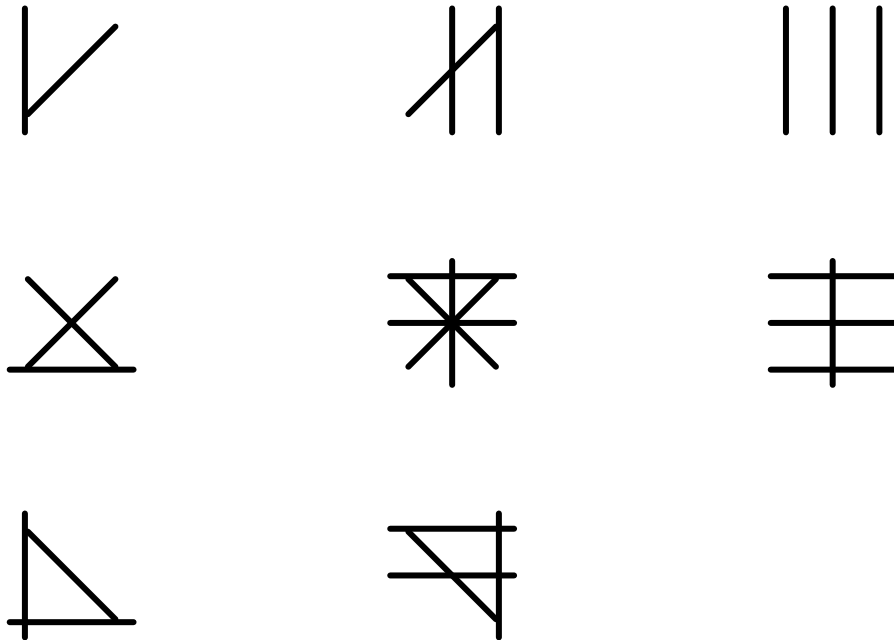
ic.inc

ic.neg



Matrici logiche – SET A

A Logic 3

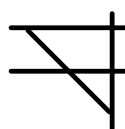
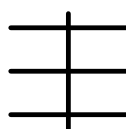
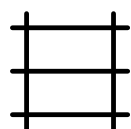


correct

r.top

r.left

d.union

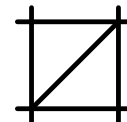
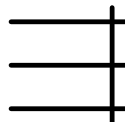
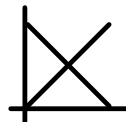
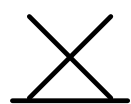


wp.copy

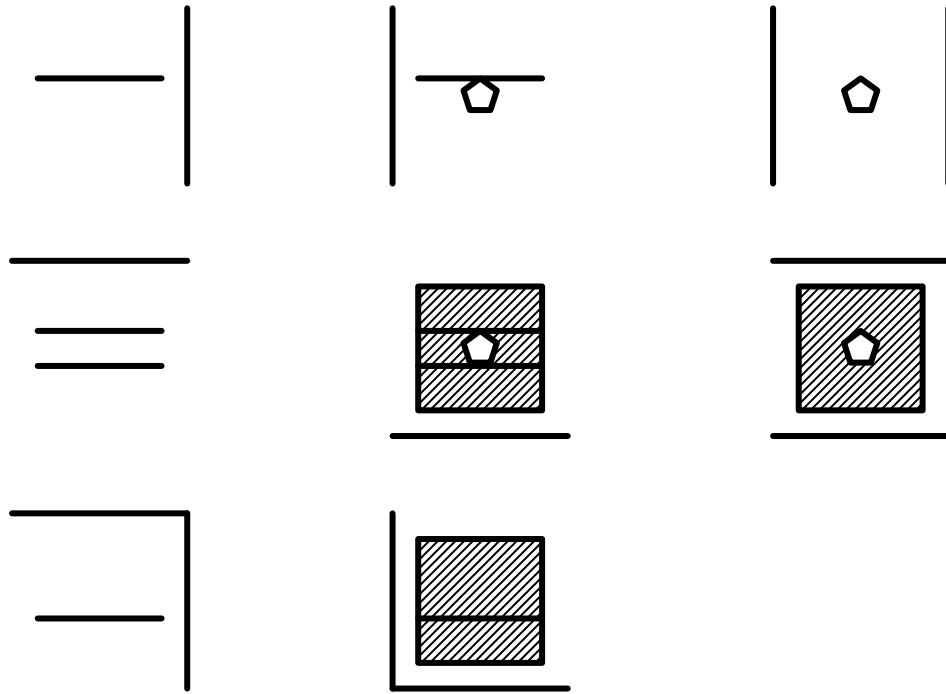
wp.matrix

ic.inc

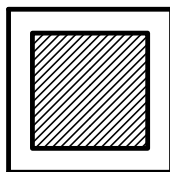
ic.flip



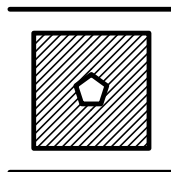
A1 Logic 3



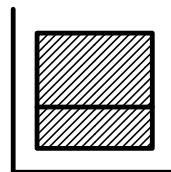
correct



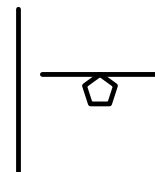
r.top



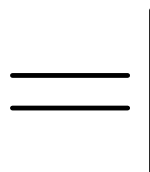
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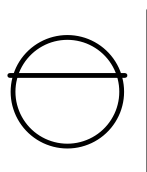
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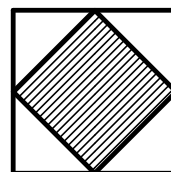
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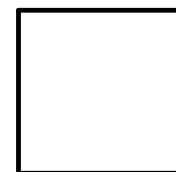
d.union



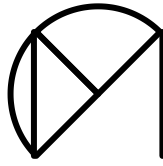
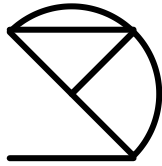
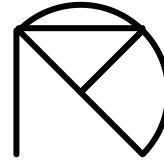
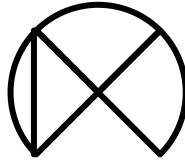
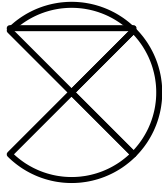
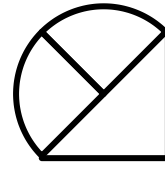
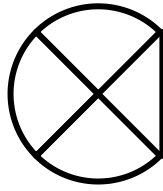
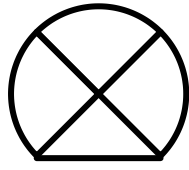
ic.flip



ic.inc



A Logic 2

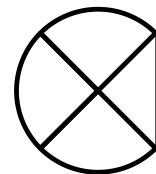
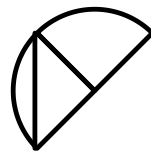
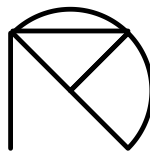


correct

r.top

r.left

wp.copy

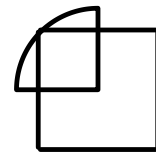
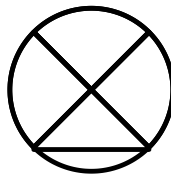
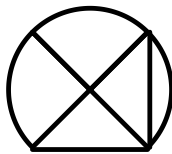


wp.matrix

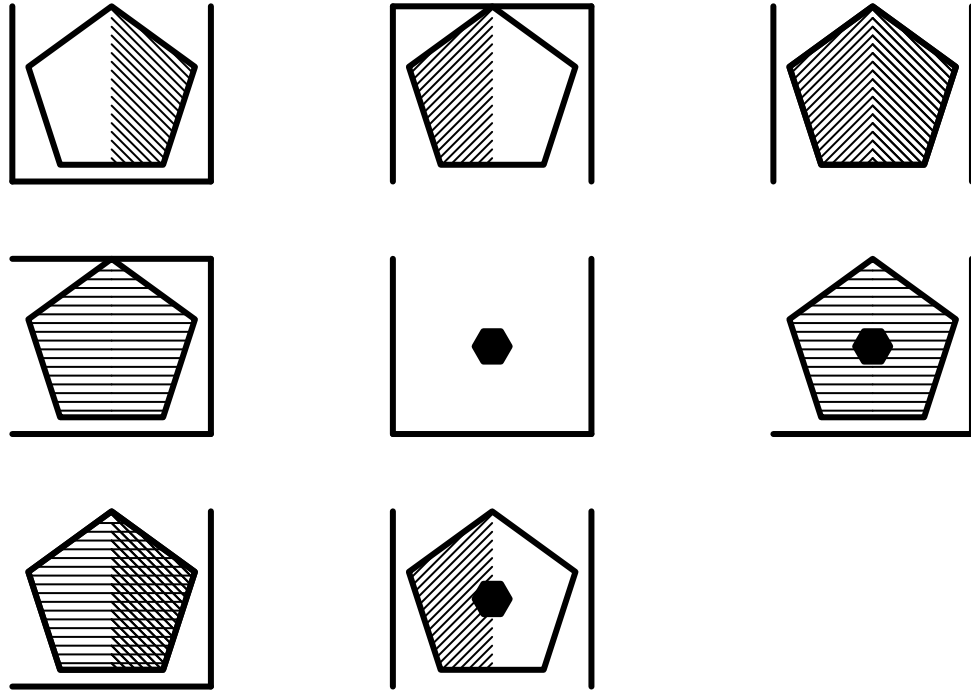
d.union

ic.inc

ic.flip



A1 Logic 2

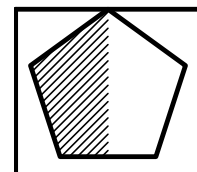
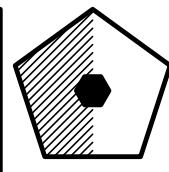
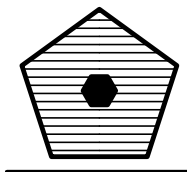
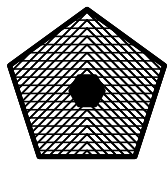


correct

r.top

r.left

wp.copy

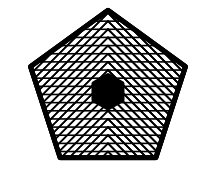
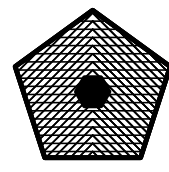
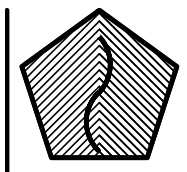
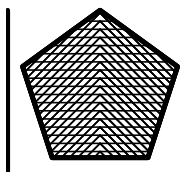


wp.matrix

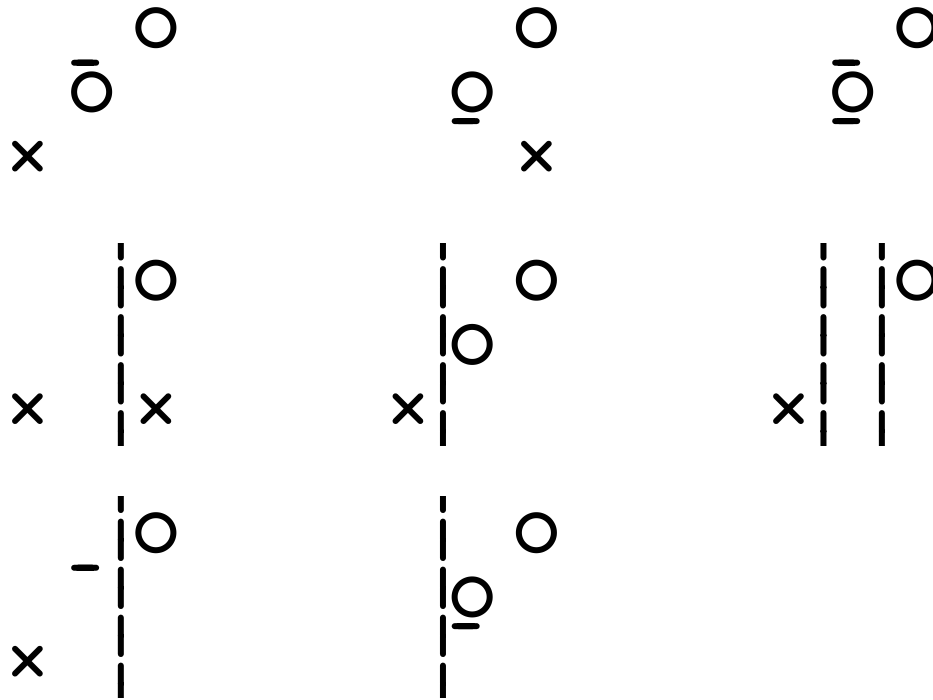
d.union

ic.inc

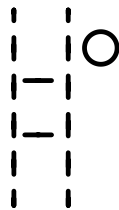
ic.flip



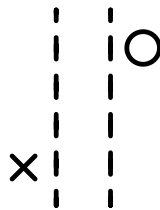
A Logic 1



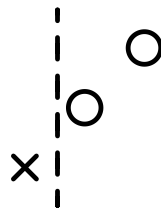
correct



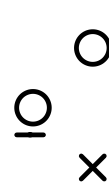
r.top



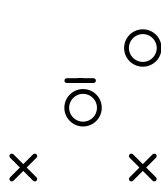
r.diag



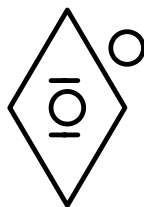
wp.copy



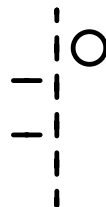
wp.matrix



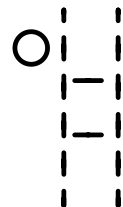
d.union



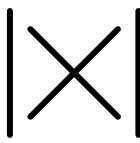
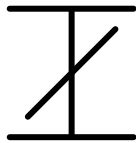
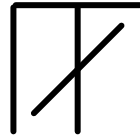
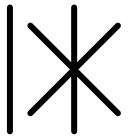
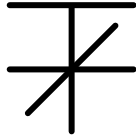
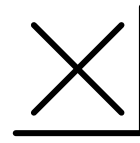
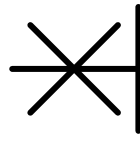
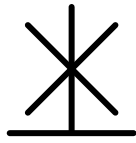
ic.inc



ic.flip



A1 Logic 1

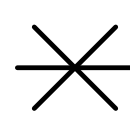
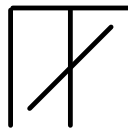
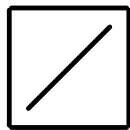


correct

r.top

r.diag

wp.copy

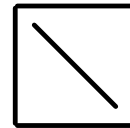
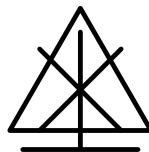
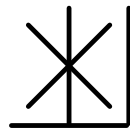


wp.matrix

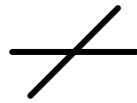
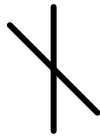
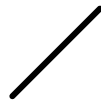
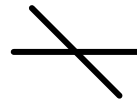
d.union

ic.inc

ic.flip



A logic 4

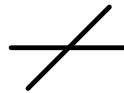
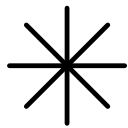


correct

r.diag

r.left

wp.copy

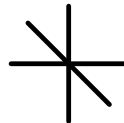
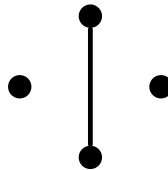
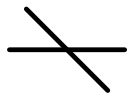


wp.matrix

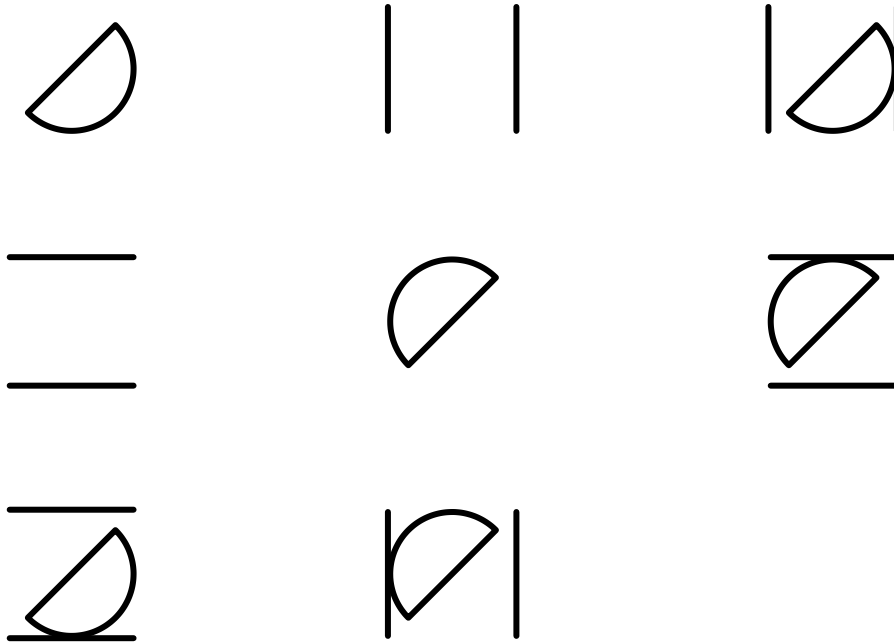
d.union

ic.inc

ic.scale



A1 logic 4

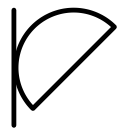
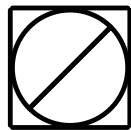


correct

r.diag

r.left

wp.copy

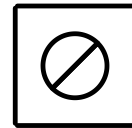
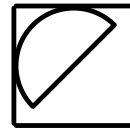
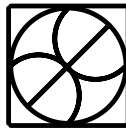
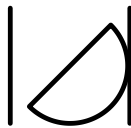


wp.matrix

d.union

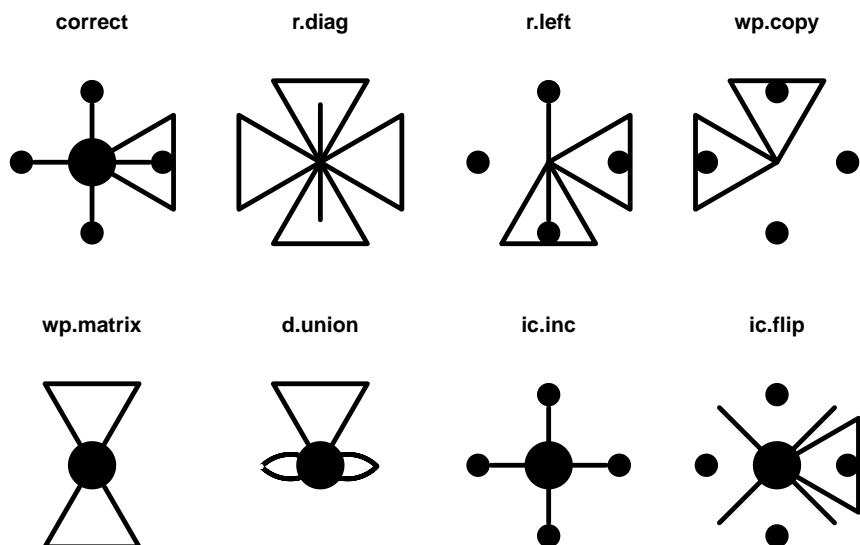
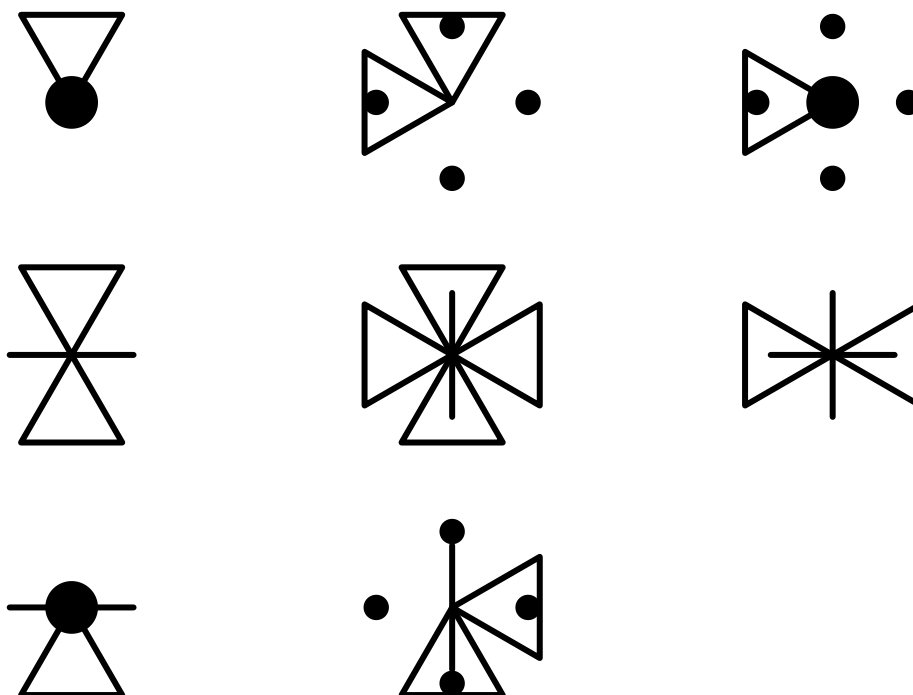
ic.inc

ic.scale

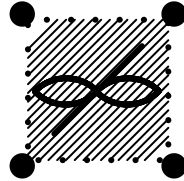
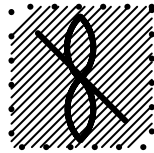
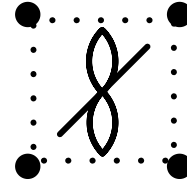
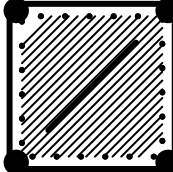
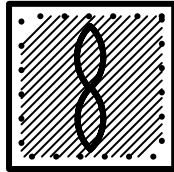
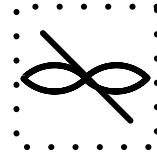
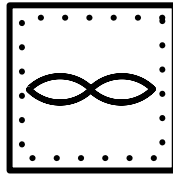
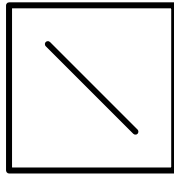


Matrici logiche – SET B

B Logic 3



B1 Logic 3

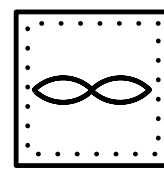
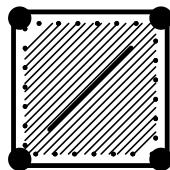
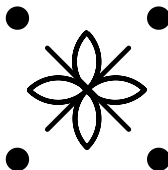


correct

r.diag

r.left

wp.copy

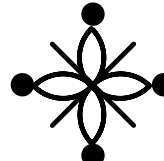
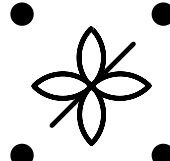
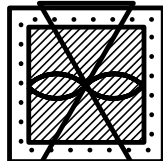
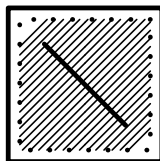


wp.matrix

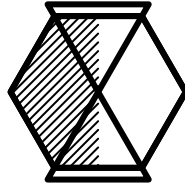
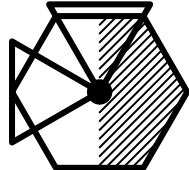
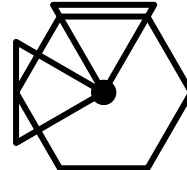
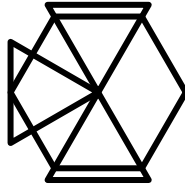
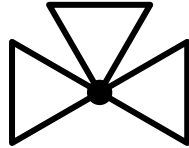
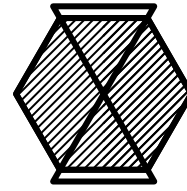
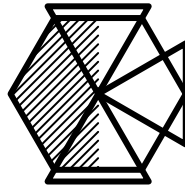
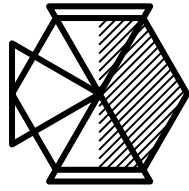
d.union

ic.inc

ic.flip



B logic 2

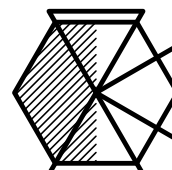
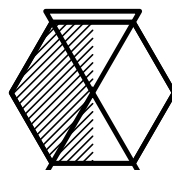
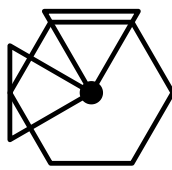
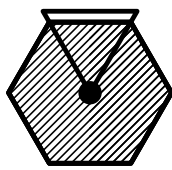


correct

r.top

r.left

wp.copy

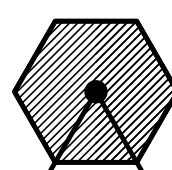
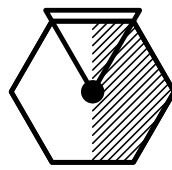
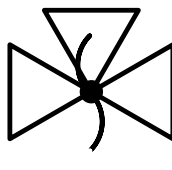
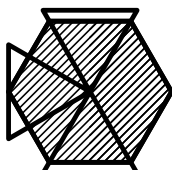


wp.matrix

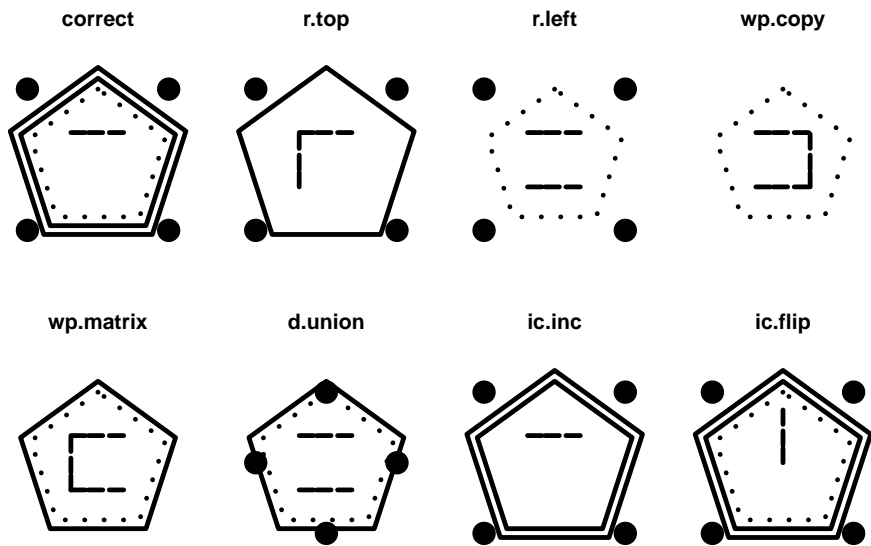
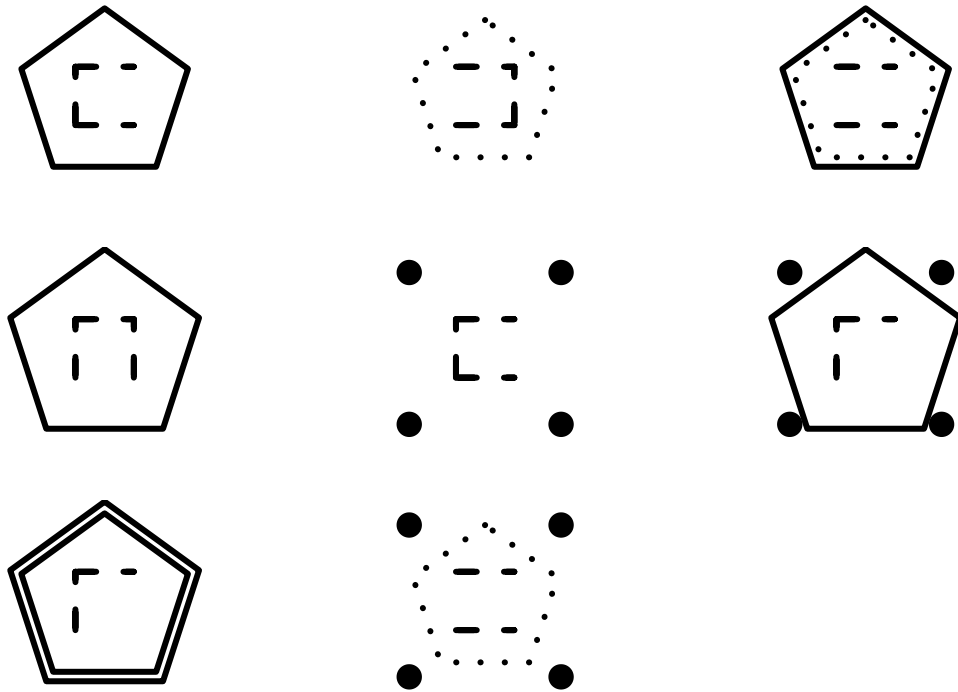
d.union

ic.inc

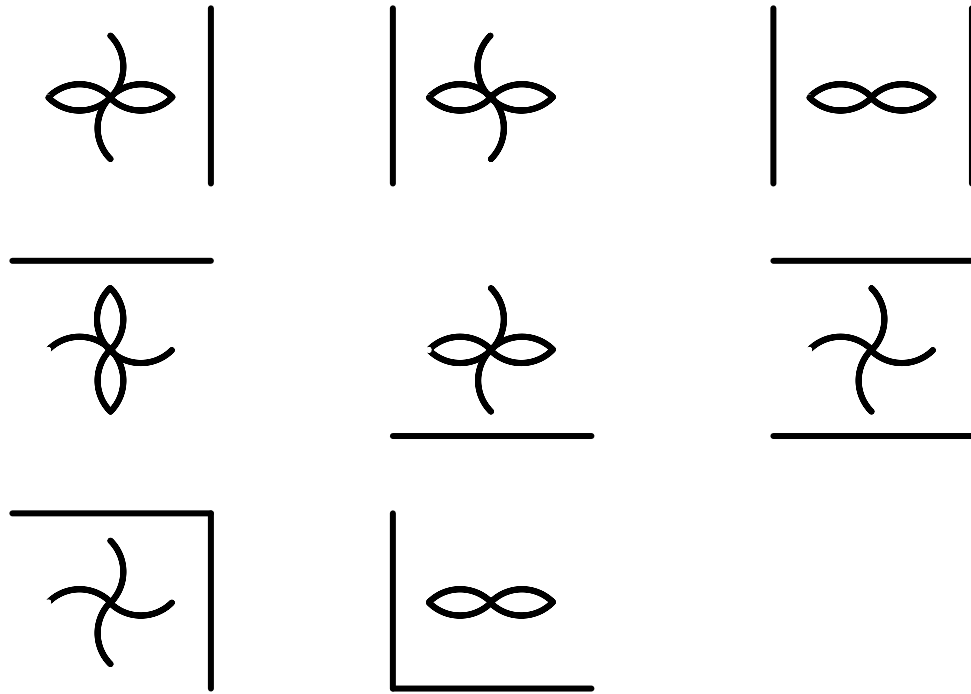
ic.flip



B1 logic 2



B Logic 1

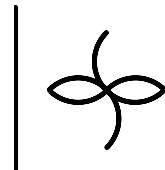
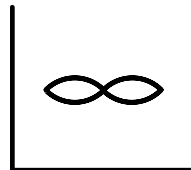
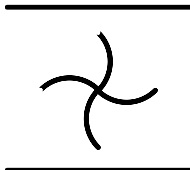
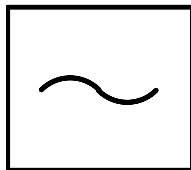


correct

r.top

r.left

wp.copy

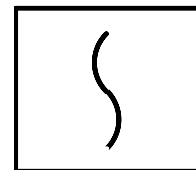
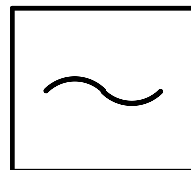
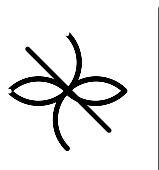


wp.matrix

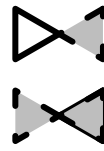
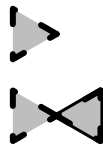
d.union

ic.inc

ic.flip



B1 Logic 1



correct

r.top

r.left

wp.copy

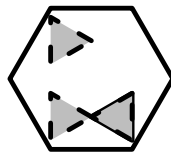


wp.matrix

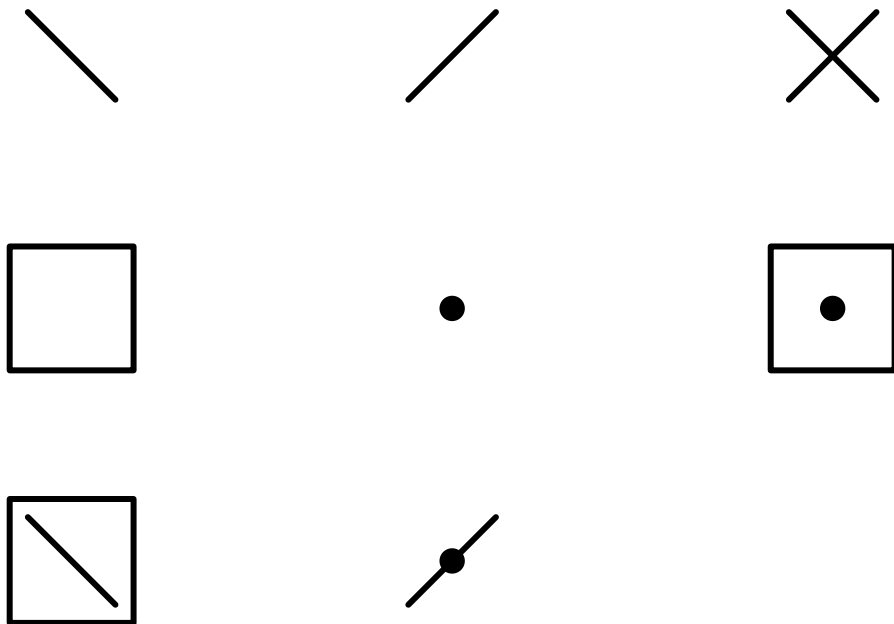
d.union

ic.inc

ic.neg



B logic 4

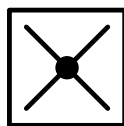


correct

r.diag

r.left

wp.copy

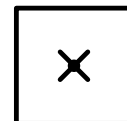
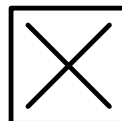
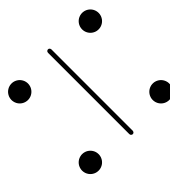


wp.matrix

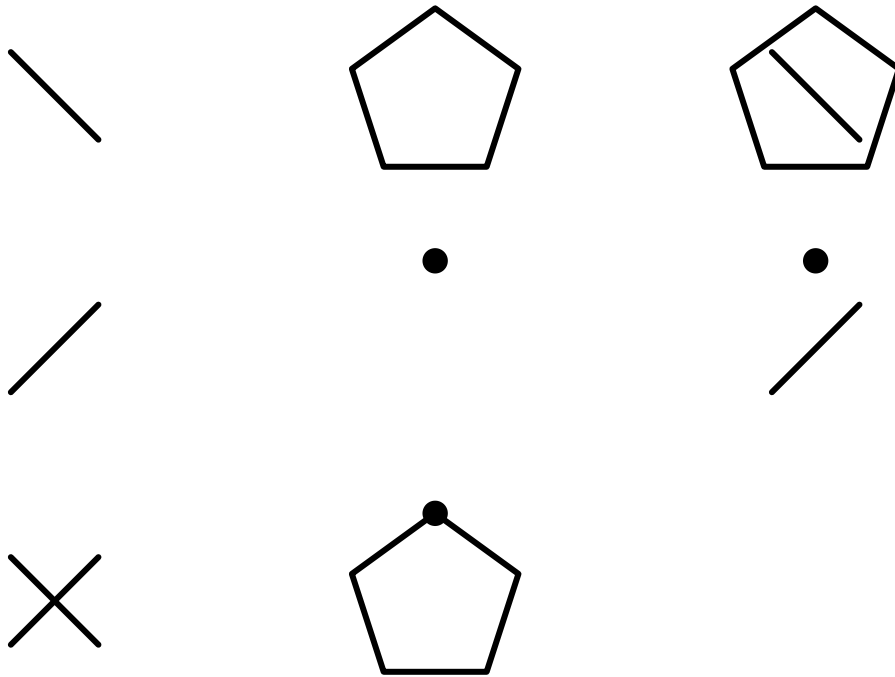
d.union

ic.inc

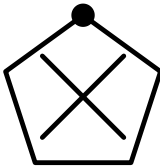
ic.scale



B1 logic 4



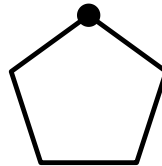
correct



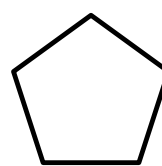
r.diag



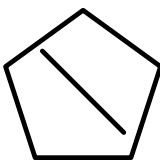
r.left



wp.copy



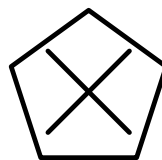
wp.matrix



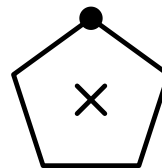
d.union



ic.inc

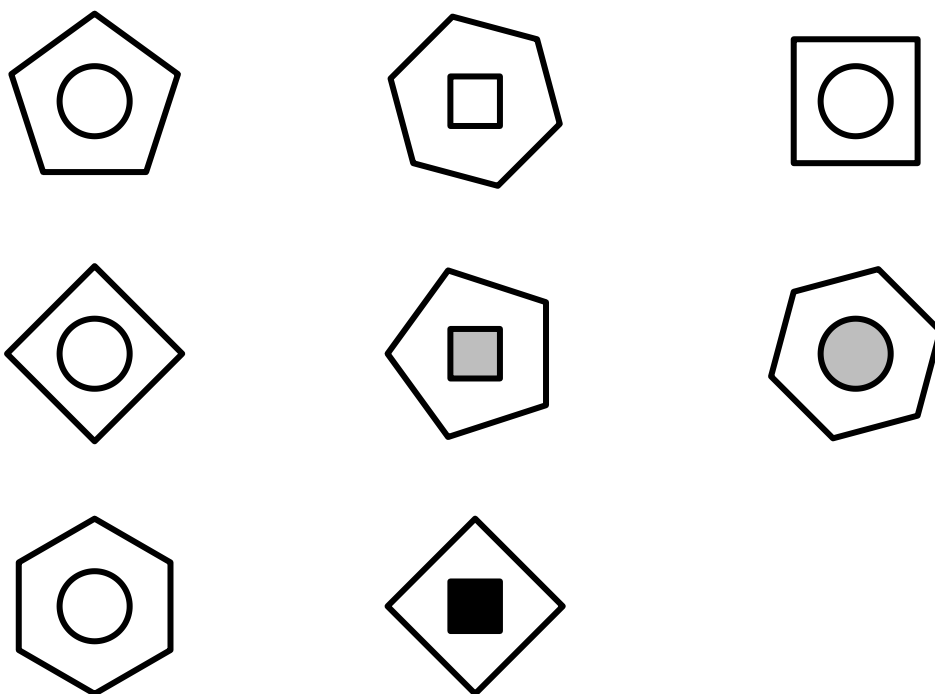


ic.scale



Matrici visuo-spaziali set A

A 1

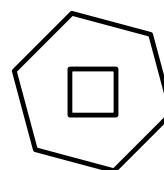
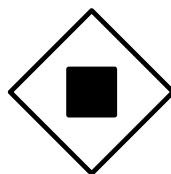
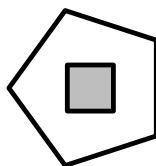
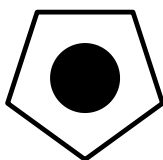


correct

r.diag

r.left

wp.copy

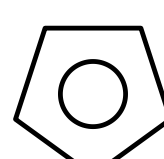
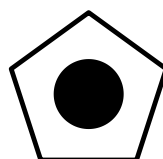
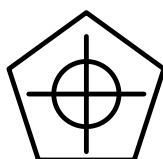
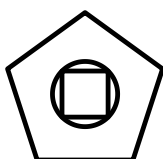


wp.matrix

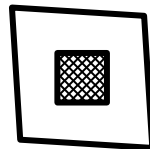
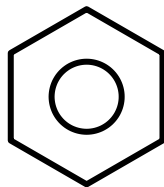
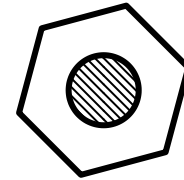
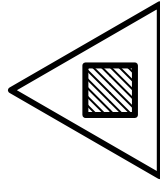
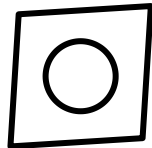
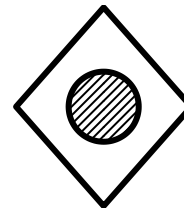
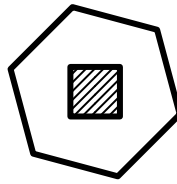
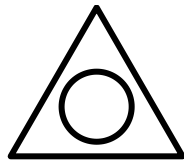
d.union

ic.flip

ic.neg



A1 2

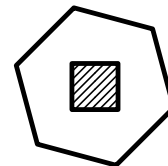
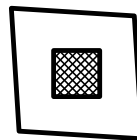
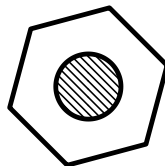
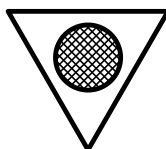


correct

r.top

r.left

wp.copy

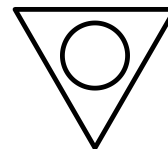
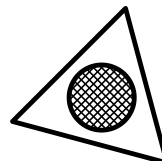
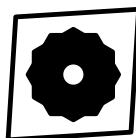
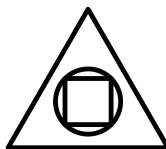


wp.matrix

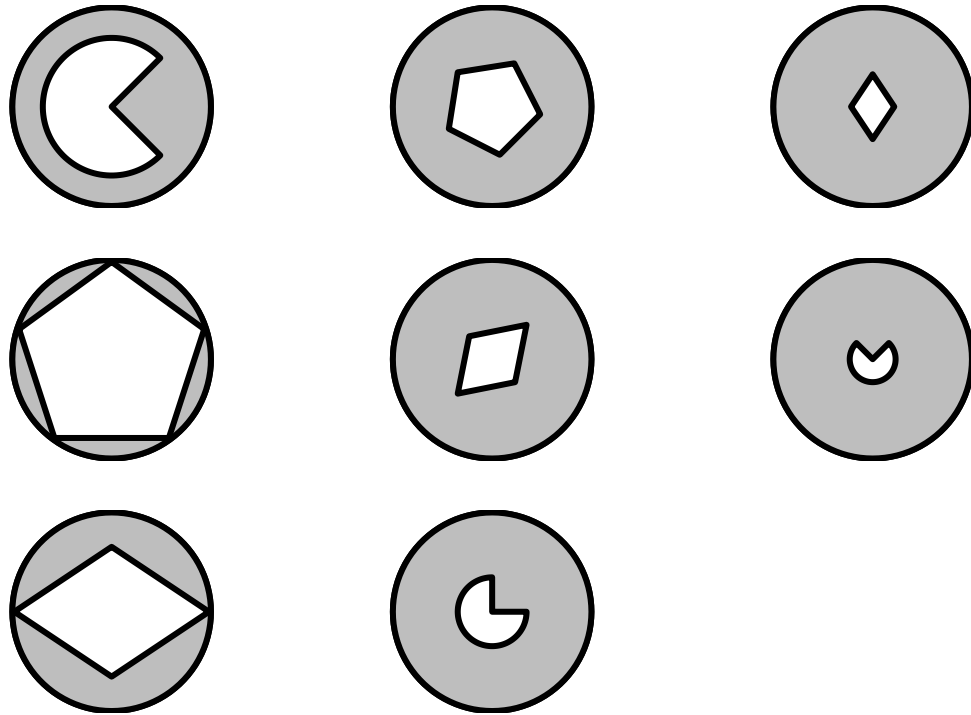
d.union

ic.flip

ic.neg



A 2

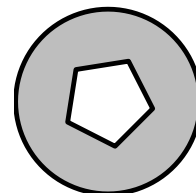
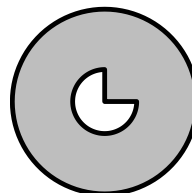
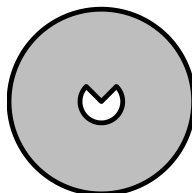
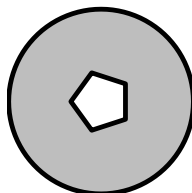


correct

r.top

r.left

wp.copy

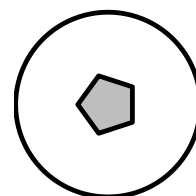
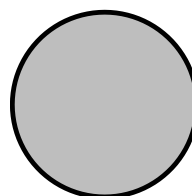
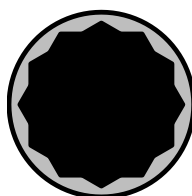
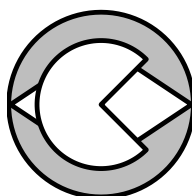


wp.matrix

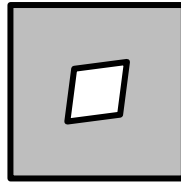
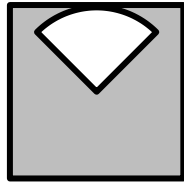
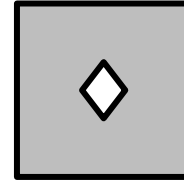
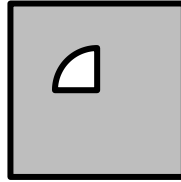
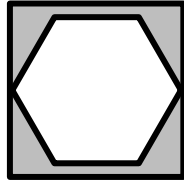
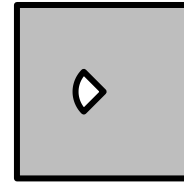
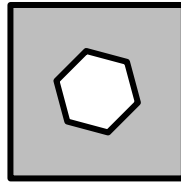
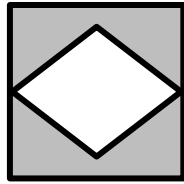
d.union

ic.inc

ic.neg



A1 2

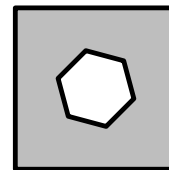
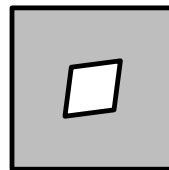
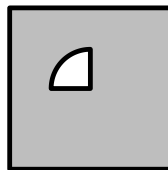
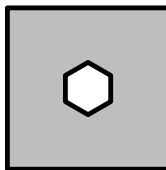


correct

r.diag

r.left

wp.copy

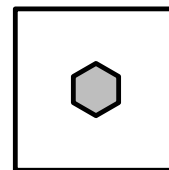
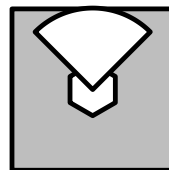
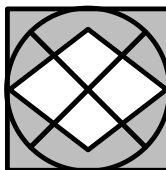


d.union

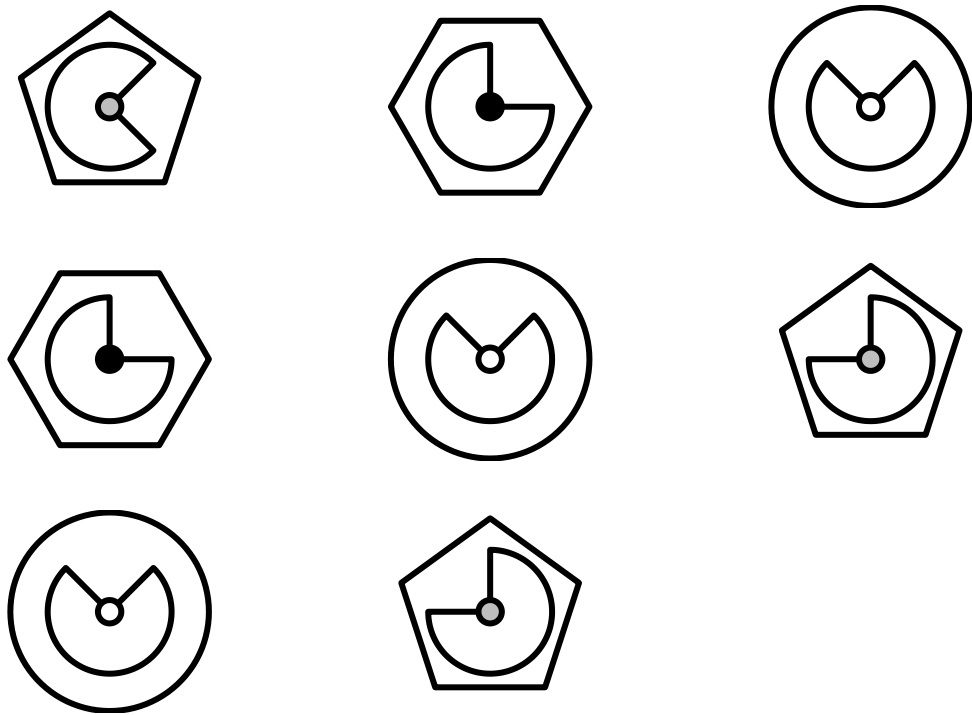
ic.inc

wp.matrix

ic.neg



A 3

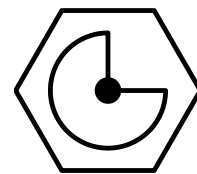
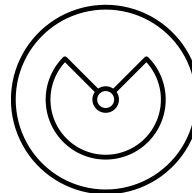
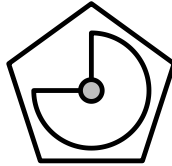
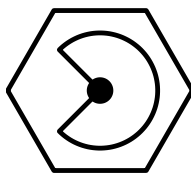


correct

r.top

r.diag

wp.copy

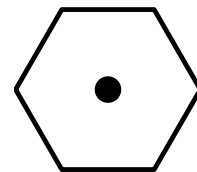
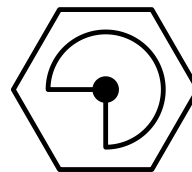
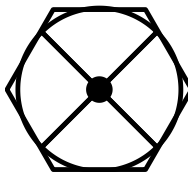
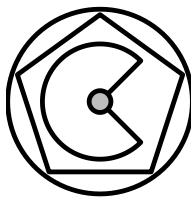


wp.matrix

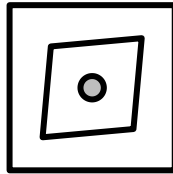
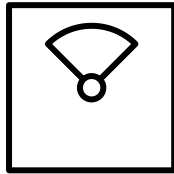
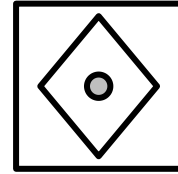
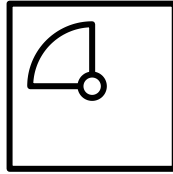
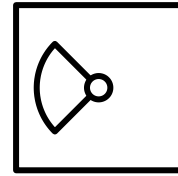
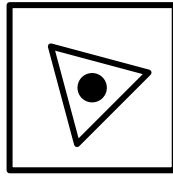
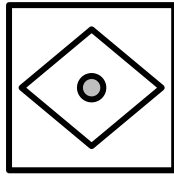
d.union

ic.flip

ic.inc



A1 3

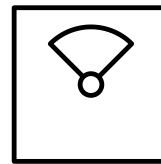
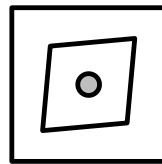
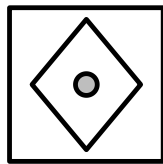
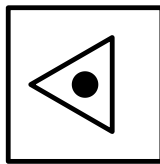


correct

r.top

r.left

wp.copy

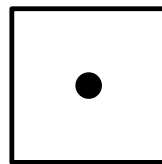
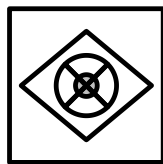
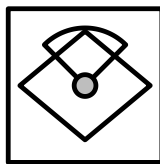


wp.matrix

d.union

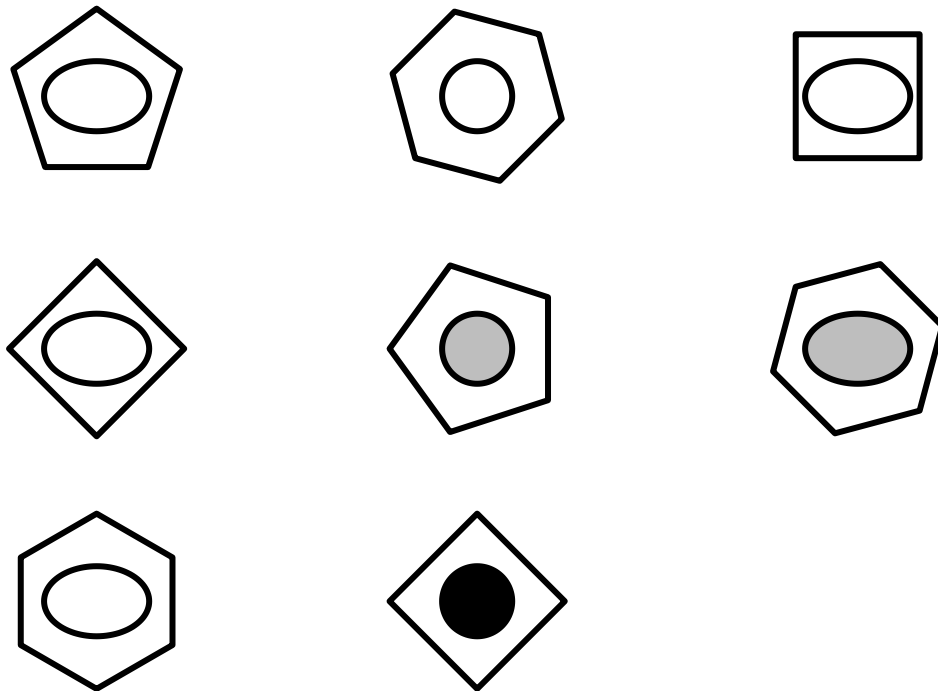
ic.inc

ic.flip



Matrici Visuo spaziali – SET B

B 1

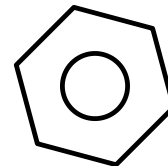
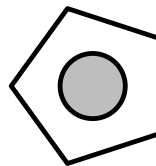
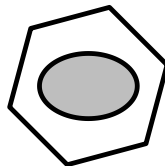
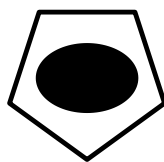


correct

r.top

r.diag

wp.copy

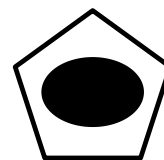
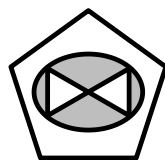
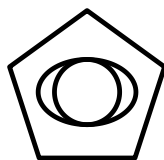


wp.matrix

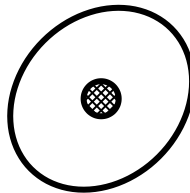
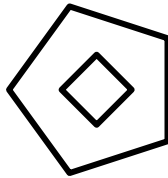
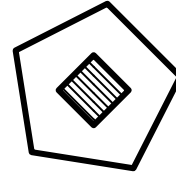
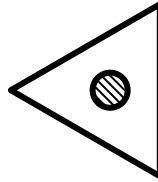
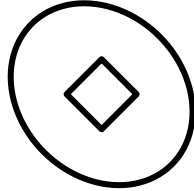
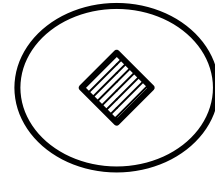
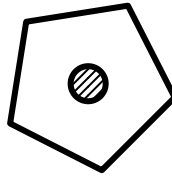
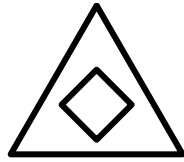
d.union

ic.flip

ic.neg



B1 1

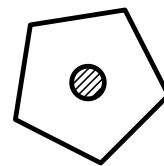
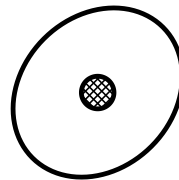
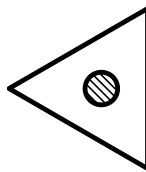
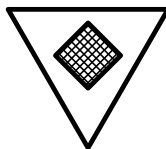


correct

r.diag

r.left

wp.copy

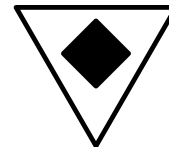
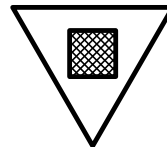
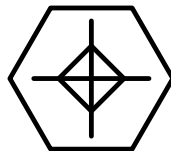
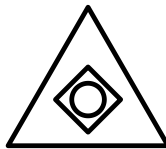


wp.matrix

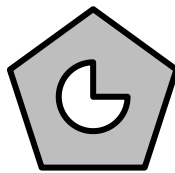
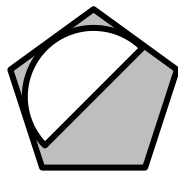
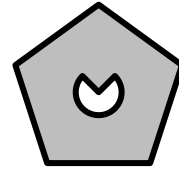
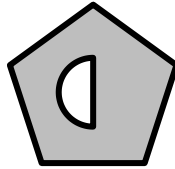
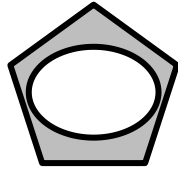
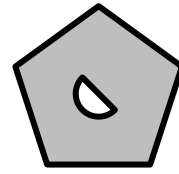
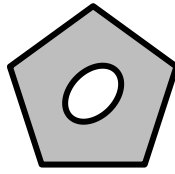
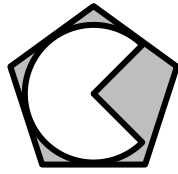
d.union

ic.flip

ic.neg



B 2

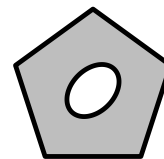
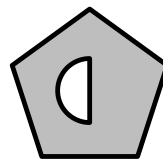
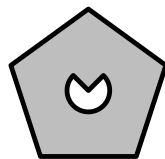
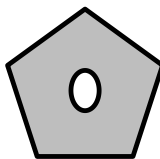


correct

r.top

r.diag

wp.copy

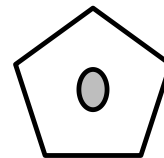
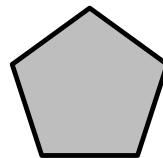
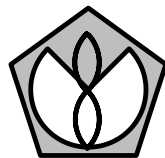
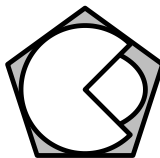


wp.matrix

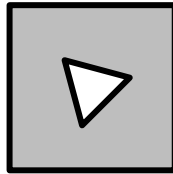
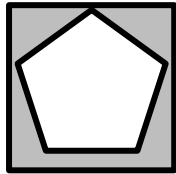
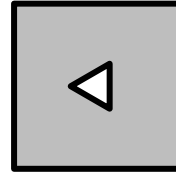
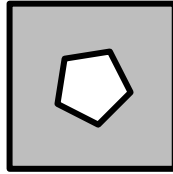
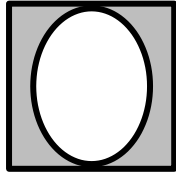
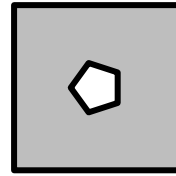
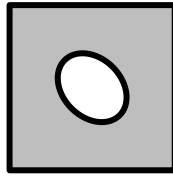
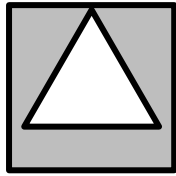
d.union

ic.inc

ic.neg



B1 2

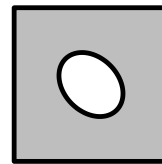
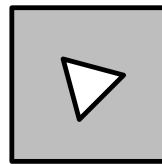
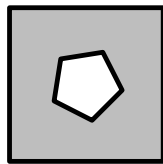
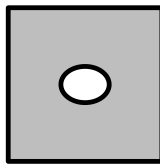


correct

r.diag

r.left

wp.copy

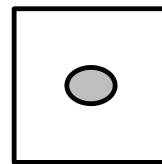
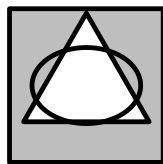
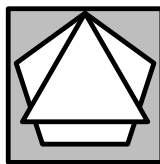


wp.matrix

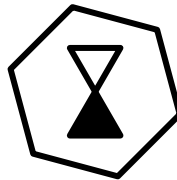
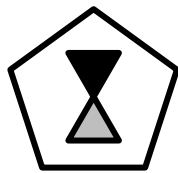
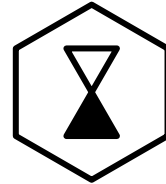
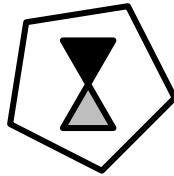
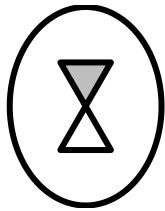
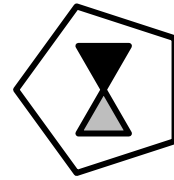
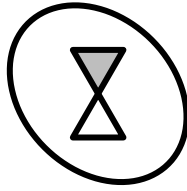
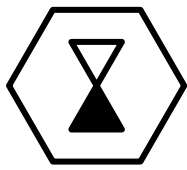
d.union

ic.inc

ic.neg



B 3

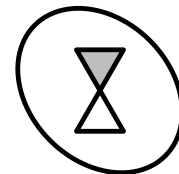
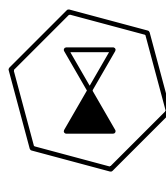
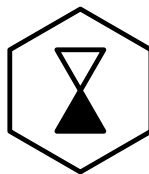
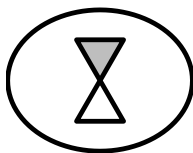


correct

r.top

r.left

wp.copy

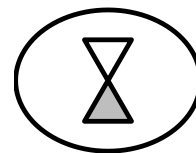
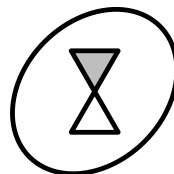
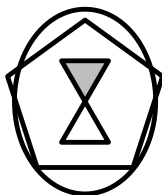


wp.matrix

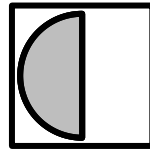
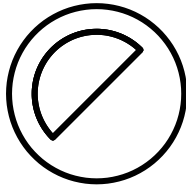
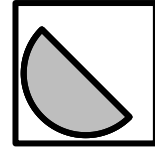
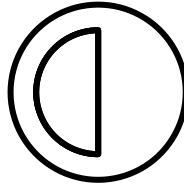
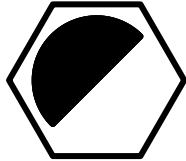
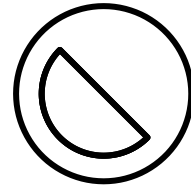
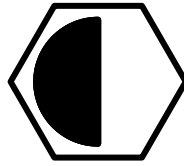
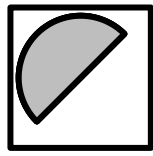
d.union

ic.flip

ic.neg



B1 3

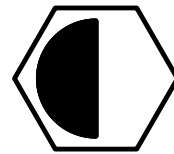
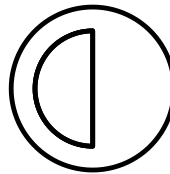
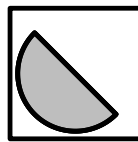


correct

r.top

r.diag

wp.copy



wp.matrix

d.union

ic.flip

ic.neg

