

# Partial Regularization of First-Order Resolution Proofs

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## Abstract

Proofs are a key feature of modern propositional and first-order theorem provers. Proofs generated by such tools serve as explanations for unsatisfiability of statements. However, these explanations are complicated by proofs which are not necessarily as concise as possible. There are a wide variety of compression techniques for propositional resolution proofs, but fewer compression techniques for first-order resolution proofs generated by automated theorem provers. This paper describes an approach to compressing first-order logic proofs based on lifting proof compression ideas used in propositional logic to first-order logic. An empirical evaluation of the approach is included.

## 1 Introduction

Proof production is a key feature that has been gaining importance for modern theorem provers. Proofs are explanations for unsatisfiability, and are crucial for applications that require certification of a prover’s answers or that extract additional information from proofs (e.g. unsat cores, interpolants, instances of quantified variables). As systems which rely on automated reasoning tools are used in a wider range of applications with greater consequences, the need to justify and verify the choices made by these systems will grow as well.

Mature first-order automated theorem provers, commonly based on refinements and extensions of resolution and superposition calculi [17, 18, 25, 15, 14], support proof generation. However, proof production is non-trivial [19], and the most efficient provers do not necessarily generate the shortest proofs.

Proof compression techniques ameliorate the difficulties that automated reasoning tools encounter during proof generation. Such techniques can be integrated into theorem provers or external tools with minimal overhead. Moreover, proof compression techniques (like those described in this paper) may result in a stronger proof which uses a strict subset of the original axioms required, which could also be considered simpler. The problem of proof compression is also closely related to Hilbert’s 24th Problem [22], which asks for criteria to judge the simplicity of proofs; proof length is one possible criterion.

There are also technical reasons to seek smaller proofs. Longer proofs take longer to check, consume more memory during proof-checking, occupy more storage space and are harder to exchange, may have a larger unsat core (if more input clauses are used in the proof), and have a larger Herbrand sequent if more variables are instantiated [26, 10, 11, 16]. Recent applications of SAT solvers to mathematical problems have resulted in very large proofs; e.g., the proof of a long-standing problem in combinatorics was initially 200GB [13]. Such proofs are hard to

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store, let alone validate. More practically, a restriction of 100GB of disk space per benchmark per solver prevented validation of proofs in the SAT 2014 competition [12]. The inability to write their results to disk renders these solvers useless in some cases. Moreover, even if the only direct improvement of shorter proofs is in the communication between systems, there are indirect benefits to the end-user of a tool e.g., in terms of its responsiveness.

For propositional resolution proofs, as those typically generated by SAT- and SMT-solvers, there is a wide variety of proof compression techniques. These techniques include investigating algebraic properties of resolution [5], rearranging and sharing chains of resolution inferences [1, 20], and splitting a proof according to a literal which may result in a compressed proof when recombined [4]. Bar-Ilan et al. [2] and Fontaine et al. [6] described a linear time proof compression algorithm based on partial regularization, which removes an inference  $\eta$  when it is redundant in the sense that its pivot literal already occurs as the pivot of another inference in every path from  $\eta$  to the root of the proof.

By contrast, there has been much less work on simplifying first-order proofs. For arbitrary proofs in the Thousands of Problems for Theorem Provers (TPTP) [21] format (including DAG-like first-order resolution proofs), there is an algorithm [23] that looks for terms that occur often in any Thousands of Solutions from Theorem Provers (TSTP) [21] proof and abbreviates them.

The work reported in this paper is part of a new trend that aims at lifting successful propositional proof compression algorithms to first-order logic. We first lifted the **LowerUnits** (LU) algorithm [6], which delays resolution steps with unit clauses, resulting in a new algorithm that we called **GreedyLinearFirstOrderLowerUnits** (GFOLU) [8]. Here we continue this line of research by lifting the **RecyclePivotsWithIntersection** (RPI) algorithm [6], which improves the **RecyclePivots** (RP) algorithm [2] by detecting nodes that can be regularized even when they have multiple children.

Section 2 defines the first-order resolution calculus and Section 3 summarizes the propositional RPI algorithm. Section 4 discusses the challenges that arise in the first-order case (mainly due to unification), which are not present in the propositional case, and concludes with conditions useful for first-order regularization. Section 6 presents experimental results obtained by applying this algorithm, and its combinations with GFOLU, on proofs generated by SPASS and randomly generated proofs. Section 7 concludes the paper.

## 2 The Resolution Calculus

As usual, our language has infinitely many variable symbols (e.g.  $x, y, z, x_1, x_2, \dots$ ), constant symbols (e.g.  $a, b, c, a_1, a_2, \dots$ ), function symbols of every arity (e.g.  $f, g, f_1, f_2, \dots$ ) and predicate symbols of every arity (e.g.  $P, Q, P_1, P_2, \dots$ ). A *term* is any variable, constant or the application of an  $n$ -ary function symbol to  $n$  terms. An *atomic formula* (*atom*) is the application of an  $n$ -ary predicate symbol to  $n$  terms. A *literal* is an atom or the negation of an atom. The *complement* of a literal  $\ell$  is denoted  $\bar{\ell}$  (i.e. for any atom  $P$ ,  $\bar{P} = \neg P$  and  $\overline{\neg P} = P$ ). The *underlying atom* of a literal  $\ell$  is denoted  $|\ell|$  (i.e. for any atom  $p$ ,  $|P| = P$  and  $|\neg P| = P$ ). A *clause* is a multiset of literals.  $\perp$  denotes the *empty clause*. A *unit clause* is a clause with a single literal. Sequent notation is used for clauses (i.e.  $P_1, \dots, P_n \vdash Q_1, \dots, Q_m$  denotes the clause  $\{\neg P_1, \dots, \neg P_n, Q_1, \dots, Q_m\}$ ). A *substitution*  $\{x_1 \backslash t_1, x_2 \backslash t_2, \dots\}$  is a mapping from variables  $\{x_1, x_2, \dots\}$  to, respectively, terms  $\{t_1, t_2, \dots\}$ . The application of a substitution  $\sigma$  to a term  $t$ , a literal  $\ell$  or a clause  $\Gamma$  results in, respectively, the term  $t\sigma$ , the literal  $\ell\sigma$  or the clause  $\Gamma\sigma$ , obtained from  $t$ ,  $\ell$  and  $\Gamma$  by replacing all occurrences of the variables in  $\sigma$  by the corresponding terms in  $\sigma$ . A literal  $\ell$  *matches* another literal  $\ell'$  if there is a substitution  $\sigma$  such that  $\ell\sigma = \ell'$ . A *unifier* of a set of literals is a substitution that makes all literals in the set

equal. We will use  $X \sqsubseteq Y$  to denote that  $X$  *subsumes*  $Y$ , when there exists a substitution  $\sigma$  such that  $X\sigma \sqsubseteq Y$ .

A *resolution proof* is a directed acyclic graph of clauses where the edges correspond to the inference rules of resolution and factoring, as explained in detail in Definition 2.1. A *resolution refutation* is a resolution proof with root  $\perp$ .

**Definition 2.1** (First-Order Resolution Proof). A directed acyclic graph  $\langle V, E, \Gamma \rangle$ , where  $V$  is a set of nodes and  $E$  is a set of edges labeled by literals and substitutions (i.e.  $E \subset V \times 2^{\mathcal{L}} \times \mathcal{S} \times V$ , where  $\mathcal{L}$  is the set of all literals and  $\mathcal{S}$  is the set of all substitutions, and  $v_1 \xrightarrow[\sigma]{\ell} v_2$  denotes an edge from node  $v_1$  to node  $v_2$  labeled by the literal  $\ell$  and the substitution  $\sigma$ ), is a proof of a clause  $\Gamma$  iff it is inductively constructible according to the following cases:

- **Axiom:** If  $\Gamma$  is a clause,  $\hat{\Gamma}$  denotes some proof  $\langle \{v\}, \emptyset, \Gamma \rangle$ , where  $v$  is a new node.
- **Resolution<sup>1</sup>:** If  $\psi_L$  is a proof  $\langle V_L, E_L, \Gamma_L \rangle$  and  $\psi_R$  is a proof  $\langle V_R, E_R, \Gamma_R \rangle$ ,  $\sigma_L$  and  $\sigma_R$  are substitutions s.t.  $\ell_L \sigma_L = \ell_R \sigma_R$ , then  $\psi_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \psi_R$  denotes a proof  $\langle V, E, \Gamma \rangle$  s.t.

$$\begin{aligned} V &= V_L \cup V_R \cup \{v\} & \Gamma &= \Gamma'_L \sigma_L \cup \Gamma'_R \sigma_R \\ E &= E_L \cup E_R \cup \left\{ \rho(\psi_L) \xrightarrow[\sigma_L]{\{\ell_L\}} v, \rho(\psi_R) \xrightarrow[\sigma_R]{\{\ell_R\}} v \right\} \end{aligned}$$

where  $v$  is a new (resolution) node and  $\rho(\varphi)$  denotes the root node of  $\varphi$ . The literals  $\ell_L$  and  $\ell_R$  are *resolved literals*, whereas  $\ell_L \sigma_L$  and  $\ell_R \sigma_R$  are its *instantiated resolved literals*. The *pivot* is the underlying atom of its instantiated resolved literals (i.e.  $|\ell_L \sigma_L|$  or, equivalently,  $|\ell_R \sigma_R|$ ).

- **Factoring:** If  $\psi'$  is a proof  $\langle V', E', \Gamma' \rangle$ ,  $\sigma$  is a unifier of  $\{\ell_1, \dots, \ell_n\}$ , and  $\ell = \ell_i \sigma$  for any  $i \in \{1, \dots, n\}$ , then  $\lfloor \psi' \rfloor_{\{\ell_1, \dots, \ell_n\}}^\sigma$  denotes a proof  $\langle V, E, \Gamma \rangle$  s.t.

$$V = V' \cup \{v\} \quad \Gamma = \Gamma' \sigma \cup \{\ell\} \quad E = E' \cup \left\{ \rho(\psi') \xrightarrow[\sigma]{\{\ell_1, \dots, \ell_n\}} v \right\}$$

where  $v$  is a new (factoring) node, and  $\rho(\varphi)$  denotes the root node of  $\varphi$ . □

### 3 The Propositional Algorithm

RPI (formally defined in [6]) removes *irregularities*, which are resolution inferences deriving a node  $\eta$  when the resolved literal occurs as the pivot of another inference located below in the path from  $\eta$  to the root of the proof. In the worst case, regular resolution proofs can be exponentially bigger than irregular ones, but RPI takes care of regularizing the proof only partially, removing inferences only when this does not enlarge the proof.

RPI traverses the proof twice. On the first traversal (bottom-up), it computes and stores for each node a set of *safe literals*: literals that are resolved in all paths from the node to the root of the proof or that occur in the root clause. If one of the node's resolved literals belongs to the set of safe literals, then it is possible to *regularize* the node by replacing it by the parent containing the safe literal. To do this replacement efficiently, the replacement is postponed by marking the other parent as a **deletedNode**. Then, on a single second traversal (top-down),

<sup>1</sup>This is referred to as “binary resolution” elsewhere, with the understanding that “binary” refers to the number of resolved literals, rather than the number of premises of the inference rule.

$$\begin{array}{c}
\frac{\eta_1: \vdash P(w, x) \quad \eta_2: P(w, x) \vdash Q(c)}{\eta_3: \vdash Q(c)} \quad \eta_4: Q(c) \vdash P(a, x) \quad \frac{\eta_1: \vdash P(w, x) \quad \eta_6: P(y, b) \vdash}{\psi': \perp} \\
\frac{\eta_6: P(y, b) \vdash \quad \eta_5: \vdash P(a, x)}{\psi: \perp}
\end{array}$$

Figure 1: A proof  $\psi$  (left), and a regularized proof  $\psi'$  (right).

regularization is performed: any node that has a parent node marked as a `deletedNode` is replaced by its other parent.

The **RPI** and the **RP** algorithms differ from each other mainly in the computation of the safe literals of a node that has many children. While the former returns the intersection, the latter returns the empty set. Moreover, while in **RPI** the safe literals of the root node contain all the literals of the root clause, in **RP** the root node's set of safe literals is always empty.

## 4 Lifting to First-Order

**Example 4.1.** Consider the left proof  $\psi$  in Figure 1. When computed as in the propositional case, the safe literals for  $\eta_3$  are  $\{Q(c), P(a, x)\}$ . As neither of  $\eta_3$ 's resolved literals is syntactically equal to a safe literal, the propositional **RPI** algorithm would not change  $\psi$ . However,  $\eta_3$ 's left resolved literal  $P(w, x) \in \eta_1$  is unifiable with the safe literal  $P(a, x)$ . Regularizing  $\eta_3$ , by deleting the edge between  $\eta_2$  and  $\eta_3$  and replacing  $\eta_3$  by  $\eta_1$ , leads to further deletion of  $\eta_4$  (because it is not resolvable with  $\eta_1$ ) and finally to the much shorter proof  $\psi'$  in Figure 1.

Unlike in the propositional case, where a resolved literal must be syntactically equal to a safe literal for regularization to be possible, Example 4.1 suggests that, in the first-order case, it might suffice that the resolved literal be unifiable with a safe literal. However, there are cases where mere unifiability is not enough and greater care is needed: e.g., when  $\eta_1: \vdash P(a, c)$  and  $\eta_2: P(a, c) \vdash Q(c)$  in Example 4.1. One way to prevent these cases is to require the resolved literal to be not only unifiable but subsume a safe literal. A slight modification to the concept of safe literals, which takes into account the unifications that occur on the paths from a node to the root, results in a weaker (and better) requirement.

**Definition 4.1.** The set of *safe literals* for a node  $\eta$  in a proof  $\psi$  with root clause  $\Gamma$ , denoted  $\mathcal{S}(\eta)$ , is such that  $\ell \in \mathcal{S}(\eta)$  if and only if  $\ell \in \Gamma$  or for all paths from  $\eta$  to the root of  $\psi$  there is an edge  $v_1 \xrightarrow[\sigma]{\ell'} v_2$  with  $\ell'\sigma = \ell$ .

As in the propositional case, safe literals can be computed in a bottom-up traversal of the proof. Initially, at the root, the safe literals are exactly the literals that occur in the root clause. As we go up, the safe literals  $\mathcal{S}(\eta')$  of a parent node  $\eta'$  of  $\eta$  where  $\eta' \xrightarrow[\sigma]{\ell} \eta$  is set to  $\mathcal{S}(\eta) \cup \{\ell\sigma\}$ . Note that we apply the substitution to the resolved literal before adding it to the set of safe literals (cf. Algorithm 2, lines 8 and 10). In other words, in the first-order case, the set of safe literals has to be a set of *instantiated* resolved literals.

In the modified case of Example 4.1, computing safe literals as in Definition 4.1 would result in  $\mathcal{S}(\eta_3) = \{Q(c), P(a, b)\}$ , where clearly the pivot  $P(a, c)$  in  $\eta_1$  is not safe. A generalization of this requirement, which can be thought of a *necessary* condition, is Definition 4.2.

**Definition 4.2.** Let  $\eta$  be a node with safe literals  $\mathcal{S}(\eta)$  and parents  $\eta_1$  and  $\eta_2$ , assuming without loss of generality,  $\eta_1 \xrightarrow[\sigma_1]{\ell_1} \eta$ . The node  $\eta$  is said to be *pre-regularizable* in the proof  $\psi$  if  $\ell_1\sigma_1$  matches a safe literal  $\ell^* \in \mathcal{S}(\eta)$ .

$$\begin{array}{c}
\frac{\eta_1: P(u, v) \vdash Q(f(a, v), u) \quad \eta_2: Q(f(a, x), y), Q(t, x) \vdash Q(f(a, z), y)}{\eta_3: P(u, v), Q(t, v) \vdash Q(f(a, z), u)} \quad \eta_4: \vdash Q(r, s) \\
\frac{\eta_6: \vdash P(c, d) \quad \eta_5: P(u, v) \vdash Q(f(a, z), u)}{\eta_7: \vdash Q(f(a, z), c)} \\
\frac{\eta_8: Q(f(a, e), c) \vdash \quad \eta_7: \vdash Q(f(a, z), c)}{\psi: \perp}
\end{array}$$

Figure 2: An example where pre-regularizability is not sufficient.

**Example 4.2.** *Satisfying the pre-regularizability is not sufficient. Consider the proof  $\psi$  in Figure 2. After collecting the safe literals,  $\mathcal{S}(\eta_3) = \{\neg Q(r, v), \neg P(c, d), Q(f(a, e), c)\}$ .  $\eta_3$ 's pivot  $Q(f(a, v), u)$  matches the safe literal  $Q(f(a, e), c)$ . Attempting to regularize  $\eta_3$  would lead to the removal of  $\eta_2$ , the replacement of  $\eta_3$  by  $\eta_1$  and the removal of  $\eta_4$  (because  $\eta_1$  does not contain the pivot required by  $\eta_5$ ), with  $\eta_5$  also being replaced by  $\eta_1$ . Then resolution between  $\eta_1$  and  $\eta_6$  results in  $\eta_7'$ , which cannot be resolved with  $\eta_8$ , as shown below.*

$$\begin{array}{c}
\frac{\eta_6: \vdash P(c, d) \quad \eta_1: P(u, v) \vdash Q(f(a, v), u)}{\eta_7': \vdash Q(f(a, d), c)} \\
\frac{\eta_8: Q(f(a, e), c) \vdash \quad \eta_7': \vdash Q(f(a, d), c)}{\psi': ??}
\end{array}$$

$\eta_1$ 's literal  $Q(f(a, v), u)$ , which would be resolved with  $\eta_8$ 's literal, was changed to  $Q(f(a, d), c)$  due to the resolution between  $\eta_1$  and  $\eta_6$ .

Thus we additionally require that the following condition be satisfied, which ensures that the remainder of the proof does not expect a variable in  $\eta_1$  to be unified to different values simultaneously. This property is not necessary in the propositional case, as the literals of the replacement node do not change lower in the proof.

**Definition 4.3.** Let  $\eta$  be pre-regularizable, with safe literals  $\mathcal{S}(\eta)$  and parents  $\eta_1$  and  $\eta_2$ , with clauses  $\Gamma_1$  and  $\Gamma_2$  respectively, assuming without loss of generality that  $\eta_1 \xrightarrow[\sigma_1]{\{\ell_1\}} \eta$  such that  $\ell_1\sigma_1$  matches a safe literal  $\ell^* \in \mathcal{S}(\eta)$ . The node  $\eta$  is said to be *strongly regularizable* in  $\psi$  if  $\Gamma_1\sigma_1 \sqsubseteq \mathcal{S}(\eta)$ .

The notion of *strongly regularizable* can be thought of as a *sufficient* condition. The longer version of this paper (available on the ArXiv [9]) discusses a conjectured weaker condition.

**Theorem 4.3.** *Let  $\psi$  be a proof with root clause  $\Gamma$  and  $\eta$  be a node in  $\psi$ . Let  $\psi^\dagger = \psi \setminus \{\eta\}$  and  $\Gamma^\dagger$  be the root of  $\psi^\dagger$ . If  $\eta$  is strongly regularizable, then  $\Gamma^\dagger \sqsubseteq \Gamma$ .*

*Proof.* By definition of strong regularizability,  $\eta$  is such that there is a node  $\eta'$  with clause  $\Gamma'$  and such that  $\eta' \xrightarrow[\sigma']{\{\ell'\}} \eta$  and  $\ell'\sigma'$  matches a safe literal  $\ell^* \in \mathcal{S}(\eta)$  and  $\Gamma'\sigma' \sqsubseteq \mathcal{S}(\eta)$ .

Firstly, in  $\psi^\dagger$ ,  $\eta$  has been replaced by  $\eta'$ . Since  $\Gamma'\sigma' \sqsubseteq \mathcal{S}(\eta)$ , by definition of  $\mathcal{S}(\eta)$ , every literal  $\ell$  in  $\Gamma'$  either subsumes a single literal that occurs as a pivot on every path from  $\eta$  to the root in  $\psi$  (and hence on every new path from  $\eta'$  to the root in  $\psi^\dagger$ ) or subsumes literals  $\ell\sigma_1, \dots, \ell\sigma_n$  in  $\Gamma$ . In the former case,  $\ell$  is resolved away in the construction of  $\psi^\dagger$  (by contracting the descendants of  $\ell$  with the pivots in each path). In the latter case, the literal  $\ell\sigma_k$  ( $1 \leq k \leq n$ ) in  $\Gamma$  is a descendant of  $\ell$  through a path  $k$  and the substitution  $\sigma_k$  is the composition of all substitutions on this path. When  $\eta$  is replaced by  $\eta'$ , two things may happen to  $\ell\sigma_k$ . If the path  $k$  does not go through  $\eta$ ,  $\ell\sigma_k$  remains unchanged (i.e.  $\ell\sigma_k \in \Gamma^\dagger$  unless the path  $k$  ceases

```

input : A first-order proof  $\psi$ 
output: A possibly less-irregular first-order proof  $\psi'$ 
1  $\psi' \leftarrow \psi$ ;
2 traverse  $\psi'$  bottom-up and foreach node  $\eta$  in  $\psi'$  do
3   if  $\eta$  is a resolvent node then
4     setSafeLiterals( $\eta$ ) ;
5     regularizeIfPossible( $\eta$ )
6  $\psi' \leftarrow \text{fix}(\psi')$  ;
7 return  $\psi'$ ;

```

**Algorithm 1:** FORPI.

```

input : A first-order resolution node  $\psi$ 
output: nothing (but the node  $\psi$  gets a set of safe literals)
1 if  $\psi$  is a root with no children then  $\mathcal{S}(\psi) \leftarrow \psi.\text{clause}$  ;
2 else
3   foreach  $\psi' \in \psi.\text{children}$  do
4     if  $\psi'$  is marked as regularized then safeLiteralsFrom( $\psi'$ )  $\leftarrow \mathcal{S}(\psi')$  ;
5     else if  $\psi' = \psi \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \psi_R$  for some  $\psi_R$  then safeLiteralsFrom( $\psi'$ )  $\leftarrow \mathcal{S}(\psi') \cup \{\ell_R \sigma_R\}$  ;
6     else if  $\psi' = \psi_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \psi$  for some  $\psi_L$  then safeLiteralsFrom( $\psi'$ )  $\leftarrow \mathcal{S}(\psi') \cup \{\ell_L \sigma_L\}$  ;
7    $\mathcal{S}(\psi) \leftarrow \bigcap_{\psi' \in \psi.\text{children}} \text{safeLiteralsFrom}(\psi')$ 

```

**Algorithm 2:** setSafeLiterals for FORPI.

to exist in  $\psi^\dagger$ ). If the path  $k$  goes through  $\eta$ , the literal is changed to  $\ell \sigma_k^\dagger$ , where  $\sigma_k^\dagger$  is such that  $\sigma_k = \sigma' \sigma_k^\dagger$ .

Secondly, when  $\eta$  is replaced by  $\eta'$ , the edge from  $\eta$ 's other parent  $\eta''$  to  $\eta$  ceases to exist in  $\psi^\dagger$ . Consequently, any literal  $\ell$  in  $\Gamma$  that is a descendant of a literal  $\ell''$  in the clause of  $\eta''$  through a path via  $\eta$  will not belong to  $\Gamma^\dagger$ .

Thirdly, a literal from  $\Gamma$  that descends neither from  $\eta'$  nor from  $\eta''$  either remains unchanged in  $\Gamma^\dagger$  or, if the path to the node from which it descends ceases to exist in the construction of  $\psi^\dagger$ , does not belong to  $\Gamma^\dagger$  at all.

Therefore, by the three facts above,  $\Gamma^\dagger \sigma' \subseteq \Gamma$ , and hence  $\Gamma^\dagger \subseteq \Gamma$ .  $\square$

## 5 Implementation

**FirstOrderRecyclePivotsWithIntersection** (FORPI) (cf. Algorithm 1) is a first-order generalization of the propositional RPI. FORPI traverses the proof in a bottom-up manner, storing for every node a set of safe literals. For a node  $\psi$ ,  $\mathcal{S}(\psi)$  is computed from the set of safe literals of its children (cf. Algorithm 2), similarly to the propositional case, but additionally applying unifiers to the resolved literals. If one of the node's resolved literals matches a literal in the set of safe literals, then it may be possible to regularize the node by replacing it by one of its parents.

In the first-order case, we additionally check for strong regularizability (cf. lines 2 and 6 of Algorithm 3). Similarly to RPI, instead of replacing the irregular node by one of its parents immediately, its other parent is marked as a **deletedNode**, as shown in Algorithm 3. As in the propositional case, fixing of the proof is postponed to another (single) traversal, as regularization proceeds top-down and only nodes below a regularized node may require fixing. During fixing, the irregular node is actually replaced by the parent that is not marked as **deletedNode**. During proof fixing, factoring inferences can be applied, in order to compress the proof further.

```

input : A node  $\psi = \psi_L \odot_{\ell_L \ell_R}^{\sigma_L \sigma_R} \psi_R$ 
output: nothing (but the proof containing  $\psi$  may be changed)
1 if  $\exists \sigma$  and  $\ell \in \mathcal{S}(\psi)$  such that  $\ell = \ell_R \sigma_R \sigma$  then
2   if  $\psi_R \sigma_R \sigma \subseteq \mathcal{S}(\psi)$  then
3     mark  $\psi_L$  as deletedNode ;
4     mark  $\psi$  as regularized
5 else if  $\exists \sigma$  and  $\ell \in \mathcal{S}(\psi)$  such that  $\ell = \ell_L \sigma_L \sigma$  then
6   if  $\psi_L \sigma_L \sigma \subseteq \mathcal{S}(\psi)$  then
7     mark  $\psi_R$  as deletedNode ;
8     mark  $\psi$  as regularized

```

**Algorithm 3:** regularizeIfPossible for FORPI.

## 6 Experiments

A prototype version of FORPI has been implemented in the functional programming language Scala as part of the **Skeptik** library. This library includes an implementation of **GFOLU** [8]. Note that by implementing the algorithms in this library, we have a relative guarantee that the compressed proofs are correct, as in **Skeptik** every inference rule (e.g. resolution, factoring) is implemented as a small class (each at most 178 lines of code that is assumed correct) with a constructor that checks whether the conditions for the application of the rule are met, thereby preventing the creation of objects representing incorrect proof nodes (i.e. unsound inferences). We only need to check that the root clause of the compressed proof is equal to or stronger than the root clause of the input proof and that the set of axioms used in the compressed proof is a subset of the set of axioms used in the input proof.

FORPI was evaluated on the same 308 proofs generated by SPASS to evaluate **GFOLU**, as well as 2280 (the same number of problems initially given to SPASS) randomly generated proofs. Proof lengths varied from 3 to 700, while the number of resolutions in a proof ranged from 1 to 368 (1-32 resolutions for proofs in the TPTP data set; 1-368 resolutions for the proofs in the random data set). The same laptop was used to perform proof compression. Details and a discussion regarding the realism reflected in the random proofs are in the next subsection. The proofs are available at <https://github.com/jgorzny/Skeptik>.

### 6.1 Proof Generation

Additional proofs were generated by the following procedure: start with a root node whose conclusion is  $\perp$ , and make two premises  $\eta_1$  and  $\eta_2$  using a randomly generated literal such that the desired conclusion is the result of resolving  $\eta_1$  and  $\eta_2$ . For each node  $\eta_i$ , determine the inference rule used to make its conclusion: with probability  $p = 0.9$ ,  $\eta_i$  is the result of a resolution, otherwise it is the result of factoring.

Literals are generated by uniformly choosing a number from  $\{1, \dots, k, k+1\}$  where  $k$  is the number of predicates generated so far; if the chosen number  $j$  is between 1 and  $k$ , the  $j$ -th predicate is used; otherwise, if the chosen number is  $k+1$ , a new predicate with a new random arity (at most four) is generated and used. Each argument is a constant with probability  $p = 0.7$  and a complex term (i.e. a function applied to other terms) otherwise; functions are generated similarly to predicates.

If a node  $\eta$  should be the result of a resolution, then with probability  $p = 0.2$  we generate a left parent  $\eta_\ell$  and a right parent  $\eta_r$  for  $\eta$  (i.e.  $\eta = \eta_\ell \odot \eta_r$ ) having a common parent  $\eta_c$  (i.e.  $\eta_\ell = (\eta_\ell)_\ell \odot \eta_c$  and  $\eta_r = \eta_c \odot (\eta_r)_r$ , for some newly generated nodes  $(\eta_\ell)_\ell$  and  $(\eta_r)_r$ ). The common parent ensures that also non-tree-like DAG proofs are generated.

This procedure is recursively applied to the generated parent nodes. Each parent of a



Algorithm	# of Proofs Compressed			# of Removed Nodes		
	TPTP	Random	Both	TPTP	Random	Both
GFOLU(p)	55 (17.9%)	817 (35.9%)	872 (33.7%)	107 (4.8%)	17,769 (4.5%)	17,876 (4.5%)
FORPI(p)	23 (7.5%)	666 (29.2%)	689 (26.2%)	36 (1.6%)	28,904 (7.3%)	28,940 (7.3%)
GFOLU(FORPI(p))	55 (17.9%)	1303 (57.1%)	1358 (52.5%)	120 (5.4%)	48,126 (12.2%)	48,246 (12.2%)
FORPI(GFOLU(p))	23 (7.5%)	1302 (57.1%)	1325 (51.2%)	120 (5.4%)	48,434 (12.3%)	48,554 (12.3%)
Best	59 (19.2%)	1303 (57.1%)	1362 (52.5%)	120 (5.4%)	55,530 (14.1%)	55,650 (14.0%)

Table 1: Number of proofs compressed and number of overall nodes removed.

Algorithm	First-Order Compression		Algorithm	Propositional Compression [3]
	All	Compressed Only		
GFOLU(p)	4.5%	13.5%	LU(p)	7.5%
FORPI(p)	6.2%	23.2%	RPI(p)	17.8%
GFOLU(FORPI(p))	10.6%	23.0%	(LU(RPI(p)))	21.7%
FORPI(GFOLU(p))	11.1%	21.5%	(RPI(LU(p)))	22.0%
Best	12.6%	24.4%	Best	22.0%

Table 2: Mean compression results.

resolution has each of its terms not contained in the pivot replaced by a fresh variable with probability  $p = 0.7$ . At each recursive call, the additional minimum height required for the remainder of the branch is decreased by one with probability  $p = 0.5$ . Thus if each branch always decreases the additional required height, the proof has height equal to the initial minimum value. The process stops when every branch is required to add a subproof of height zero or after a timeout is reached. In any case, the topmost generated node for each branch is generated as an axiom node.

The minimum height was set to 7 (which is the minimum number of nodes in an irregular proof plus one) and the timeout was set to 300 seconds (the same timeout allowed for SPASS). The probability values used in the random generation were carefully chosen to produce random proofs similar in shape to the real proofs obtained by SPASS. For instance, the probability of a new node being a resolution (respectively, factoring) is approximately the same as the frequency of resolutions (respectively, factorings) observed in the real proofs produced by SPASS.

## 6.2 Results

For each proof  $\psi$ , we measured the time needed to compress the proof ( $t(\psi)$ ) and the compression ratio  $((|\psi| - |\alpha(\psi)|)/|\psi|)$  where  $|\psi|$  is the number of resolutions in the proof, and  $\alpha(\psi)$  is the result of applying a compression algorithm or some composition of FORPI and GFOLU. Note that we consider only the number of resolutions in order to compare the results of these algorithms to their propositional variants (where factoring is implicit). Moreover, factoring could be made implicit within resolution inferences even in the first-order case and we use explicit factoring only for technical convenience.

Table 1 summarizes the results of FORPI and its combinations with GFOLU. The first set of columns describes the percentage of proofs that were compressed by each compression algorithm. The algorithm ‘Best’ runs both combinations of GFOLU and FORPI and returns the shortest proof output by either of them. The total number of proofs is  $308 + 2280 = 2588$  and the total number of resolution nodes is  $2,249 + 393,883 = 396,132$ . The percentages in the last three columns are computed by  $(\sum_{\psi \in \Psi} |\psi| - \sum_{\psi \in \Psi} |\alpha(\psi)|) / (\sum_{\psi \in \Psi} |\psi|)$  for each data set  $\Psi$  (TPTP, Random, or Both: the union of the other two data sets). The use of both algorithms allows at least an additional 17.5% of proofs to be compressed. Furthermore, the use of both algorithms removes almost twice as many nodes than any single algorithm. Only nine proofs from the TPTP data



set were compressed by FORPI, reducing the number of resolutions by at least one and at most three. Given the size of the TPTP proofs, it is unsurprising that few are compressed: small proofs are a priori less likely to contain irregularities. However, 252 (0.11%) of the randomly generated proofs achieved some compression using only FORPI.

Table 2 compares the results of FORPI and its combinations with GFOLU (on first-order proofs) with their propositional variants (on propositional proofs) as evaluated in [3]. The first column describes the mean compression ratio for each algorithm including proofs that were not compressed by the algorithm, while the second column calculates the mean compression ratio considering only compressed proofs. It is unsurprising that the first column is lower than the propositional mean for each algorithm: there are stricter requirements to apply these algorithms to first-order proofs. In particular, additional properties must be satisfied before a unit can be lowered, or before a pivot can be recycled. On the other hand, when first-order proofs are compressed, the compression ratios of the first-order algorithms are on par with or better than their propositional counterparts.

Figure 3 (a) is a scatter plot comparing the number of resolutions of the input proof against the number of resolutions in the compressed proof for each algorithm. The results on the TPTP data are magnified in the sub-plot. For the randomly generated proofs (points outside of the sub-plot), it is often the case that the compressed proof is significantly shorter than the input proof. Interestingly, GFOLU appears to reduce the number of resolutions by a linear factor in many cases. This is likely due to a linear growth in the number of non-interacting irregularities (i.e. irregularities for which the lowered units share no common literals with any other sub-proofs), which leads to a linear number of nodes removed.

Figure 3 (b) is a scatter plot comparing the size of compression obtained by applying FORPI before GFOLU versus GFOLU before FORPI. Data obtained from the TPTP data set is marked in red; the remaining points are obtained from randomly generated proofs. Points that lie on the diagonal line have the same size after each combination. There are 249 points beneath the line and 326 points above the line. Therefore, as in the propositional case [6], it is not a priori clear which combination will compress a proof more. Applying FORPI after GFOLU is more likely to maximize the likelihood of compression, and the achieved compression also tends to be larger.

Figure 3 (c) shows a plot comparing the difference between the cumulative number of resolutions of the first  $x$  input proofs and the cumulative number of resolutions in the first  $x$  proofs after compression (i.e. the cumulative number of *removed* resolutions). The TPTP data is displayed in the sub-plot; note that the lines for everything except FORPI largely overlap (since the values are almost identical; cf. Table 1). The data shows that the best approach is to try both combinations of FORPI and GFOLU and choose the best result.

Proof generation required approximately 110 minutes (including some cluster time), while the total time to apply both algorithms on all these proofs was just over 7.5 minutes, only 6.8% more time than generating proofs in the first place, on a simple laptop computer. All times include parsing time. These compression algorithms are still fast in the first-order case, and may simplify the proof considerably for a relatively small cost in time.

The use of FORPI alongside GFOLU allows at least an additional 17.5% of proofs to be compressed. Furthermore, the likelihood of compression is maximized by applying FORPI after GFOLU, and trying both compositions may be even more beneficial. On large proofs, thousands of nodes may be removed for only a small slow-down relative to the time required to generate the proof in the first place.

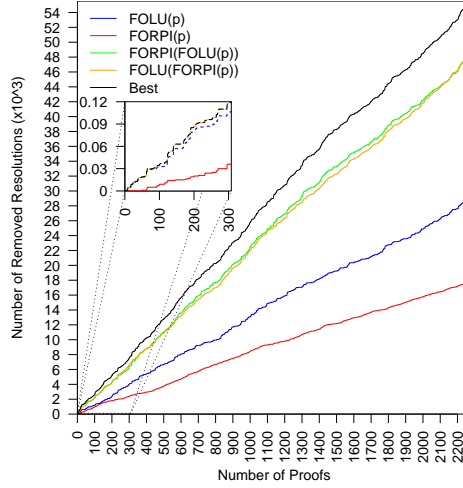
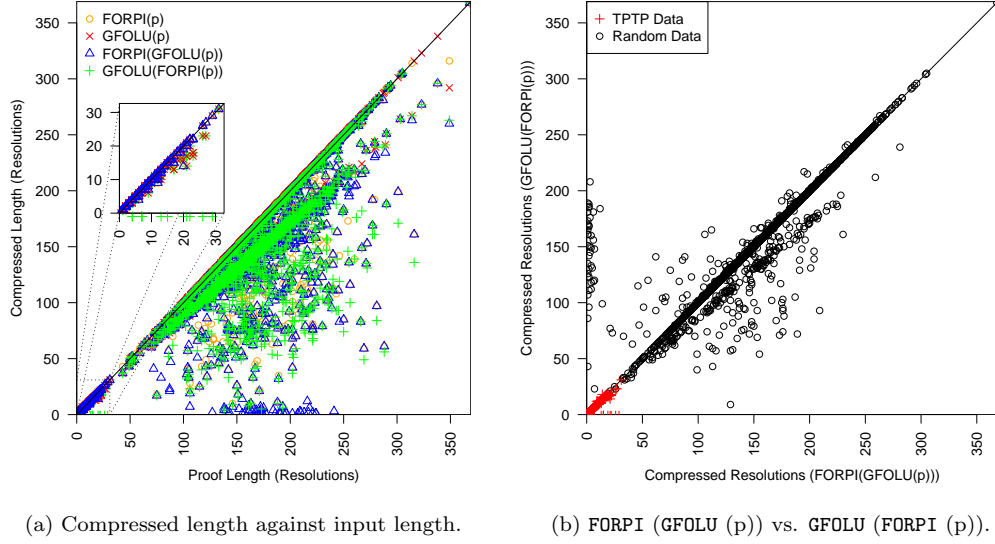


Figure 3: GFOLU &amp; FORPI Combination Results.

## 7 Conclusions and Future Work

The main contribution of this paper is the lifting of the propositional proof compression algorithm RPI to the first-order case. As indicated in Section 4, the generalization is challenging, because unification instantiates literals and, consequently, a node may be regularizable even if its resolved literals are not syntactically equal to any safe literal. Unification must be taken into account when collecting safe literals and marking nodes for deletion.

We evaluated the algorithm on two data sets, and the compression achieved by FORPI in

a short amount of time on this data set was compatible with our expectations and previous experience in the propositional level. The obtained results indicate that FORPI is a promising compression technique to be reconsidered when first-order theorem provers become capable of producing larger proofs. Although we carefully selected generation probabilities in accordance with frequencies observed in real proofs, it is important to note that randomly generated proofs may still differ from real proofs in shape and may be more or less likely to contain irregularities exploitable by our algorithm.

In this paper, for the sake of simplicity, we considered a pure resolution calculus without restrictions, refinements or extensions. However, in practice, theorem provers do use restrictions and extensions. It is conceptually easy to adapt the algorithm described here to many variations of resolution. For instance, a common extension of resolution is the splitting technique [24]. When splitting is used, each split sub-problem is solved by a separate refutation, and FORPI could be applied to each refutation independently.

It would be interesting to determine if proof compression could be applied during proof search, in order to improve the performance theorem provers. Additionally, it would be interesting to see if similar techniques can be applied to proofs in higher-order logics.

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