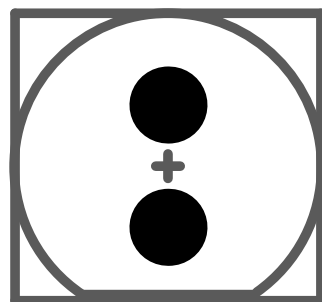
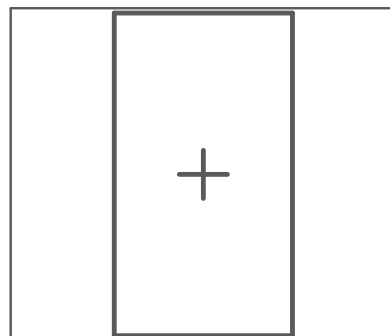


res1



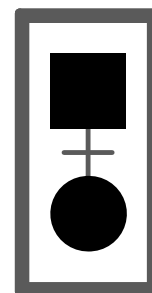
red_LED



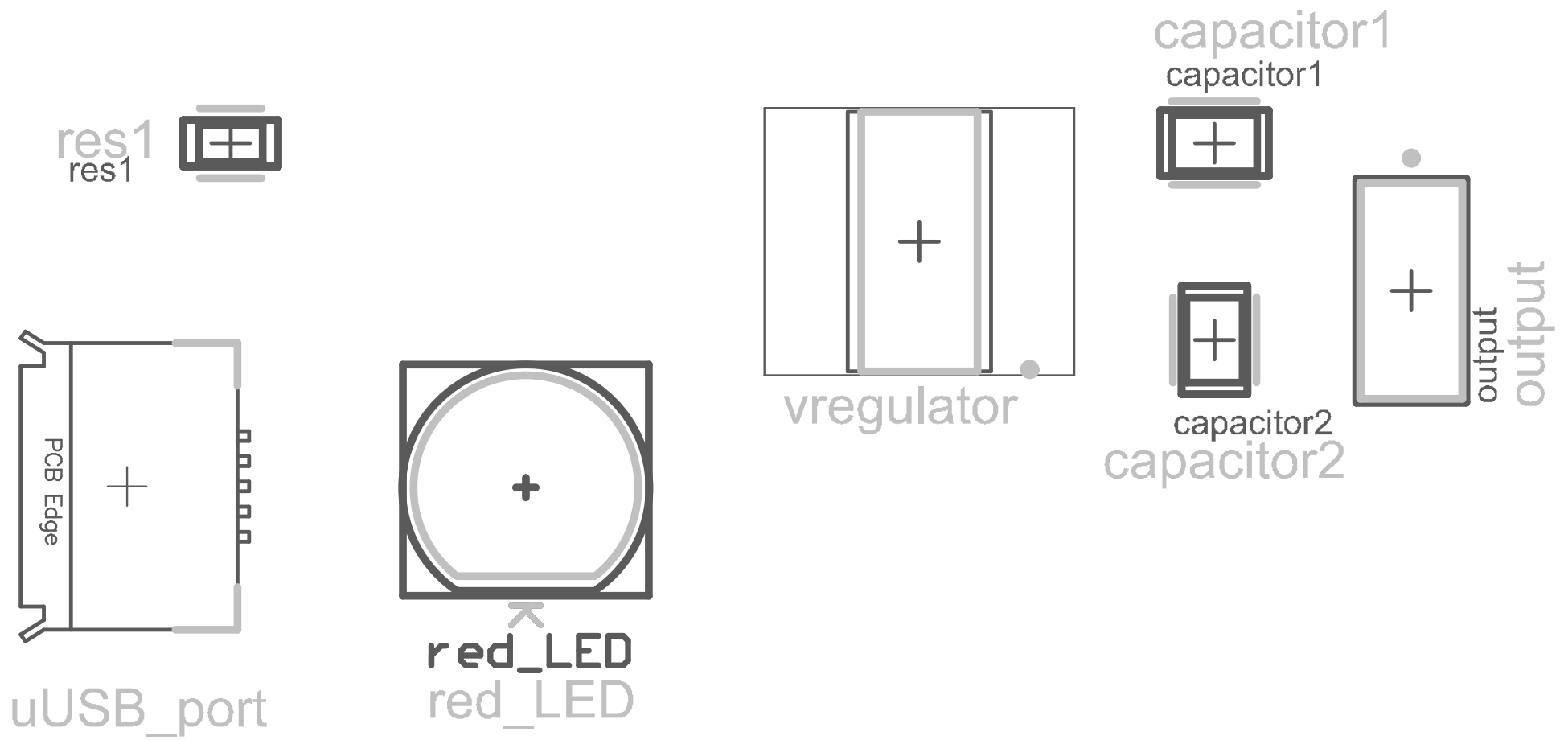
capacitor1

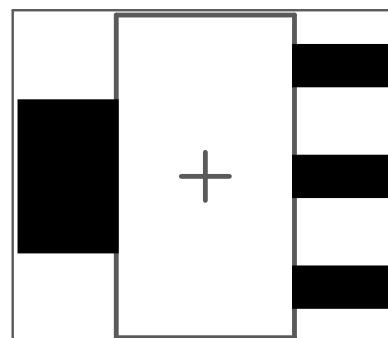
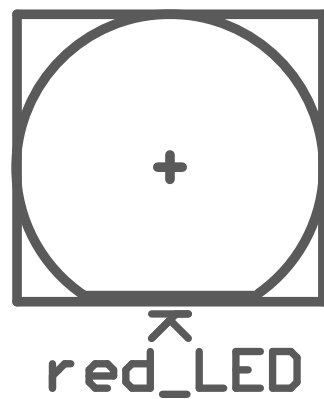
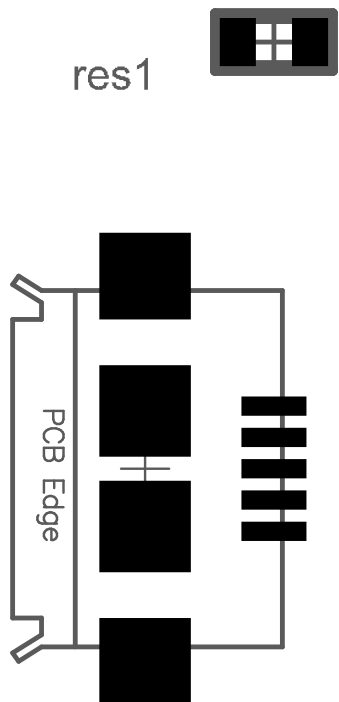


capacitor2



output

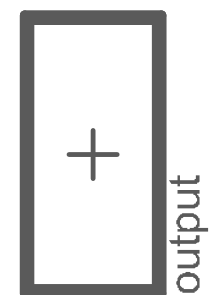


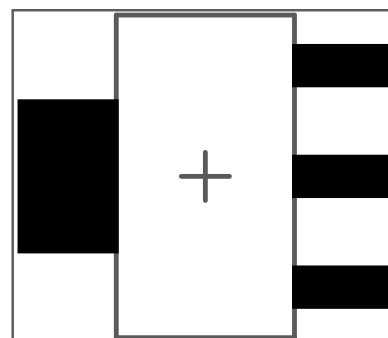
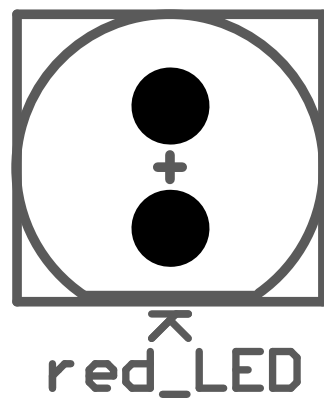
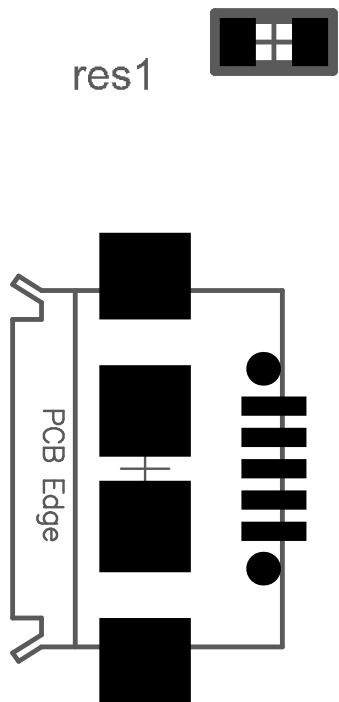


capacitor1



capacitor2

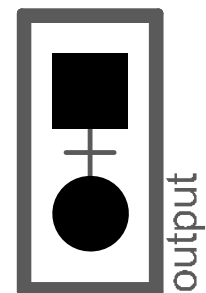
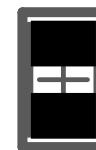


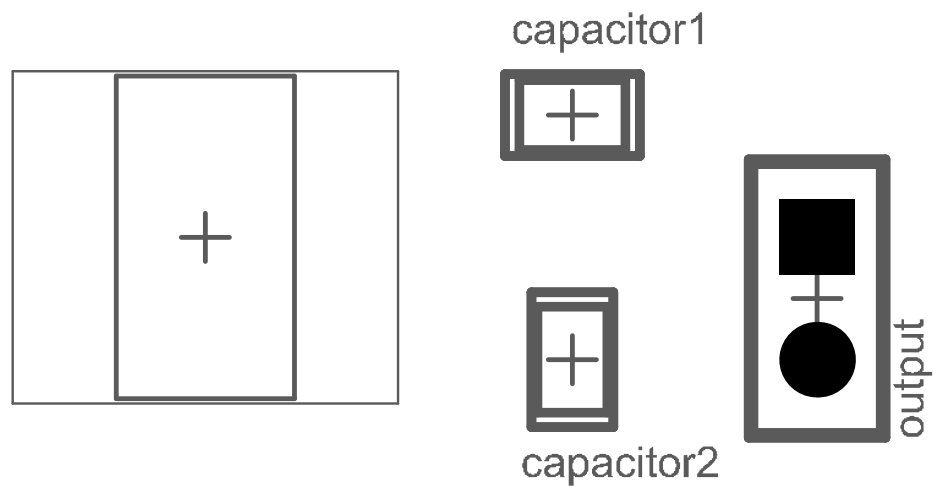
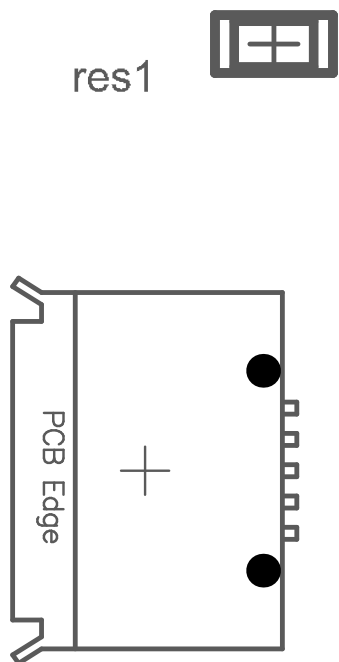


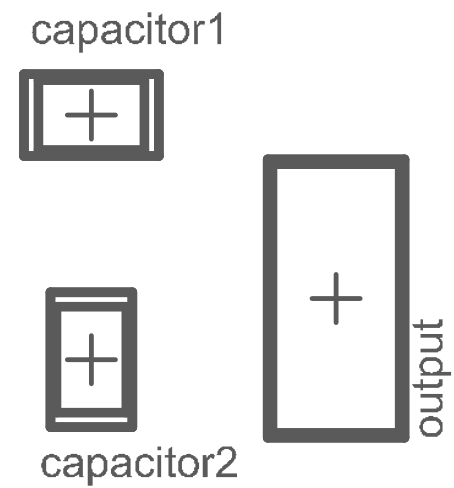
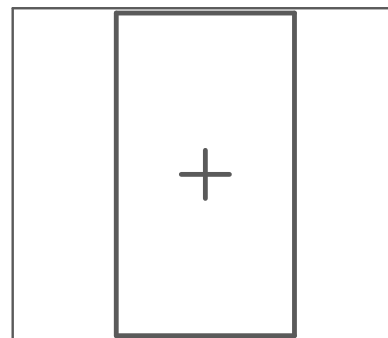
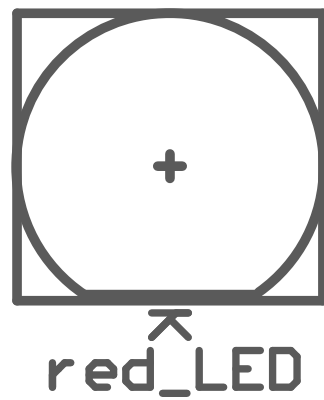
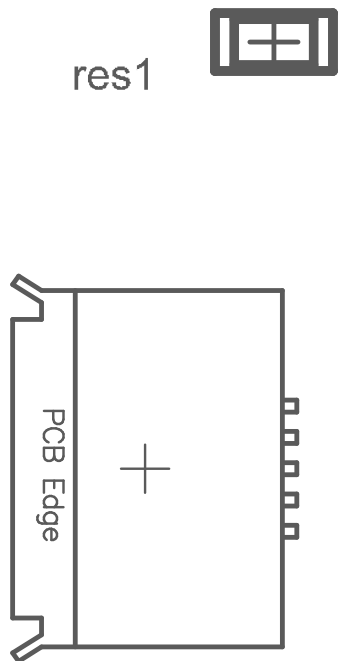
capacitor1

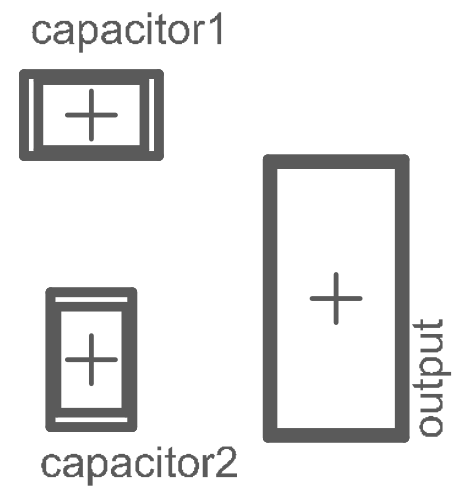
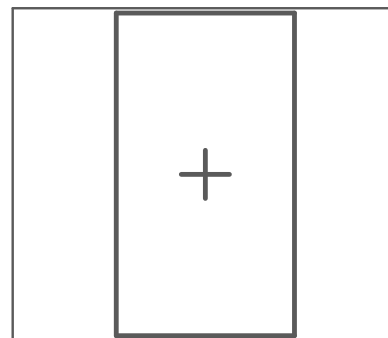
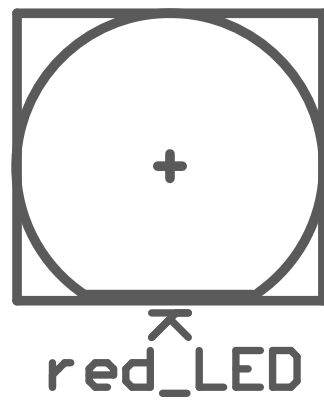
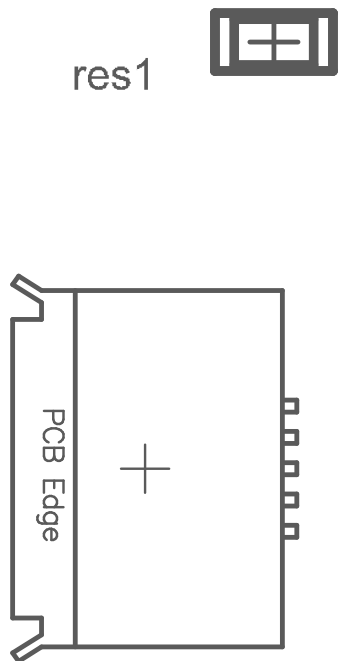


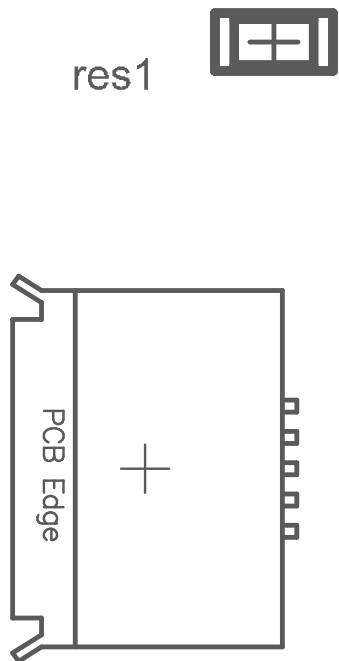
capacitor2



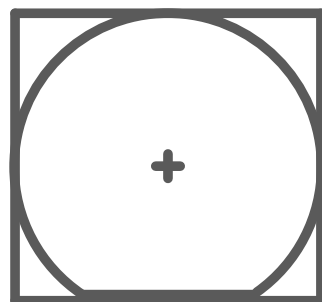




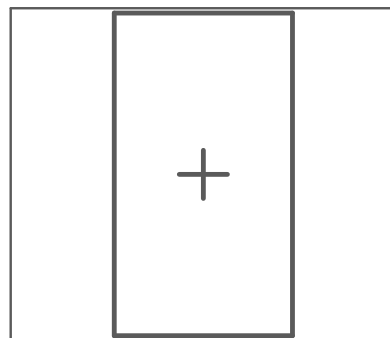




res1



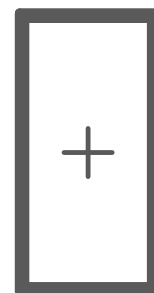
red_LED



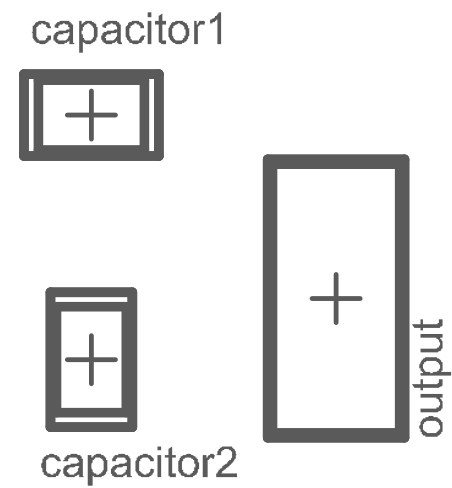
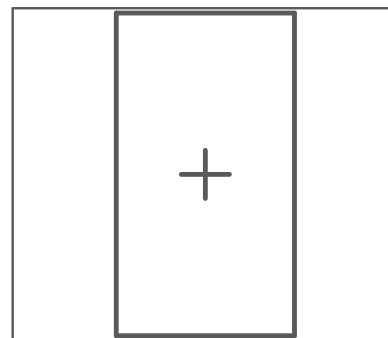
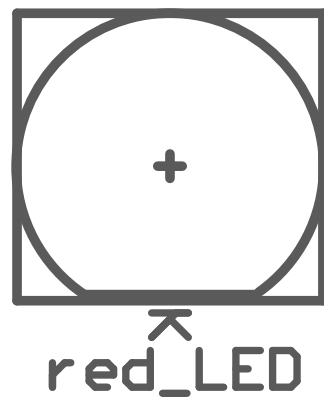
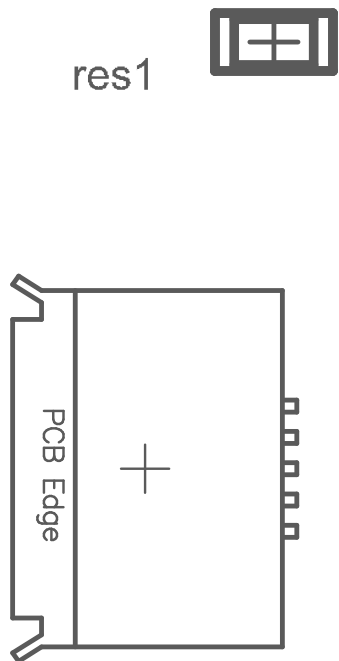
capacitor1

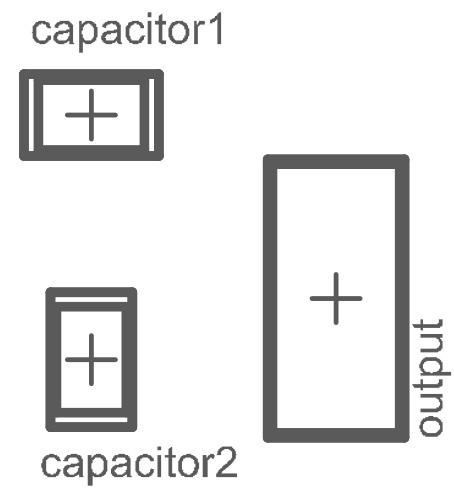
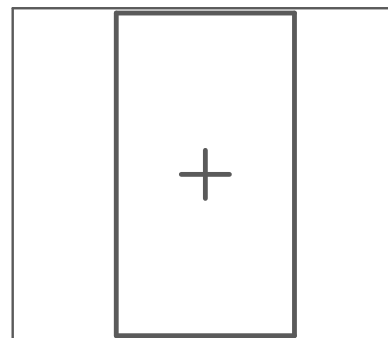
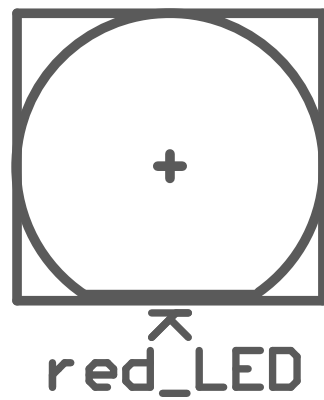
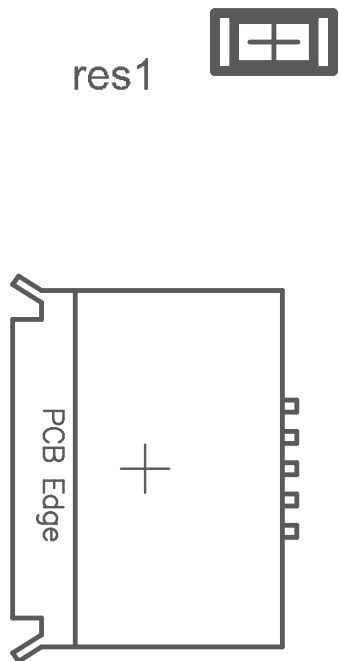


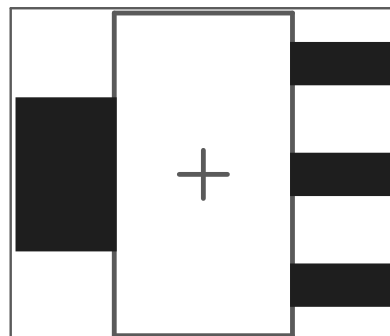
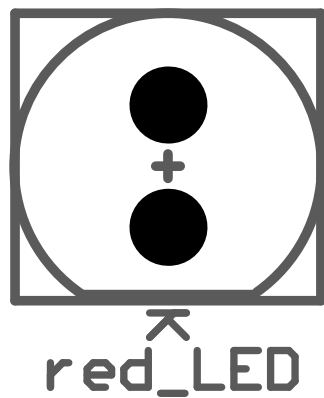
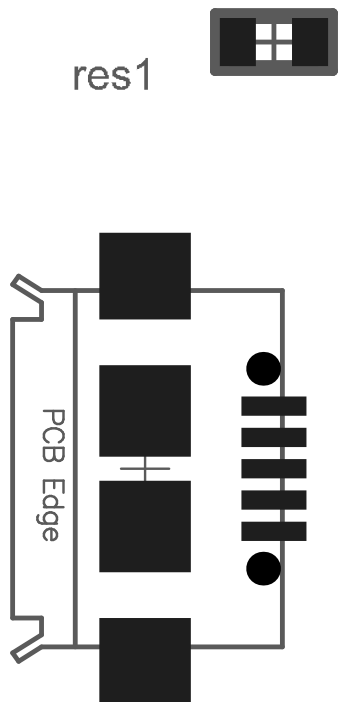
capacitor2



output



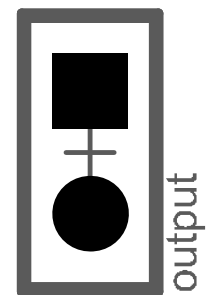


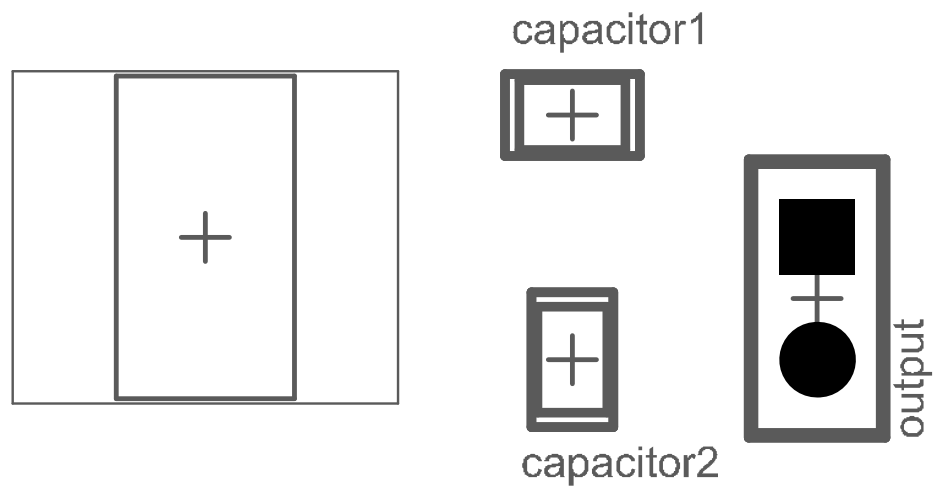
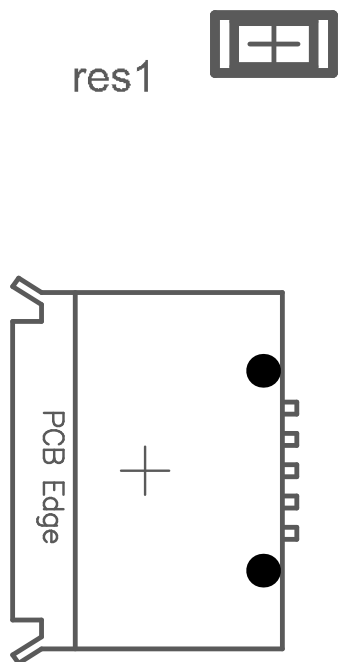


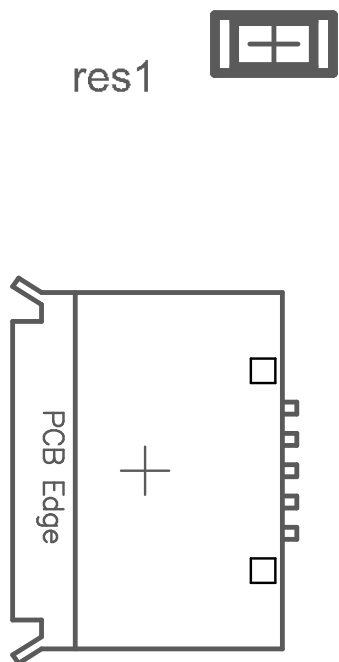
capacitor1



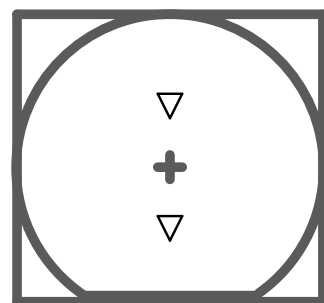
capacitor2



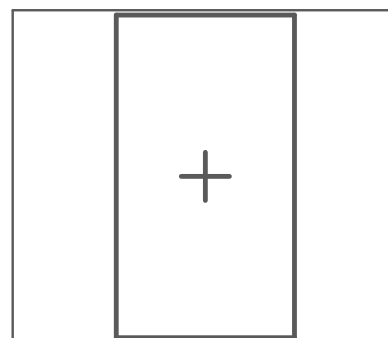




res1



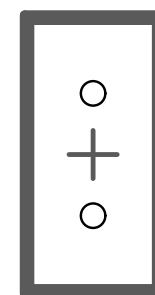
red_LED



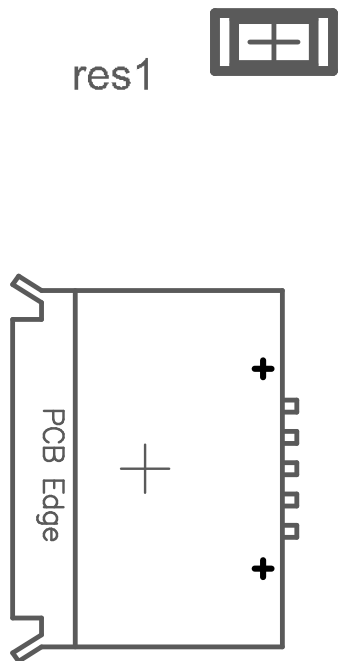
capacitor1



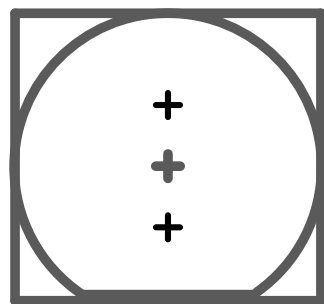
capacitor2



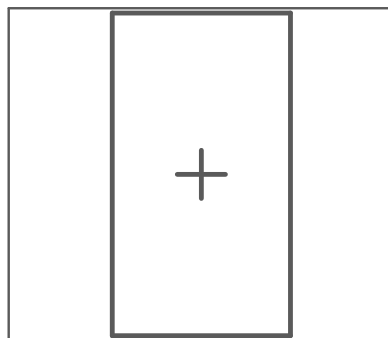
output



res1



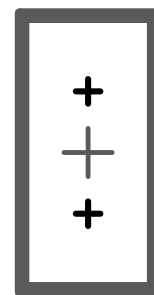
red_LED



capacitor1



capacitor2



output