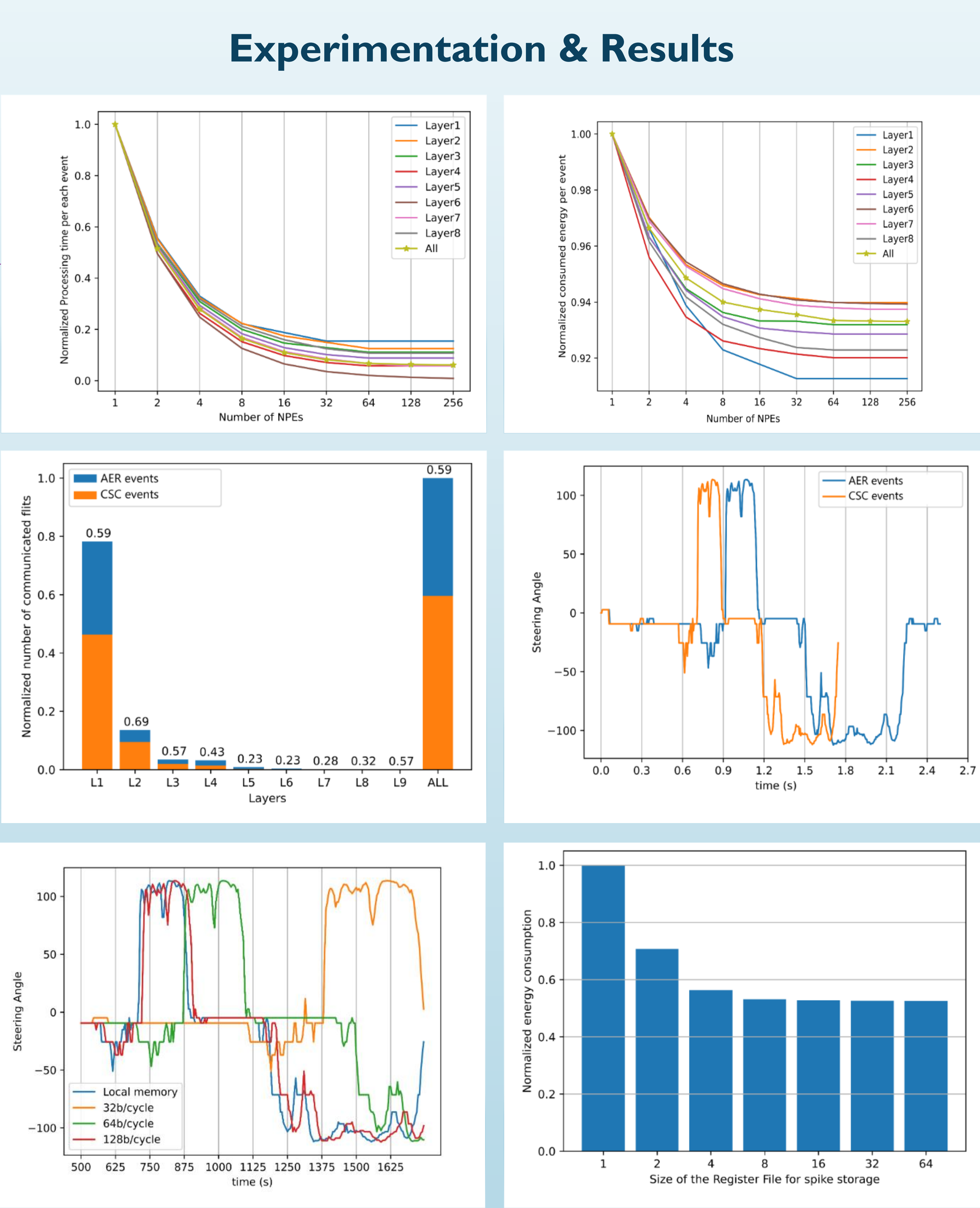
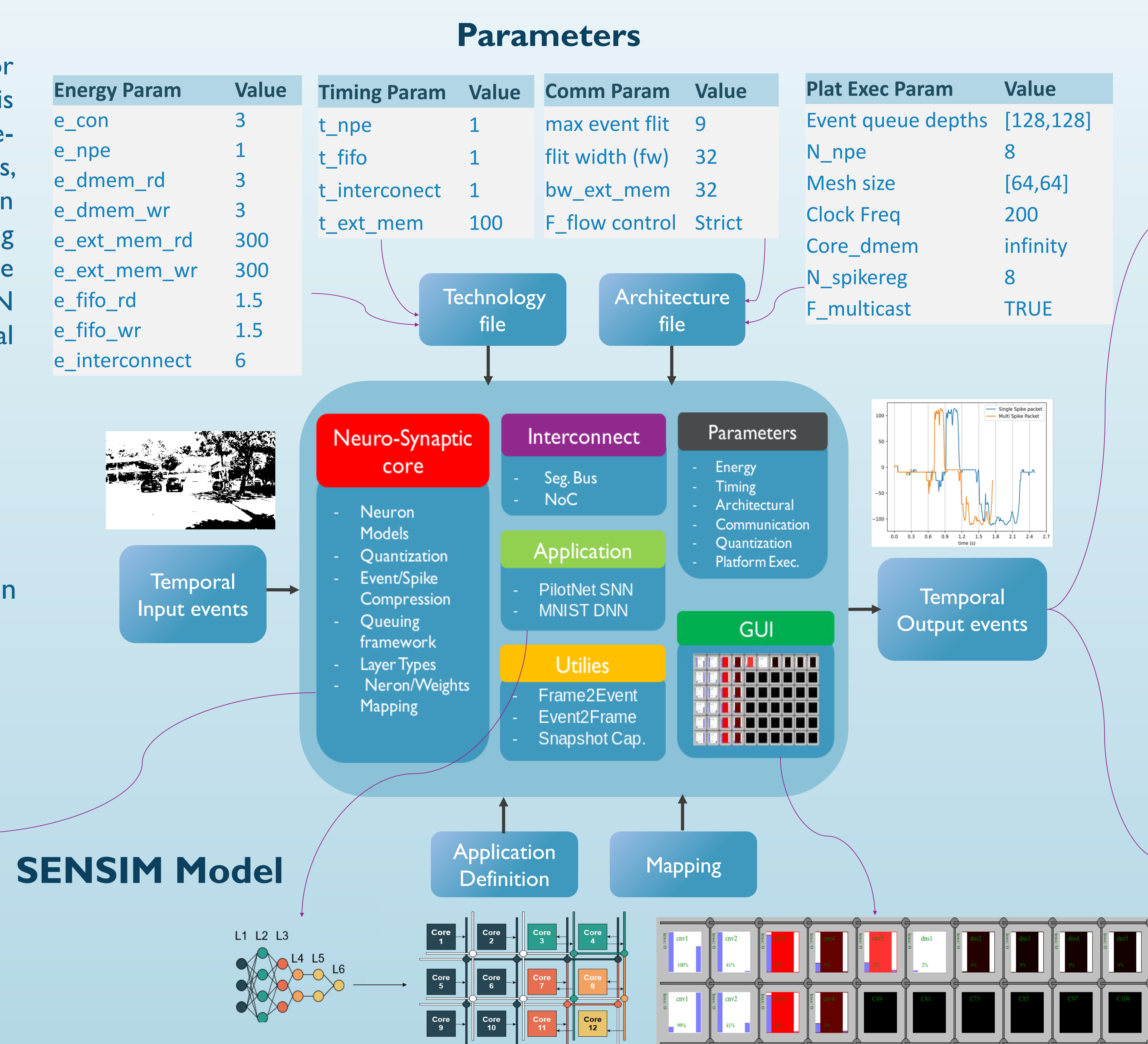
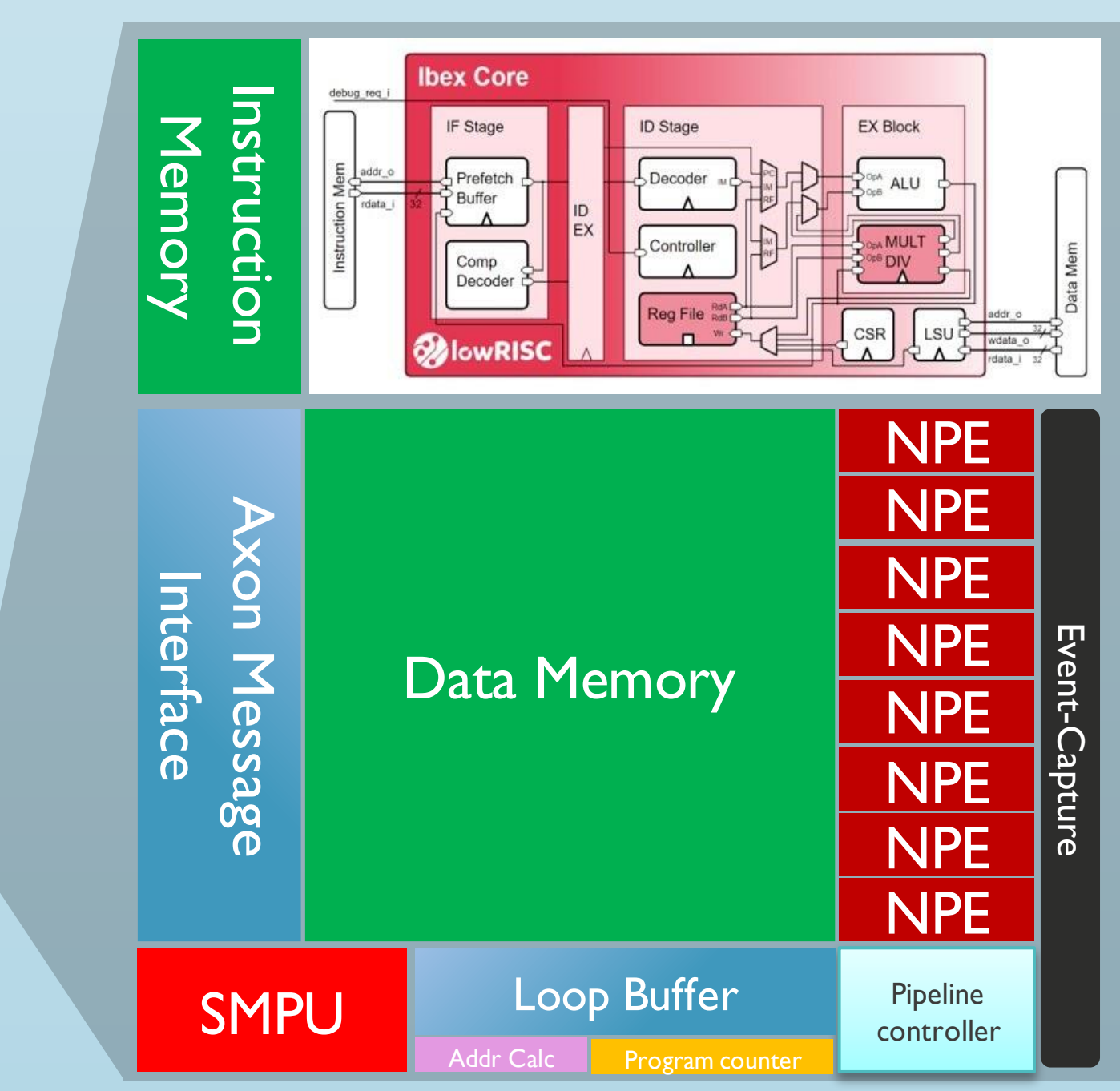


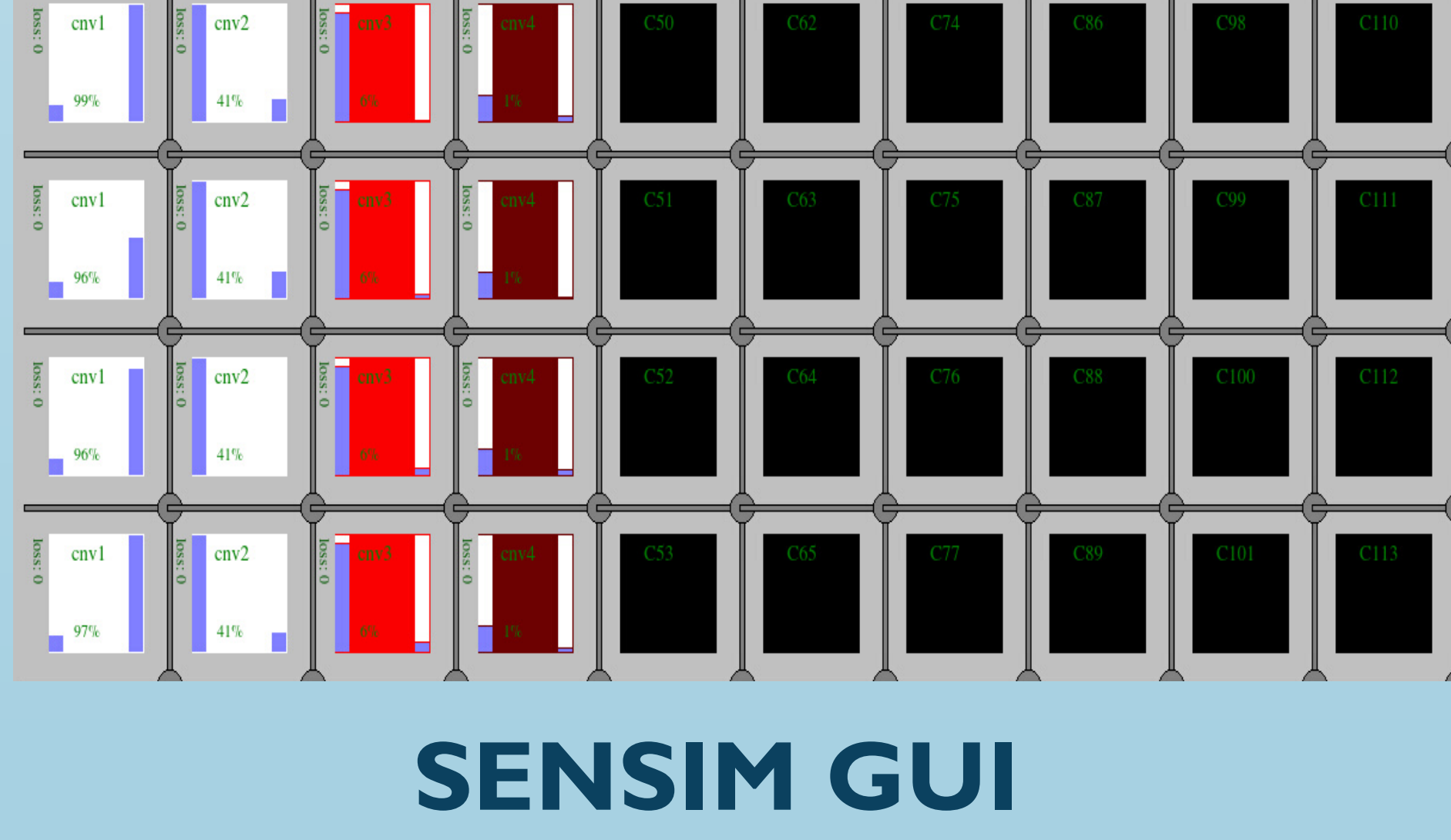
Abstract

In this paper, we present SENSIM, which is an open-source simulator designed specifically for the SENECA neuro-morphic processor. This simulator is unique in that it combines features from both hardware-specific and hardware-agnostic spiking neural network simulators, resulting in a hybrid event-driven and time-step-driven simulation approach. This allows for flexibility between accuracy and speed during different stages of simulation. Our work highlights the open-source SENSIM platform, which enables the mapping of large-scale SNN/DNN models to the SENECA cores, as well as the benchmarking of crucial KPIs such as power and latency estimation.

- ### Objectives
- ✓ Open-source SENECA Simulation Platform
 - ✓ Hybrid event-driven Design
 - ✓ Scaling up the Flexible SENECA Neuro Synaptic Core
 - ✓ Faster design space exploration and energy/latency estimation
 - ✓ Easy Integration with SNN/DNN models
 - ✓ Customization for Future Research



- ### Parallel research & References
- ✓ Amirreza Yousefzadeh, et al. Multiplexing AER Asynchronous Channels over LVDS Links with Flow-Control and Clock-Correction for Scalable Neuromorphic Systems. May 2017.
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