

1

2

3

4

5

6

A

ASSY Configuration Codes (Binary OR)

Bit	Value	0 case	1 case
0	1	Not 2 cell operation;	2 cell operation, place JP10 in position B and DNP R65 and R73;
1	2	Not 3 cell operation;	3 cell operation, place JP10 in position B, DNP B3, R52, R53 and R76;
2	4	Not 4 cell operation;	4 cell operation, Place JP10 in position A, DNP R52, R53, R73 and R76;

B

ECO List

MOD Level	Number	Action Summary

C

Changes from REVA0 / BM2-F1

No previous Rev

D

PUMPKIN

INCORPORATED

750 Naples Street  
San Francisco, California 94112

Title: Battery Module Development Board Cells - Modification Information	
Part Number: 705-02190	Revision: A0
File: MOD.SchDoc	Sheet: 17 of 17
Date: 29/01/2020	Size: B
Drawn By: DJW	

1

2

3

4

5

6

Test Point List

Number	Net	Function
TP43	VC2	3rd center-tap
TP44	VC3	2nd center-tap, positive side of second lowest cell
TP45	VC4	1st center-tap, positive side of lowest cell
TP58	VC1	negative side of bottom cell
TP59	VC5	positive side of highest cell