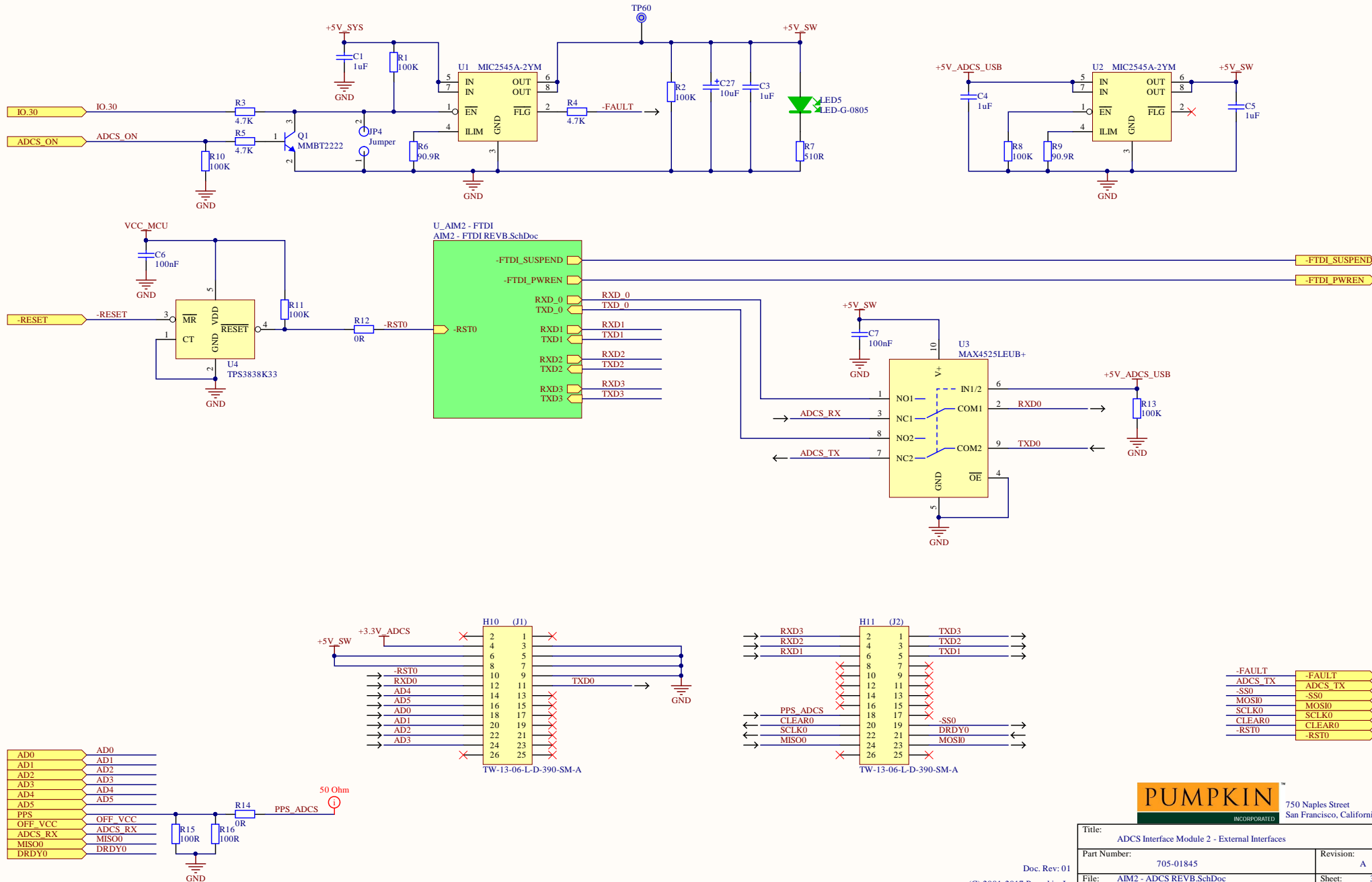


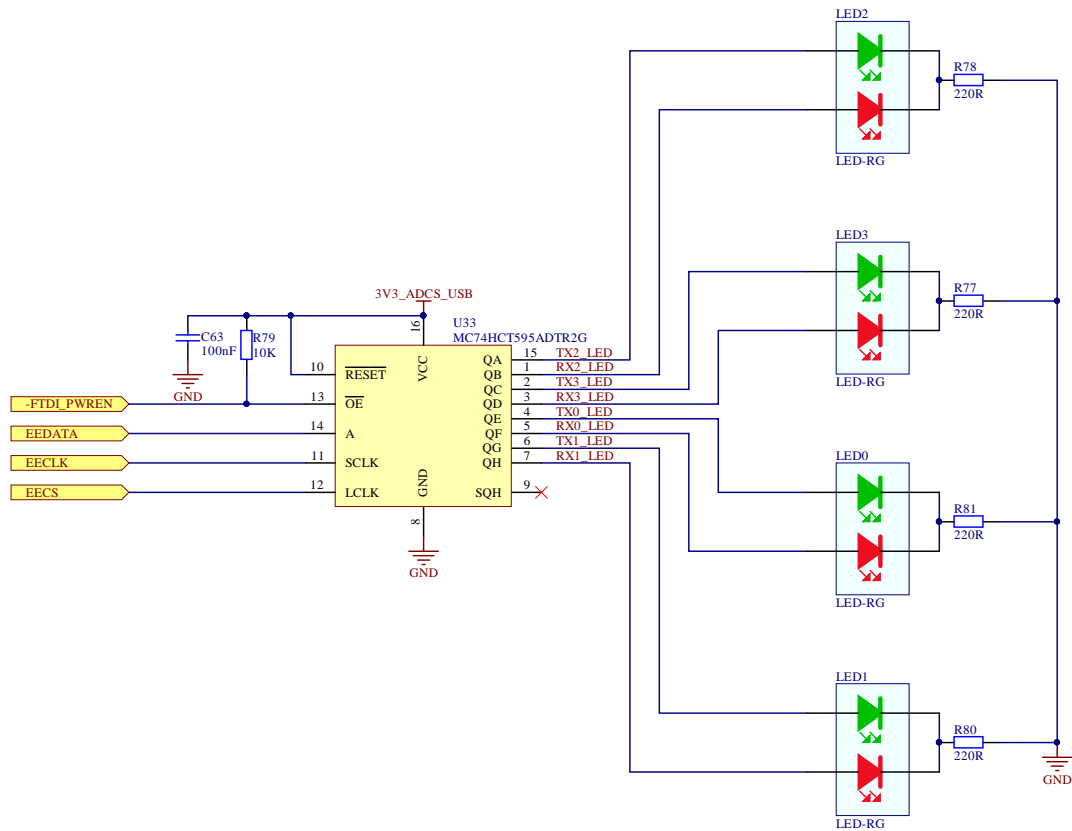
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|---|--------------------------------------|-----------|---------|
| Title: AIM2 - ADCS Multiplexing REVB.SchDoc | | | |
| Part Number: | 705-01845 | Revision: | A |
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| | | | |
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A

B

C

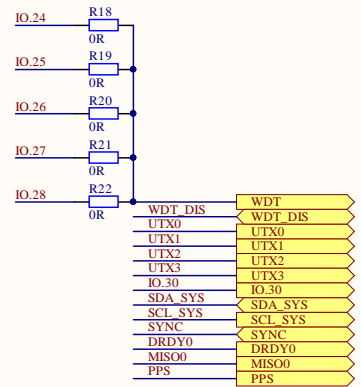
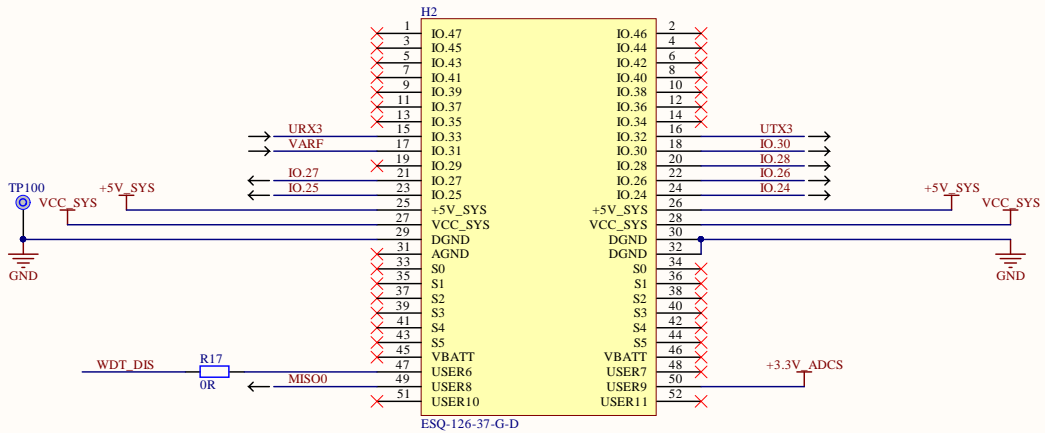
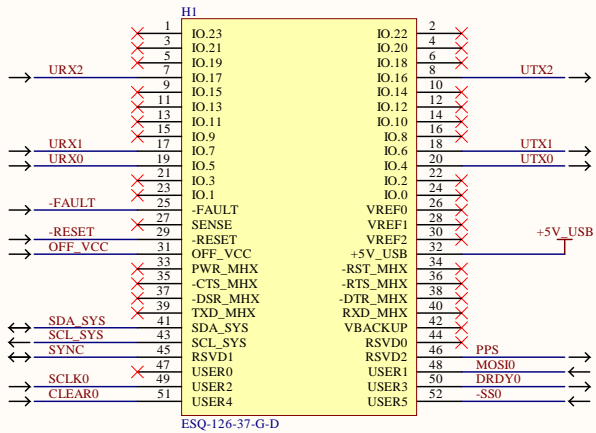
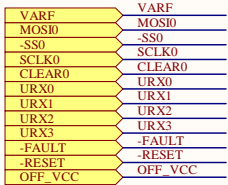
D

A

B

C

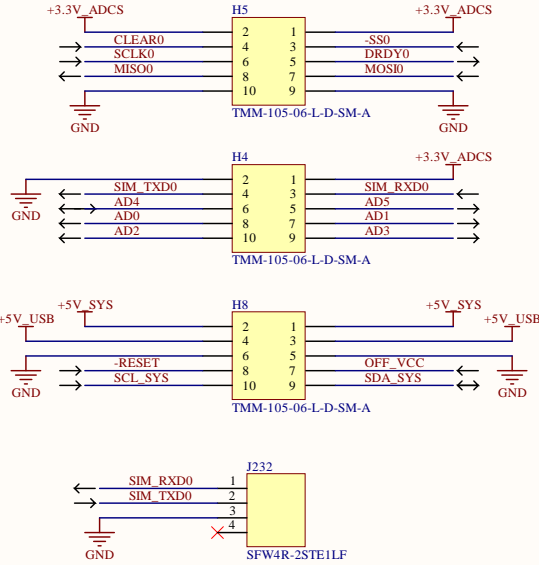
D



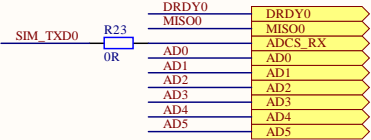
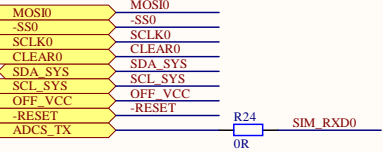
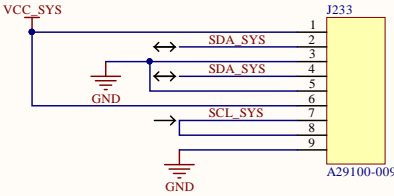
| | | | |
|--|-----------------------------------|-----------|---------|
| Title: ADCS Interface Module 2 - Bus Interface | | | |
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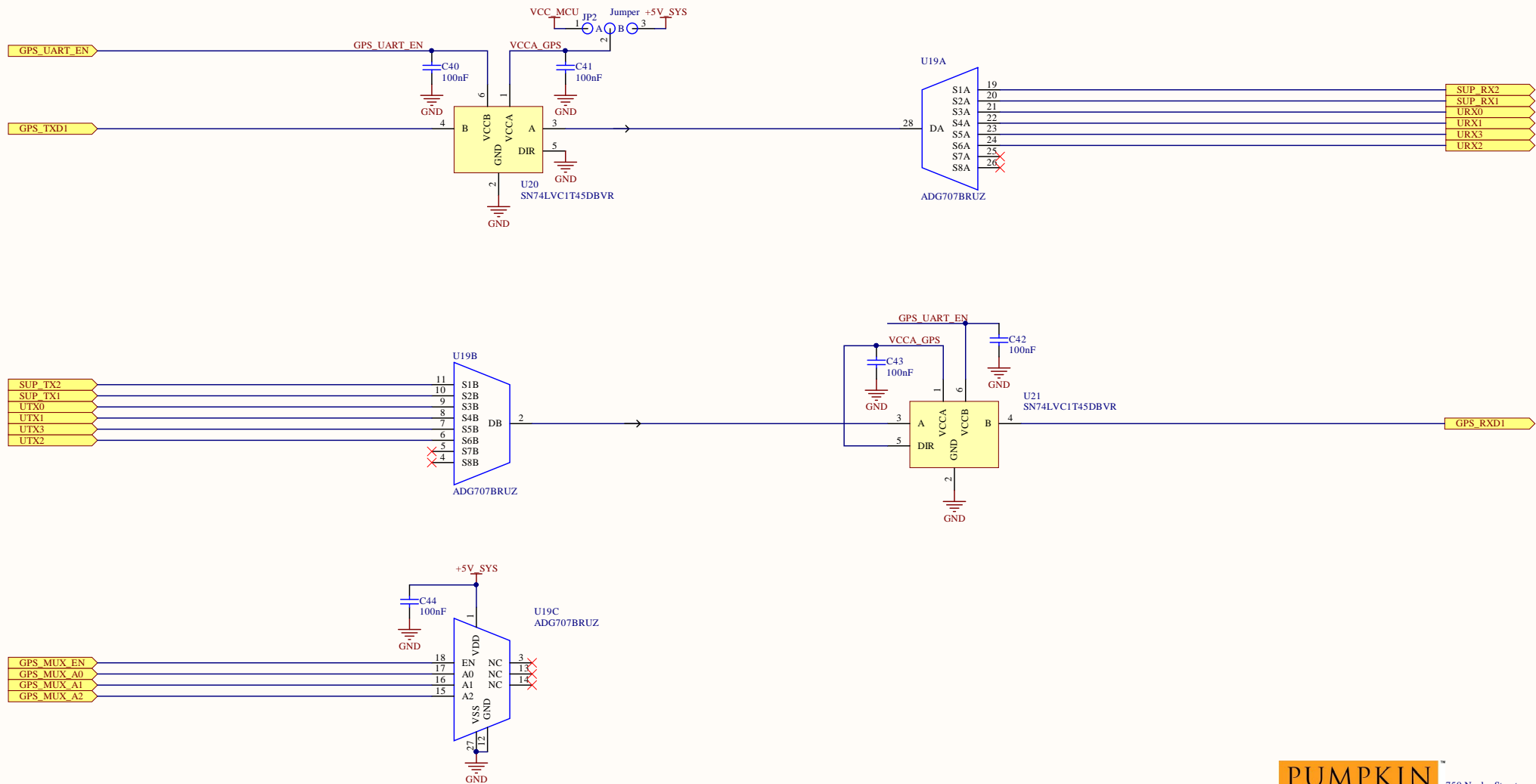
SIM Connections

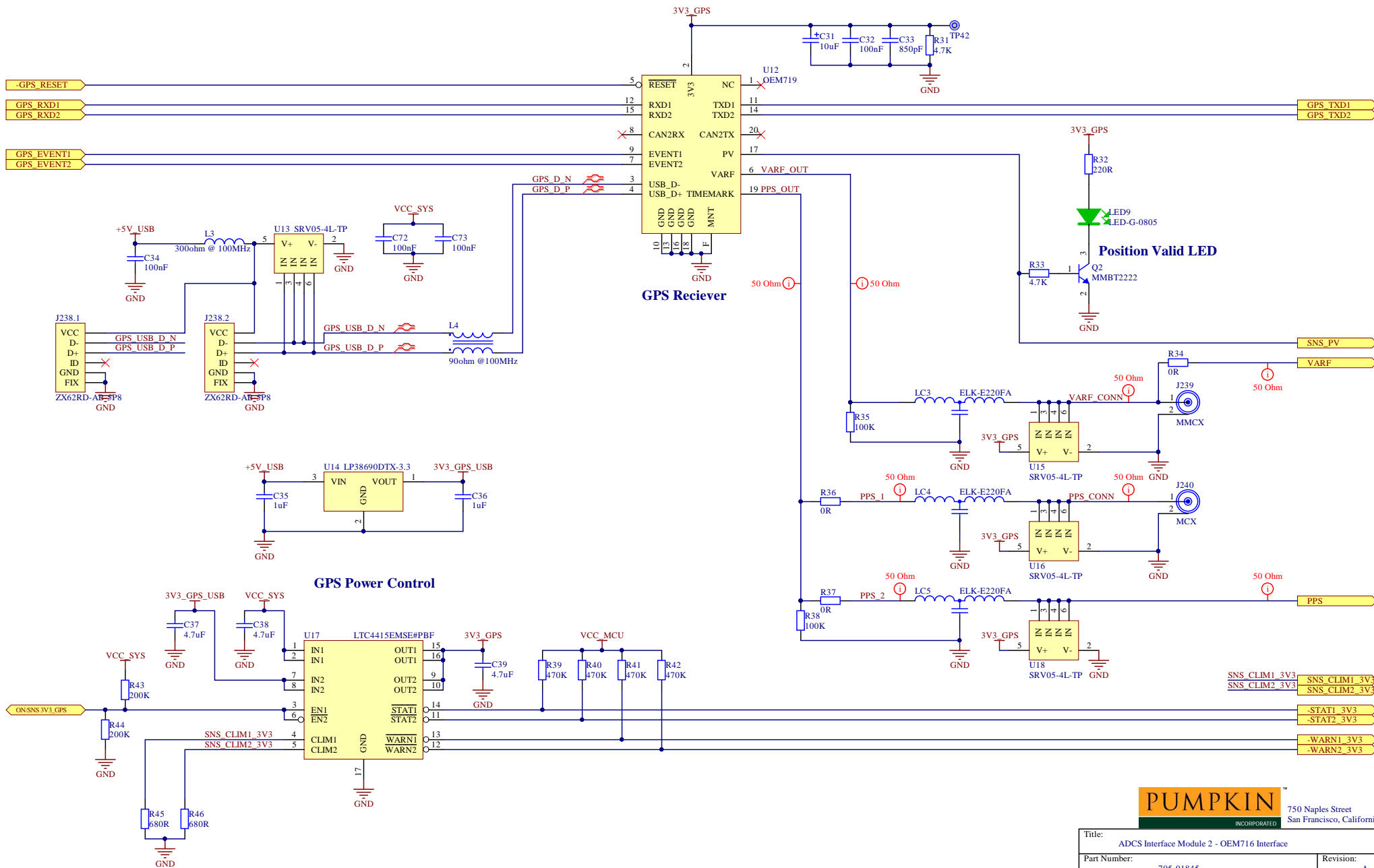


ANTS Connection



| | | | |
|--|---|-----------|---------|
| Title: ADCS Interface Module 2 - External Interfaces | | | |
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| | | | | | |
|---|---|---|---|---|---|
| 1 | 2 | 3 | 4 | 5 | 6 |
| A | | | | | |
| B | | | | | |
| C | | | | | |
| D | | | | | |

MOD Level Codes (Binary OR)

| | | |
|----|----|---------------------------------|
| 1: | 0: | Do not cut tracks at X2 |
| | 1: | Cut the two tracks in window X2 |
| 2: | 0: | Do not cut tracks at X1 |
| | 1: | Cut the two tracks in window X1 |

ASSY Revision Codes (Binary OR)

| Bit | Value | 0 case | 1 case |
|-----|-------|--|--|
| 0 | 1 | R47 is placed allowing OFFVCC to shutdown the SupMCU. | R47 is not placed, protecting the SupMCU from OFFVCC. |
| 1 | 2 | R62 is placed allowing -RESET to reset the SupMCU. | R62 is not placed protecting the SupMCU from -RESET. |
| 2 | 4 | R52 is not placed. | R52 is placed. |
| 3 | 8 | JP1 is in position A so the ADCS talks to the bus at a 3.3V logic level. | JP1 is in position B so the ADCS radio talks to the bus at a 5V logic level. |
| 4 | 16 | JP2 is in position A so the GPS talks to the bus at a 3.3V logic level. | JP2 is in position B so the GPS talks to the bus at a 5V logic level. |
| 5 | 32 | R68 is not placed to prevent WDT asserting -RESET on the bus. | R68 is placed so the WDT can assert -RESET on the bus. |
| 6 | 64 | R71 is not placed so the WDT cannot assert OFF_VCC on the bus. | R71 is placed so the WDT can assert OFF_VCC on the bus. |
| 7 | 128 | R17 is not placed, disconnecting the WDT_DIS signal. | R17 is placed, connectding the WDT_DIS signal. |
| 8 | 256 | R18 is not placed so the WDT cannot be kicked by IO.24. | R18 is placed so the WDT can be kicked by IO.24. |
| 9 | 512 | R19 is not placed so the WDT cannot be kicked by IO.25. | R19 is placed so the WDT can be kicked by IO.25. |
| 10 | 1024 | R20 is not placed so the WDT cannot be kicked by IO.26. | R20 is placed so the WDT can be kicked by IO.26. |
| 11 | 2048 | R21 is not placed so the WDT cannot be kicked by IO.27. | R21 is placed so the WDT can be kicked by IO.27. |
| 12 | 4096 | R22 is not placed so the WDT cannot be kicked by IO.28. | R22 is placed so the WDT can be kicked by IO.28. |
| 13 | 8192 | J231.1 and J238.1 are not placed. | J231.1 and J238.1 are placed - Also perform MOD 1. |
| 14 | 16384 | J231.2 and J238.2 are not placed. | J231.2 and J238.2 are placed - Also perform MOD 2. |
| 15 | 32768 | JP4 is not placed so the SupMCU controls ADCS power. | JP4 is placed so the ADCS is always on. |
| 16 | 65536 | J233 is not placed. | J233 is placed. |



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