ASSY Configuration Codes (Binary OR)

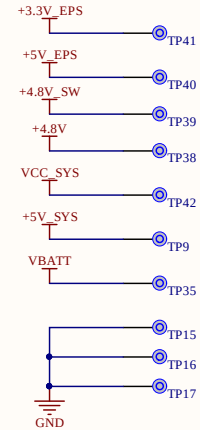
Bit	Value	0 case	1 case
0	1	R65 is placed to allow OFFVCC to shutdown the SupMCU;	R65 is not placed, protecting the SupMCU from OFFVCC;
1	2	R19 is placed, allowing -RESET to reset to SupMCU;	R19 is not placed, protecting the SupMCU from -RESET;
2	4	R4 is not placed, separating CLK0 for CLKOUT;	R4 is placed connecting CLK0 and CLKOUT;
3	8	Not bus flight switches;	Using bus flight switches, place JP1, JP2, JP4, JP5;
4	16	Not bypassing flight switches;	Bypassing flight switches, place JP3 and JP6;
5	32	Using External Thermistors DNP TS1 and TS2;	Using PCB mount thermistors, place TS1 and TS2;
6	64	750mA charging current, place R30 and R39 as 2.49K resistors;	1150mA charging current, place R30 and R39 as 1.5K resistors;
7	128	AGND connection is not made;	AGND connection is made, place R34;
8	256	No Through-hole components are placed;	Place Through-hole components B1-4, H1-2, J253, K1-3, J1;

Test Point List

Number	Net	Function
TP8	-RST	Reset Input to the SupMCU
TP9	+5V_SYS	System 5V Supply
TP15	GND	System Ground
TP16	GND	System Ground
TP17	GND	System Ground
TP35	VBATT	System Battery Voltage Supply
TP36	BATT_A+	Positive terminal of Battery A
TP37	BATT_B+	Positive terminal of Battery B
TP38	+4.8V	Module 4.8V Supply Voltage
TP39	+4.8V_SW	Battery Charging Supply Voltage
TP40	+5V_EPS	Module 5V Regulated Supply
TP41	+3.3V_EPS	Module 3.3V Regulated Supply
TP42	VCC_SYS	System 3.3V Supply

ECO List

MOD Level	Number	Action Summary



Changes from REVD0

Designed in Altium
Changed to PIC24 based SupMCU achitecture
Added better current measurement
Changed thermistor/heater connections
Simplified SupMCU Bus interface to I2C only
Added USB FTDI functionality

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Title: CubeSat Kit Linear EPS - Modification Information			
Part Number: 705-00337			Revision: E0
File: Linear EPS MOD.SchDoc			Sheet: 8 of 8
Date: 15/11/2018	Size: B		Drawn By: DJW

Doc. Rev: 01