

2 Cell configuration:
R53, R52 and R76 are placed
R73 and R65 are not placed
JP10 is in position B

3 Cell configuration:
R65 and R73 are placed
R52, R53 and B3 are not placed
JP10 is in position B

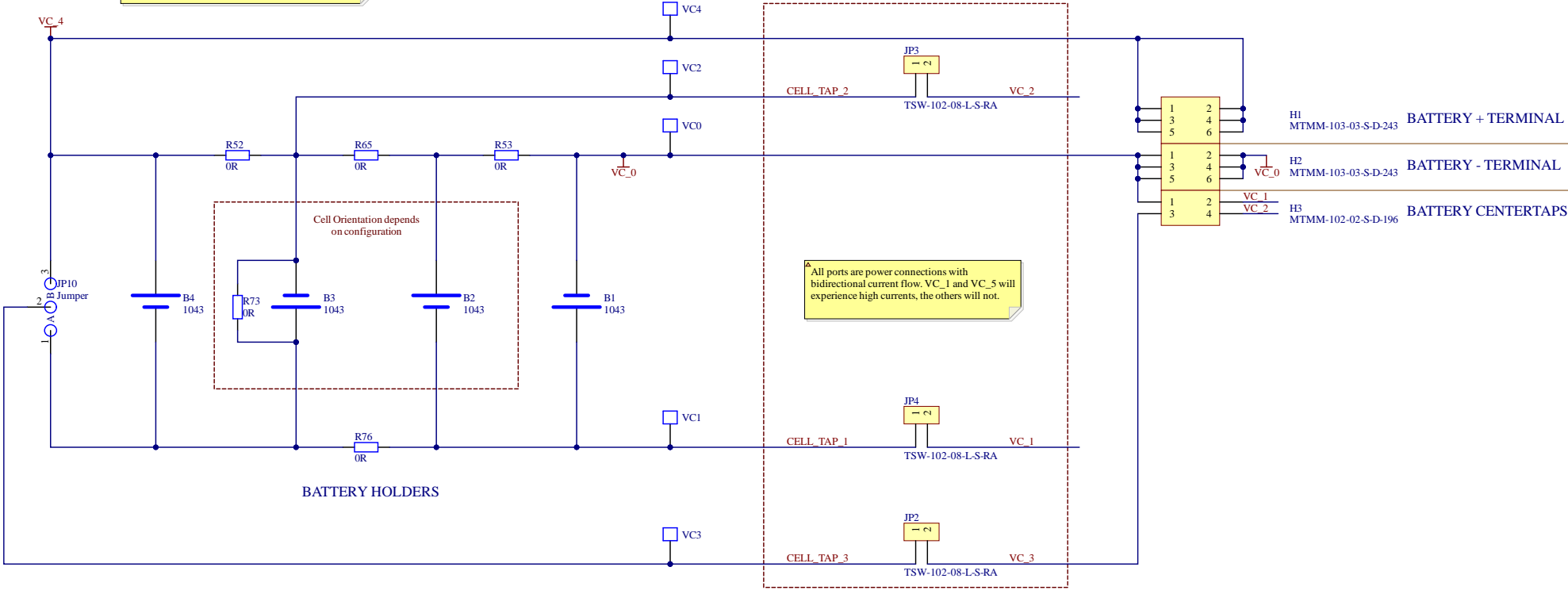
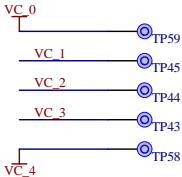
4 Cell configuration:
R65 is placed
R52, R53, R73, R76 is not placed
JP10 is in position A

JP2-4 can also be used so that the connection can have current flow measured

All ports are power connections with bidirectional current flow. VC_1 and VC_5 will experience high currents, the others will not.

VC1-5 provide direct connections to the cells

U_MOD
MOD.SchDoc



| | | | |
|---|------------------------------------|-----------|---------|
| Title: Battery Module Development Board Cells - Battery Connections | | | |
| Part Number: | 705-02190 | Revision: | A0 |
| File: | Battery_Module_Dev_Contacts.SchDoc | Sheet: | 9 of 17 |
| Date: | 24/03/2021 | Size: | B |
| | | Drawn By: | DJW |

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ASSY Configuration Codes (Binary OR)

| Bit | Value | 0 case | 1 case |
|-----|-------|-----------------------|--|
| 0 | 1 | Not 2 cell operation; | 2 cell operation, place JP10 in position B and DNP R65 and R73; |
| 1 | 2 | Not 3 cell operation; | 3 cell operation, place JP10 in position B, DNP B3, R52, R53 and R76; |
| 2 | 4 | Not 4 cell operation; | 4 cell operation, Place JP10 in position A, DNP R52, R53, R73 and R76; |
| 3 | 8 | :- | Two Board stacking configuration, top board. Place H2 and H3 as MTMM-103-09-S-D-243 and H1 as MTMM-102-07-S-D-196; |
| 4 | 16 | :- | Two Board stacking configuration, bottom board. Place H1, H2 and H3 as ESQT-108-03-F-D-709; |

Test Point List

| Number | Net | Function |
|--------|-----|---|
| TP43 | VC2 | 3rd center-tap |
| TP44 | VC3 | 2nd center-tap, positive side of second lowest cell |
| TP45 | VC4 | 1st center-tap, positive side of lowest cell |
| TP58 | VC1 | negative side of bottom cell |
| TP59 | VC5 | positive side of highest cell |

ECO List

| MOD Level | Number | Action Summary |
|-----------|--------|----------------|
| | | |

Changes from REVA0 / BM2-F1

| |
|-----------------|
| No previous Rev |
|-----------------|



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|--|---------|-----------------|--|
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