| 1 | | 2 | 3 | 4 | | 5 | 6 |
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| ASSY C | Configuration Codes (B | Sinary OR) | | | Test Point | t I jet | |
| Bit | Value Value | 0 case | | 1 case | Number | Net | Function |
| 0 1 2 3 4 | 1 Not 2 cell operation; 2 Not 3 cell operation; 4 Not 4 cell operation; 8 -; 16 -; | | 2 cell operation, place JP10 in position B and I 3 cell operation, place JP10 in position B, DNF 4 cell operation, Place JP10 in position A, DNF Two Board stacking confirguration, top board. MTMM-102-07-S-D-196: Two Board stacking confirguration, bottom bo | | TP43 | VC2 VC3 VC4 VC1 VC5 | 3rd center-tap 2nd center-tap, positive side of second lowest cell 1st center-tap, positive side of lowest cell negative side of bottom cell positive side of highest cell |
| ECO Lis | st | | | | | | |
| MOD Leve | | | Action Summary | | | | |
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| Changes No previous I | s from REVA0 / BM2- | F1 | | | | | |
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| | | | | | | | PUMPKIN 750 Naples Street San Francisco, California |
| | | | | | (€) 2001.∹ | Doc. Rev: 01 | Battery Module Development Board Cells - Modification Information Part Number: 705-02190 File: MOD.SchDoc Sheet: 1' |
| | | 2 | 3 | 4 | (0) 2001-2 | 5 | Date: 26/03/2020 Size: B Drawn By: D 6 6 |
| 1 | | | | | | | |