

Logic Systems, Type Theory & Applications of Program Verification

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Abstract

This is a report on advancements in type theory and formalized proof-based programming languages often referred to as verified programming languages. I have tried to explain the importance of mathematical modelling and logic system development based on the ancient works of mathematicians like Per-Martin Lof by the applications in present programming toolchain and possible future aspects like applications in a quantum programming toolchain.

Things are explained in chapter-wise manner and sufficient effort has been put to properly introduce things making understanding things easy. The chapters deal with mathematical logic systems first and then proceed to explain how this has been used and developed in real systems. With the current developments that are going on in this field, I have tried to explain the very possible usage in modelling a quantum computer's theoretical behaviour and applications in Machine Learning.

This area of study comes under the umbrella term of Programming Language Research and scientists all over have been using these concepts to do explain the language semantics of any new programming language. This works using a system of inductive logics and as in abstract algebra, various operations on a given mathematical structure (*example : Rings, Groups, Sets etc.*) a computer program is thought of being an operation on the a given type system. Type theory in its most literal meaning deals with the abstract idea of Types as a fundamental mathematical structures. Classical programming languages deals with data types and now some functional languages like *Haskell, Agda, Ocaml etc* consider operations too as a type. Debugging has been made so easy because of the type systems and test driven development. Consider the case where it is just some control signals flipping the states of bits and without a proper analytical typed constraint. The system's behaviour in this case will be completely unpredictable program-wise. Thus I would assume readers to believe with me that we would all like to have programs check that our programs are correct. Today most people who write software ,people from both academia and industry assume that the costs of formal verification of program outweighs the benefits. One simple example case is of *javascript* programming language which is very informally developed thus has a very weak type system that often leads to bugs. *Haskell* on other hand does not allow programs to disobey the type system used for that program.

Categories

[**Operating Systems**]: ReliabilityVerification, [**Software Engineering**]: Software/Program Verification

Key Words

Verification, Design, COQ, PLT (Programming Language Theory)

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Chapter 1

Introduction and History

In mathematics, logic, and theoretical computer science, a *Type System* is any of a class of formal systems which can serve as alternative standard logic system of sets. We consider every "object" has a "type" and all different kinds of operations are restricted to the objects of only a particular type.

Type Theory is closely related to and is often served as the basis of modern computer's type systems, which are a *programming language feature* for common bug reduction. Type theory was studied to avoid paradoxes like KleeneRosser paradox, Russell's Paradox [27] in a variety of formal logics and to rewrite systems. It describes the correctness of step-by-step working of an abstract model of machine. This along with complexity theory does constitute the system theory for complete functioning of any computational machine.

Organized research for the mathematics foundation started at the end of the 19th century and formed a new mathematical discipline called mathematical logic, which strongly links to theoretical computer science. It went through a lot of paradoxical results, until the discoveries stabilized during the 20th century with finally imparting mathematical knowledge with several new aspects and components known as *set theory, model theory, proof theory, etc.*, whose properties are still an active research field. *Stephan Wolfram* in his book titled *New Kind of Science* [45] explores the computational aspects of machine and tries to focus on the idea that simple systems can actually reason perfectly for complex behaviour of a large systems. Classical examples of simple Cellular Automaton* and Turing-Complete machines have been studied.

Type theory in a similar sense argues that complex working can be induced from logic constraints of simpler systems. A small Type system can thus account for type-safe* behaviour of a computer system. Now for better comprehension we use the concept of (λ) -calculus to combine abstraction of type models. In mathematics, two well-known type theories that can serve as logic foundation for a system are *Alonzo Church's typed (λ) -calculus* [42] and *Per Martin-Lof's intuitionistic type theory* [38].

1.1 λ -Calculus

Lambda calculus [36] is a formal system in mathematical logic for expressing computation based on function abstraction and its application using variable binding and substitution. It is an accepted universal model of computation based on reduction and abstraction that can be used to

theoretically understand behaviour of Turing machine. It was introduced in 1930s by mathematician Alonzo Church. This deals with constructing lambda terms (variable terms which change according to conditions and boundness) and performing reduction operations on them. Reduction Operations consist of α and β transformations. The former deals with renaming bound variable's name and second with replacing the bound variable with the argument expression in the body of the abstraction. It follows left associativity i.e. $fg h$ is just syntactic sugar (alternate form of representation) for $(fg)h$.

A working thumb rule for performing reductions :

$$\boxed{(\lambda param.output)input \implies output[param := input] \implies result}$$

Example :

$$(\lambda x.xy)z \longrightarrow_{\beta} (xy)[x := z] \longrightarrow_{\beta} (zy) \longrightarrow zy$$

1.2 Intuitionistic Type theory

Intuitionistic type theory (also known as constructive type theory, or Martin-Lof type theory) has mathematical constructs built to follow a one-to-one correspondence with logical connectives. For example, the logical connective called implication ($A \implies B$) corresponds to the type of a function ($A \rightarrow B$). This correspondence is called the *Curry Howard isomorphism*. This basically is an equivalent of unique mapping and existence of inverse as in the theory of sets. Previous type theories also followed this isomorphism, but Martin-Lof's was the first to extend it by introducing **Dependent types** as a basis of *High Order function dependence on basic functions*.

Machine Assisted Proving dates back as early as 1976 when the four color theorem was verified using a computer program. Butterfly Effect's discovery also was possible due to computer simulation of program with given some finite initial states. Most computer-aided proofs to date have been implementations of large *Proofs-By-Case* of a mathematical theorem. It is also called as *Proof-By-Induction* where child cases are considered first in an attempt to fully prove a theorem. Various attempts have been made in the area of *Artificial Intelligence* research to create smaller, explicit proofs of mathematical theorems using machine reasoning techniques such as *heuristic search*.

Such **Automated Theorem Provers** have found new proofs for known theorems also given a number of new results. Additionally, interactive proof assistants allow mathematicians to develop acceptable human-readable proofs which are then again verified too in a similar procedure.

1.3 Proof Assistants Introduction

Machine theorem basically involves model checking, which, in the simplest case involves brute-force enumeration of many possible states (although the actual implementation of model checkers requires much robustness for checking every case, and it does not simply reduce to brute force). There are *hybrid* theorem proving systems which use model checking as an inference rule. There are also programs which were written to prove a particular theorem, with an assumption that if the program finishes with a certain result, then our proposed theorem is true.

In computer science and mathematical logic, a **Proof Assistant or Interactive Theorem Prover** is a software tool to help with the development of formal proofs. This involves interactive editor, or other interface, with which a human can guide the search for proofs. Some steps are reduced by the computer and base cases are enlisted to be proved. We will discuss this part later.

In the present Programming Language research, the correctness of *Computer Programs* is proved using similar notions considering some "pre" and "post" cases. This idea can thus be extended to proving correctness of a programming language semantics.

Currently the developments in Quantum Computing strongly uses these proof-based programming language mainly because of the completely random behaviour of a *Quantum states* of Qubits. All that we have are probabilities of existence of a given quantum state. Let's say we can we can organize the chaos to some extent by *categorizing* those probabilities in few cases, then later operations and control logics will have to have an inductive transfer of the qubit's states. This is analogous to how we deal with things in Type theory. Therefore developing a strong typed abstract model will benefit quantum computing to a much bigger extent.

We will understand the working of proof assistants by firstly revisiting topics in abstract algebra, computer architecture and compiler technology. We will then proceed to introduce the type systems and core elements of type theory in study and finally combine both to reason for correctness of a programming language and a programming stack.

I have later introduced concepts of quantum computing followed by the application of type theory in it to put some light on some ongoing active research in that field.

Chapter 2

Abstract Algebra

Abstract algebra [16] also referred to as modern algebra is the study of algebraic structures. An algebraic structure serves as an explanatory basis of functional operations on an underlying set. A set here also is an abstract idea of a collection of things that share certain common features and should not only be confined with sets dealt in classical set theory. Over the time on the basis of types of operations and *logical freedom* to do so on various sets have led to acceptance of various algebraic structures defined below. The study of abstract algebra is used primarily in areas of topology of n dimensions. For anyone this is insane for physical boundations arise when we try to visualize things out. Thus we need to classify abstractions in one of the algebraic structures and then deal with it.

2.1 Group Theory

This is a part of abstract algebra where we deal with **Group** mathematical structures. In mathematics, a **Group** is an abstract algebraic structure that consists of a set of elements and various operations which when performed on any *two* elements of the set results in a *third* element from same set. It satisfies four conditions called the "group axioms" or "group properties", namely closure or closed operations i.e. that maps from set to itself, associativity, identity and invertibility. One of the most common examples of a group structure is the set of integers with the addition operation. This algebraic structure is fundamental basis of other more complex algebraic structures. It is studied in followings ways.

A group G with the property with a given operation "o" such that

$$a \circ b = b \circ a \mid \forall a, b \in \{G\}$$

is called abelian or commutative. Groups not satisfying this property are said to be nonabelian or non-commutative.

2.1.1 Cyclic Groups

A **Cyclic Group** is a group that can be generated by a single element often called as group generator). Cyclic groups are always Abelian. A cyclic group of finite group order n is denoted G_n . The generalized generation rule can be specified as :

$$X^n = I(X \in G)$$

In the simple sense, it means identity element can be generated by any single element by repeated application of group operations. And since the identity element can be realized this way, all the elements of group can be realized too.

2.1.2 Permutation groups

A **Permutation Group** is a group G whose elements are permutations of a given set M and whose group operation is the composition of permutations in G (which are thought of as bijective functions from the set M to itself). The group of all permutations of a set M is the symmetric group of M , often written as $\text{Sym}(M)$. [1] The term permutation group thus means a subgroup of the symmetric group. If $M = 1, 2, \dots, n$ then, $\text{Sym}(M)$, the symmetric group on n letters is usually denoted by S_n .

2.1.3 Group Actions

an action of a group is a formal way of interpreting the manner in which the elements of the group correspond to transformations of some space in a way that preserves the structure of that space.

. For other groups, an interpretation of the group in terms of an action may have to be specified, either because the group does not act canonically on any space or because the canonical action is not the action of interest. For example, we can specify an action of the two-element cyclic group

$$C_2 = \{0, 1\}$$

on the finite set

$$\{a, b, c\}$$

by specifying that 0 (the identity element) sends

$$a \mapsto a, b \mapsto b, c \mapsto c$$

, and that 1 sends

$$a \mapsto b, b \mapsto a, c \mapsto c$$

. This action is not canonical.

2.2 Field Theory

In mathematics, a field is a set on which addition, subtraction, multiplication, and division are defined, and behave as the corresponding operations on rational and real numbers do. A field is thus a fundamental algebraic structure, which is widely used in algebra, number theory and many other areas of mathematics.

The best known fields are the field of rational numbers, the field of real numbers and the field of complex numbers. Many other fields, such as fields of rational functions, algebraic function fields, algebraic number fields, and p-adic fields are commonly used and studied in mathematics, particularly in number theory and algebraic geometry. Most cryptographic protocols rely on finite fields, i.e., fields with finitely many elements.

The relation of two fields is expressed by the notion of a field extension. Galois theory, initiated by variste Galois in the 1830s, is devoted to understanding the symmetries of field extensions. Among other results, this theory shows that angle trisection and squaring the circle can not be done with a compass and straightedge. Moreover, it shows that quintic equations are algebraically unsolvable. Fields serve as foundational notions in several mathematical domains. This includes different branches of analysis, which are based on fields with additional structure. Basic theorems in analysis hinge on the structural properties of the field of real numbers. Most importantly for algebraic purposes, any field may be used as the scalars for a vector space, which is the standard general context for linear algebra. Number fields, the siblings of the field of rational numbers, are studied in depth in number theory. Function fields can help describe properties of geometric objects.

2.3 Rings Theory

In mathematics, a ring is one of the fundamental algebraic structures used in abstract algebra. It consists of a set equipped with two binary operations that generalize the arithmetic operations of addition and multiplication. Through this generalization, theorems from arithmetic are extended to non-numerical objects such as polynomials, series, matrices and functions.

Whether a ring is commutative or not (i.e., whether the order in which two elements are multiplied changes the result or not) has profound implications on its behavior as an abstract object. As a result, commutative ring theory, commonly known as commutative algebra, is a key topic in ring theory. Its development has been greatly influenced by problems and ideas occurring naturally in algebraic number theory and algebraic geometry. Examples of commutative rings include the set of integers equipped with the addition and multiplication operations, the set of polynomials equipped with their addition and multiplication, the coordinate ring of an affine algebraic variety, and the ring of integers of a number field. Examples of noncommutative rings include the ring of $n \times n$ real square matrices with $n \geq 2$, group rings in representation theory, operator algebras in functional analysis, rings of differential operators in the theory of differential operators, and the cohomology ring of a topological space in topology.

2.3.1 Polynomial Rings

In mathematics, especially in the field of abstract algebra, a polynomial ring or polynomial algebra is a ring (which is also a commutative algebra) formed from the set of polynomials in one or more indeterminates (traditionally also called variables) with coefficients in another ring, often a field. Polynomial rings have influenced much of mathematics, from the Hilbert basis theorem, to the construction of splitting fields, and to the understanding of a linear operator. Many important conjectures involving polynomial rings, such as Serre's problem, have influenced the study of other rings, and have influenced even the definition of other rings, such as group rings and rings of formal power series.

A closely related notion is that of the ring of polynomial functions on a vector space.

2.4 Iso-morphisms

In abstract algebra, a group isomorphism is a function between two groups that sets up a one-to-one correspondence between the elements of the groups in a way that respects the given group operations. If there exists an isomorphism between two groups, then the groups are called isomorphic. From the standpoint of group theory, isomorphic groups have the same properties and need not be distinguished.

2.5 Homo-morphisms

In algebra, a homomorphism is a structure-preserving map between two algebraic structures of the same type (such as two groups, two rings, or two vector spaces). Homomorphisms of vector spaces are also called linear maps, and their study is the object of linear algebra.

Chapter 3

Computer Architecture

Computer architecture in layman terms is all about building a computer machine with some or total specification and detailing of how a set of software and hardware technologies interact with each other as to form a computer system. It refers to how a computer system is designed and what technologies it is compatible with.

One of the most followed Logical model of computer architecture is **Von Neumann's Architecture**[44]. This was proposed by the mathematician *John von Neumann* in 1945. It describes the design of an electronic computer with its CPU, which includes the arithmetic logic unit, control unit, registers, memory for data and instructions, an input/output interface and external storage functions. It follows that an instruction fetch and data operation cannot be performed at the same time. Another used architecture is **Harvard's Architecture**[33] which is also similar to Neuman's Architecture except that it uses different physical paths or data buses for carrying data and instruction. This idea has led to creation of parallelism and this distinction has evolved over to reason for the different Cache mechanisms used for a *Random Accessed Memory (RAM)* and *Read Write Memory (RWM)* type memory storage.

Let's now look at the basic parts of computer architecture.

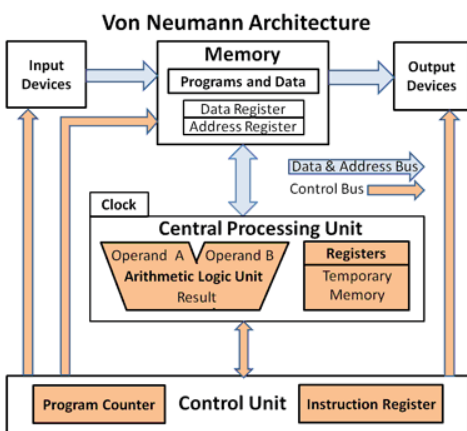


Figure 3.1: Von Neuman architecture
(source: wikipedia)

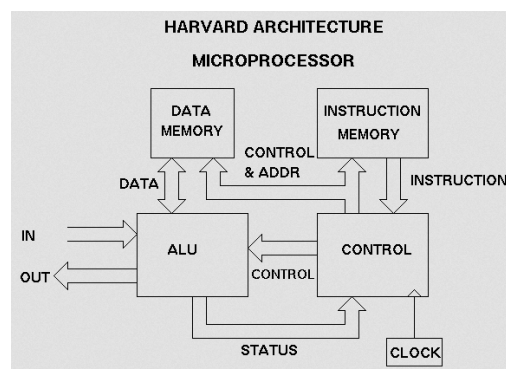


Figure 3.2: Harvard architecture
(source: wikipedia)

3.1 Instruction Set Architecture

Instruction Set Architecture (ISA) serves as the interface between computer software and hardware. It is a set of rules or instructions that define the working of a computer. These have evolved overtime due to numerous optimizations implemented for the same hardware. Such improvements have led to more abstractness. For example, most of present ISA deal with pointers rather than directly referring to registers in the machine byte code implementation. ISA define some of the very fundamental data types that correspond to some fixed bit size storage in memory. It handles very low-level control flow and Logic operations. Its very important to understand many compiler-implementations can produce same Instruction Set. Instruction Set is defined only for a hardware and assembly-level machine codes should follow these IS specifications only.

An ISA may be classified by their implementation complexity. A **Complex Instruction Set Computer (CISC)** has many special instructions but can be realized with other basic instructions too thus a **Reduced Instruction Set Computer (RISC)**[5] is used that increases efficiency by implementing only those instructions that are frequently used in programs, and also some special instructions if required and made possible by an appropriate memory-time tradeoff.

Other types include **Very Long Instruction Word (VLIW)** architectures, and the closely related **Long Instruction Word (LIW)** and **Explicitly Parallel Instruction Computing (EPIC)** architectures. These architectures seek to exploit instruction-level parallelism with less hardware than RISC and CISC by making the compiler responsible for instruction issue and scheduling.

3.2 Microarchitecture Design

Microarchitecture design also called computer organization desing deals with the ways a given instruction set architecture (ISA), is implemented in a particular processor. The microarchitecture is usually represented as diagrams that describe the interconnections of the various microarchitectural electronic hardware elements of the machine which includes components from basic registers to complete ALU. These diagrams generally separate the datapath and instruction control path. Some important terms related to the microarchitecture design are given here under.

The **Instruction Cycle** is the basic operational process of a computer system. It is the process by which a computer retrieves a program instruction from its memory, determines what actions the instruction describes, and then carries out those actions. **Instruction Pipelining** is a technique for implementing instruction-level parallelism within a single processor. **Cache** is simply very fast memory. It can be accessed in a few cycles as opposed to many-step access or other type of memory. **Multiprocessing** is the use of two or more central processing units (CPUs) within a single computer system. The term also refers to the ability of a system to support more than one processor or the ability to allocate tasks between them. **Multithreading** or **Concurrency** is the ability of a central processing unit (CPU) (or a single core in a multi-core processor) to execute multiple processes or threads concurrently, supported by the operating system.

3.3 Logic Synthesis and Implementation

Finally when both the ISA and Microarchitecture design are available, the next thing to do is finally implement it. This includes all the logic implementation (the gate logic), Circuit Implementation and design validation through FPGAs. The way it works is making complete circuitry on a FPGA board and then performing error reductions to improve efficiency. Else one may simulate the proposed μ -architecture on an existing machine or computer system.

Here comes the interesting part that follows from the Church-Turing Hypothesis[\[30\]](#) that says any first order recursive and computable function can be well simulated in a **Turing-Complete machine**. All classical machines are however not perfectly turing complete and some functions can't be simulated on them. Here on **Destusch** added his hypothesis[\[11\]](#) claiming the *Quantum Parallelism* couldn't be sufficiently simulated because of much large randomness in the resulting values.

Chapter 4

Compilers

Compilers^[1] are that piece of software that *translates* the programs in one language to other languages. They are different from **Assembler**. An assembler converts the assembly codes to final machines' instruction codes according to the ISA of the target computer hardware. Compilers are generally thought to convert higher level programs to low level programs. They are of different types like Cross compilers, Bootstrap Compilers (it comes under cross compilers) , Transilers and De-compilers. The basic operations carried by most of the compilers are :

4.1 Lexical Analysis

This is the first part of any compiler that takes in the program source code and does word-substitutions, Macro expansions, generating *Tokens* and cleaning up extra white spaces. All this work is done by using regular expression to increase compactness of input code. The tokens when combined in a special way form what is known as **Abstract Syantax Trees (AST)**. The regular expressions are interpreted as *Non-Deterministic Finite Automata (N DFA)* since the states or the inner expression in between any two parts of a Regex can't be fixed since it will vary with the input used. Thus the N DFA is converted to *Deterministic Finite Automata (DFA)* which will be parsed to AST using the technique of ϵ -Transformation.

4.2 Syntax Analysis or Parsing

This is the process of recombining the context-free grammatical tokens into a data structure, syntax tree. It is important that any sort of syntax error should be reported at this point of compilation. Context-free grammar is a recursive notation for describing sets of strings and imposing a structure on each such string without consiering the context in which the syntax is generated.

4.2.1 Abstract Syntax Trees and Abstract Binding Trees

In computer science, an abstract syntax tree (AST), or just syntax tree, is a tree representation of the abstract syntactic structure of source code written in a programming language. It is "Abstract" in the sense that it does not represent every detail appearing in the real syntax of the programming

language used, but rather just the structural and content-related details.

An abstract syntax tree is an ordered tree whose leaves are variables and whose interior nodes are operators whose arguments are its children. Asts are classified into a variety of sorts corresponding to different forms of syntax that divide ASTs into syntactic categories. For example, common programming languages often have a syntactic distinction between commands and an expression. These are two type of sorts for ASTs.

Abstract Binding Trees (ABT) enrich the meaning of AST with the methods to introduce new variables and symbols called *bindings* to an existing AST. It should be noted that it is a theoretical concept and only AST is used in compilers while parsing. The additions are performed within a given range of significance of the added terms called its *scope*. In desinging type systems, we therefore deal with these ABT to construct new language.

Writing a new Programming language grammar basically involves writing syntactic sugar for *Expressions*, *Statements* (often called commands) and *Declaration* like header declarations and variable assisgnments. There are some **rules** defined and *Judgements and derivations* are performed on them. The derivations are mostly inductive and also covers the finite aspect of machine. This can be understood in a way that DFA is mantained and machine never goes to a NDFA state.

For example here is a derivation of successor of a natural number, [succ(succ(succ(zero)))nat] :

$$\frac{\frac{\frac{zero \quad nat}{succ(zero) \quad nat}}{succ(succ(zero)) \quad nat}}{succ(succ(succ(zero))) \quad nat}$$

The denominator in the expression is an inductive derivation from the numberator conditions.

4.3 Type Checking

This is one of the most important part where data consistency is checked of input source code. The Compiler may exploit or depend on type information, which makes it natural to combine calculation of types with the actual translation of source code. A language is considered to be type-safe if does not allow violation of basic data types. The varying level of strictness leads to formation of weak and strong type safe languages.

It shows a diagram of the design space of static vs. dynamic and weak vs. strong typing, placing some well-known programming languages in this design space.

4.4 Optimization

This part deals with converting the present code as AST into more efficient using a series of transformations that makes the code use lesser sources but produces same output and variable scope is maintained throughout. Some of the common optimization techniques are :

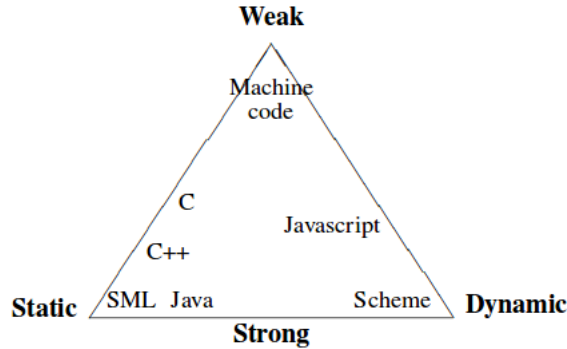


Figure 4.1: Design Space of Types (source : Dragon book of compiler Technology)

Peephole optimizations replaces a complex multi-step instruction by an instruction of less-number of steps preferably a single step, for example multiplication by 2 is equivalent of rotating bits to left by 1. **Data Flow Optimizations** convert expressions into simple to reduce number of duplicate expressions. **Static Single Assignment (SSA) optimization** involves assigning variables only a single value. If variables have same values then they are considered as one variable only. Scope Tables are used in these cases.

4.5 Code Generation

This part converts the optimized AST into machine codes of a hardware compatible ISA representation. It involves exploiting complex instructions and carefully placing jump and branch instructions for the machine code program. This part is also to be optimized and techniques of *Register allocation, Instruction selection, Instruction scheduling* and *Rematerialization*.

4.6 Interpreter

Interpreters are different from the compilers only in the aspect it performs operations as the code is produced without having a previously compiled code and then executing it. It works similar to a compiler in terms of basic steps but uses an **Intermediate Representation (IR)** of AST before producing machine code and executing it too.

Low Level Virtual Machine (LLVM)[18] is a popular compiler technology that takes into account the benefits of IR in compilation. It is used all over now and has increased portability of softwares a lot. Recently the *Polyhedral Loop Optimizations (Polly)* in the LLVM IR has gained popularity and they have been used in all possible places.

Chapter 5

Type Systems

A **Type System**[43] for any programming language is a set of rules that associate a property called "*Type*" with the basic grammatical constructs of the language like variables, constants, statements and commands. It is the computer programming equivalent of type theory which uses type safety and type validation to prevent bugs, compiler runtime errors and compiler optimizations. It is very basic for any programming language to have types to actually perform any of Higher Order Logic on the host machine. Note that a type system can work only if the program is run. In other words it is absolutely insignificant to check a program if it is not run or atleast compiled to be run.

5.1 Dependent Types

Dependent Types [32] is the most important kind of Types whose definition depends on a value. It is the conceptual overlap between computer's Type System and Type theory as it allows evaluation by induction. In functional Programming languages, dependent types increases expressivity of a defined type. It's examples include dependent functions and dependent pairs. Introduction of new variable or new values may completely change the overall behaviour. Thus the Structural Type checking would be used and not nominal (defined below). It is considered to be of two types from mathematical point of view :

- 5.1.1 Π -Type

It is called **Dependent Product Type** where given a type space Ω , we define a family of types $B:A \rightarrow \Omega$ that says every term $a:A$ has a type $B(a):\Omega$. It explains that there is not a fixed co-domain for functions of this type. The name 'pi-type' comes from the idea that these may be viewed as a Cartesian product of types. Pi-types can also be understood as models of universal quantifiers. For an example let's say a function gives tuple of order n for a given natural number input n . Clearly the terms in that tuple does not depend on the value of n . Here also, the Dependent product nature is preserved.

- **5.1.2 Σ -Type**

It is called **Dependent Sum Type** and is categorically dual to dependent product type where given a type space Ω , if we have a family of types $B:A \rightarrow \Omega$ then there must exist a dependent pair type. It explains that there should be fixed codomain for any function.

They use mathematical operators (\rightarrow) and binders (\forall) to produce complex types. The higher order **Dependently Typed Polymorphic lambda calculus** also called **Calculus of Inductive Constructions** is the foundational basis of all Proof Assistants.

5.2 Flow-Sensitive Types

Such Type systems determine the type of a variable by controlflow. The type of a variable may change in any of the program's method. The type inference (defined below) is used for checking these type systems.

5.3 Latent Types

Latent typing refers to a type system where types are associated with values and not variables.

5.4 Refinement Types

These are a set of some preconditions that define the behaviour of the function or variable. These can be understood as the return types of most of functions which may depend on some 'if/else' conditions.

5.5 Substructural Types

This is the type system defined depending on the use of a variable. For example, a variable may be used once or many times. It has **Linear**, **Affine**, **Relevant** and **Ordered** as subcategories. The Linear system allows the use of object exactly once, Affine system allows use at most once, Relevant system allows usage any number of times and Order system allows using objects only once in an order.

5.6 Unique Types

A Unique type guarantees that an object is used in a single-threaded way only with maximum one reference to it. It maintains the referential transparency and improves the efficiency of functional languages.

5.7 Type Safety & Memory Safety

Type safety refers to type equivalence and behavioural equivalence of all language constructs for a given program written in any programming language. Memory safety on the other hand refers to

leakage and corruption of memory by any program. example : stack overflow.

Some Type checking techniques to ensure type safety are :

- **Static vs Dynamic**

A **Static** type system refers to compilation time type checking. The source code's type correctness is verified first and then the executable is generated. A **Dynamic** type system checks type correctness during the runtime. This is the case when bindings and linking are created so that data passed on from one of the program to other is correct type-wise. Together both create polymorphic type checking where a single interface or a single Type check-pass is capable of doing both kind of type checks and is very portable to use.

- **Nominal vs Structural**

In a **Nominal** or **Nominative** type system, the equivalence of data types is determined by explicit declarations or name of types. For example, in C programming language, two "*struct*" types with different names in the same translation unit are never considered compatible, even if they have identical field declarations. In **Structural** type system, the inner structure of a data type are evaluated to show data equivalence and type safety.

- **Manifest vs Inferred**

Manifest Typing is explicit identification by the software programmer of the type of each variable being declared. For example: if variable X is going to store integers then its type must be declared as integer. Type **Inference** refers to the automatic detection of the data type of an expression in a programming language. For example, user may write a float point decimal without having declared that variable as a float point. The compiler technology should be able to detect what program is trying to do and perform further actions accordingly. This is also referred to as **Typeless Typeing** where the type is later introduced depending on data passed.

- **Duck Type-ing**

This type system got it's name from on of the most common computer engineering practices which says if something behaves like a duck, quacks like aduck then it must be duck. In other words the behaviour determines the data type. For example, adding a float type integer to a double type integer, the answer should have a double type because it is surely to have characteristics of a double type integer.

5.8 Unified Type System

In the object oriented programming (OOP) context, the abstraction and scope of methods deals with it's derivation from a base class. similar logic can be extended to class objects whose properties and methods depends on a base class. A **Unified Type System** states that all the Types of objects are derived from a single root type. Thus that object is capable of performing operations approved by the root type. Example of such a programming language is C# developed by Microsoft. In C# the concepts of encapsulation and methods have been decoupled from the reference requirement so that a type can support methods and encapsulation without being a **Reference type**. A reference type is also an OOP concept that says all instances of class objects are by reference.

Chapter 6

Proving

In this and next chapter I will discuss how proving is done on a computer machine. Similarity of mathematical type structures and a programming language type system is to be understood thus explaining the theoretical correctness of afore mentioned equivalence.

6.1 Hilbert-style deduction systems

This is a kind of logic system [34]. The idea of a formal deduction says that there are mathematical formulaes or functions (for Input-Output generalization) which are either the base cases often called Axioms or are derived from some axioms by applying some hypotheses. Let Γ be a set of presumed contions and base cases (Axioms) and ϕ be deduction made using Γ then we can write this as $\Gamma \vdash \phi$ meaning ϕ can be deduced and hence can be proved from Γ .

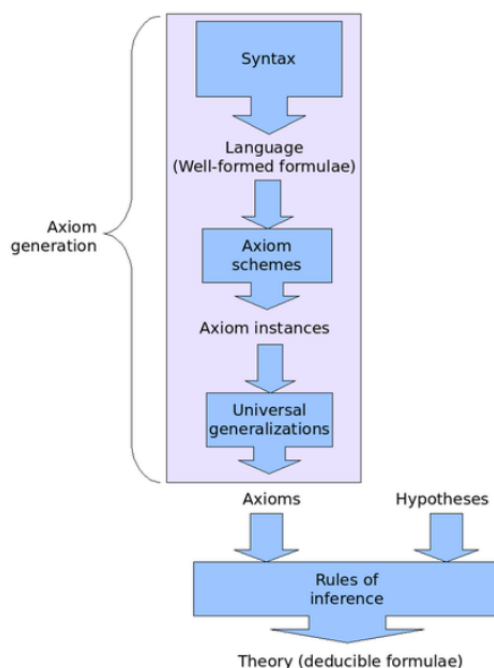


Figure 6.1: A Basic Deduction System (source : Wikipedia)

The Deduction system is well characterized by use of numerous Logical Axioms called an **Axiom Scheme**. It gives us a iterative and inductive nature of axiom deduction so that it can be extended as many step as one wish. The terms called *Combinators* (like \rightarrow) that interweave the results of a set of a given Axiom Scheme and *Quantifiers* (like \forall) generalize the results of axiom schemes using some quantifiable data like the initial value of a mathematical function.

Using Combinators and Quantifiers one may easily reduce the deductions into less number of steps hence forming metatheorems. A metatheorem in mathematics is the one that can be achieved using some other very basic mathematical theorems. Thus this gives us idea of correctness of untyped λ -calculus. We can thus extend this to other objects also. Below I have tried to explain formation of a Hilbert-style deduction system for Abstract Binding Tree.

Professor **Robert Harper** in his book *Practical Foundations of Programming Languages*[13] defines the term *Judgement* 'J' defined on a set of rules 'R' defined for a type that satisfies a property 'P' (hence forming an Abstract Binding Tree ABT) and it follows an inductive definition

$$\frac{J_1 J_2 J_3 \cdots J_k}{J}$$

where each J_i is some sort of pre-defined abstract judgements or base cases. The set R is a collection of all such J_i s. R is also referred to as an *Axiom* if $k=0$ i.e it is just first judgement to be made. for example :

$$\frac{}{zero \quad nat} \quad \text{and} \quad \frac{a \quad nat}{succ(a) \quad nat}$$

He explains *Derivability* Judgement $J_1 \dots J_k \vdash_R K$ for a given set of rules 'R', with J_i and K as basic judgements to show that we may derive K from the expansion of $R \cup \{J_1 \dots J_k\}$ of the rules with axioms

$$\overline{J_1} \quad \overline{J_2} \quad \overline{J_3} \quad \cdots \quad \overline{J_k}$$

$\Gamma \vdash_R K$ means that a Judgement K is derivable from rules $R \cup \Gamma$.

He defines *Admissibility* Judgement written as $\Gamma \vdash_R J$, that if there exists any judgement like $\vdash_R \Gamma$ then it implies $\vdash_R J$. It means that if all the assumptions (base cases) are derivable from R then J is derivable from R.

The mathematical formalization of static part and dynamic part of a typed programming language is expressed in the form of the judgements.

The static feature of a language imposes constraints on the formation of the phases that are sensitive to the context in which they occur. It follows an induction definition like this

$$\chi | \Gamma \vdash e : \tau$$

where χ is a finite set of variables and Γ is a typing context consisting of hypothesis of the form $x : \tau$, one for each $x \in \chi$. The Dynamics feature basically means the transition of states expressed in form of *Transition* Judgement having a transition $s \mapsto^n s'$ (for n^{th} order transition) as

$$\frac{}{s \mapsto^n s'} \quad \text{and} \quad \frac{s \mapsto s' \quad s' \mapsto^* s''}{s \mapsto^* s''}$$

It is important to note that all these operations are performed on presumed abstract mathematical objects, Abstract Binding Trees (ABTs) which are also the most important fundamental part

of any type system. This theoretical equivalence thus allows us to construct the static and dynamic rules and some derived Judgements or abstract functions (with type correctness) for a programming language. A very good hand on experience can be found in the books Software Foundations [20] and Certified Programming with Dependent Types [3].

6.2 Curry-Howard Correspondence

In programming language theory the **CurryHoward correspondence**[31] (also known as the CurryHoward isomorphism) says that mathematical proofs and computer programs are equivalent. This equivalence depends on *Axiom Schemes* with a Type as a combinator. It also follows the *Combinatory Logic System* that allows removing a mentioned variable using some 'ways'. These ways also called functions and they use some combinators to define a result from given arguments. Then it assumes the correctness of Typed- λ Calculus.

Let's assume a function F that has a Type defined by set of rules R and some set of initial conditions J_i ($i=1 \dots n$) then we can write :

$$F \equiv J_{1 \dots n} \vdash_R \lambda \quad (\lambda \text{ is the result of function that also has a Type defined by } R.)$$

A computer program is a set of such functions and a computer aided proof is a computer program. Thus it can be derived that "A *computer Proof is a program with the thesis being proved is type for that program*". Thus this forms the basis of proof assistants and Functional Programming.

6.3 Hoare Logic

It [35] is a logic system of formal rules to rigorously reason for the correctness of computer programs. The central feature of Hoare logic is the *Hoare triple*. A triple describes how the execution of a piece of code changes the state of the computation. A Hoare triple is of the form

$$\{ P \} C \{ Q \}$$

where P and Q are some 'Quantifiable' assertions and C is a command. P is named the *Precondition* and Q is called as the *Postcondition*. It follows then that, when the precondition is met, executing the command should establish the postcondition. It's basic structure is developed on basis of Axiomatic and Inference rules that are able to reason for concurrency, Procedures, Jumps and Pointers.

6.4 Separation Logic

This logic system [41] is an extension of Hoare system and describes states (as an analogue to basic Axioms in hoare system) as the store or heap, roughly corresponding to the local variables and stack allocated objects. The states are valid if there exists actual physical address (valid register is present) for that state. The various functions basically maps the different states onto one another. The correctness of this transformation is extensively under this logic system. All the data structures are treated in the formal system using separation logic system.

Chapter 7

Proof Assistants

Having discussed some basic ideas of working of a Proof Assistant and theoretical soundness of proving things, let's now get to the programming and implementation. Proof Assistants as stated earlier is a software tool do help in developing formal proofs. As soon as the formal logic system was developed, a very basic theorem prover called *Logic for Computable Functions* (LCF) [37] was developed along with a basic theoretical programming language *Programming Computable Functions* (PCF) [39] was developed.

These were some of the initial works in the field of Proof Assistants. The successors include *Isabelle* [2] and *COQ* [15] . Also the theory advanced to reason for working at higher order of complexity called *Higher Order Logics* (HOL).

7.1 LCF - Logic for Computable Functions

This was the first interactive Automated theorem prover that also introduced a new *Domanin Specific Language* (DSL) called ML. It allowed users to generate new tactics (discussed below) to improve proving. The type system of ML ensures that theorems are derived completely by the given inference rules.

LCF shaped the thought process of how to write a type of a function to finally verify it by using terms like *Inclusive* rules, *Conditional* rules, λ rules, *Truth* rules. It also introduced the idea of tactic logics and put forward a *Read Evaluate Print Loop* (REPL) type approach of proof assistant. It worked by breaking a given hypotheses into smaller forms. The smaller forms often are Lemmas on which the proof depends.

The LCF-style inference kernel used in many proof assistants consists of two layers: proof objects and theorems. Proof objects are represented as concrete datatypes. Depending on system parameters, a varying amount of information is maintained here. In any case, there is a complete record of oracles used in the proof. An explicit λ -term representation of the proof is also possible, but requires significantly more resources. Theorems are elements of an abstract datatype thm.

According to the original LCF tradition, theorems are proven propositions that have been certified by the kernel module, which implements primitive inferences as abstract datatype constructors. Any operation on theorems has to go through that kernel, so any value of type thm is *correct by construction*.

7.2 Tactics

The forward reasoning of proving a hypothesis consisting of first proving sub-hypotheses say A and B and finally concluding that if A is proved and B is proved we have proved $A \hat{\wedge} B$ (our main hypothesis). The backward reasoning says to prove $A \hat{\wedge} B$, we need to prove A and B . **Tactics** basically run inference rules forward or backwards to help break down proving things. It may also apply a predefined lemma (\simeq function application), split up a lemma about some inductive type (in that particular step of proving) into a case for each constructor, and so on.

Basic tactics may succeed or fail depending upon the context in which they are applied. More advanced tactics are like little functional programs that run the basic tactics, perform pattern matching over the terms in the goal and/or assumptions, make choices based on the success or failure of tactics, and so forth. More advanced tactics deal with arithmetic and other specific kinds of theories. The key paper on Coq's tactic language is the following:

7.3 De Bruijn criterion

One may ask how to verify the verifying system being used. **De Bruijn criterion** answers this by ensuring that proof assistants create an independently checkable proof object while the user is interactively proving a theorem. These proof objects should be checkable by a program that a skeptic user could easily write him/herself. De Bruijn's Automath systems were the first to specifically focus on this aspect and therefore this property was coined De Bruijn criterion by Barendregt (Barendregt & Geuvers 2001). In De Bruijn's systems, the proof objects are basically encodings of natural deduction derivations that can be checked by a type checking algorithm.

Chapter 8

Programming Language Verification

Any computer programming language consists of type system to its core supported by data structures and control flow techniques. Any Programming language finally can be broken down to machine codes resulting in performing specified actions. The assembly level machine codes should theoretically represent the same semantics as implied by the higher order programming language. **Programming Language Verification** in the broader sense is checking that programs in one programming language is equivalent to the one in other. Generally this equivalence is tested between a high order language and a small low level C language.

The basic understanding of how the verification work can be thought as, say there are two programs e_1 and e_2 then to prove they are equivalent they must meet the following criteria :

- Input Parameters as well as the context in which the program is being run is same. Input parameters can be thought as some initial values to a program. Input parameters should necessarily be of same type. The context refers to same hardware used as well as the scope of the program in consideration is maintained same. Context can then again be broken as a *Global* context and a *Local* context.
- The result should be same and should be of the same type. It is necessary that any program should not violate any of the bonding rules imposed by context.

How do we check this ?

Well the method used is called **Step-Indexing**[\[12\]](#). In this method we look for holes in a program. These holes represented by C_i can be thought as a program's loop structure or the way conditional arguments have been put up. In any step of a program thus if we are able to show that at that point, $C_2[e_1]$ terminates and $C_1[e_2]$ terminates also producing same results then we have shown the programs are equal. The cardinality issues related to number of such possible holes in programs increases the complexity of verification proof. This method therefore says that we should unfold the program upto required N steps and consider the equivalence then.

The areas where this method sticks is where we are unable to predict N. This is the case of IO type functions. The work on this is going on and improved proofs are being generated.

8.1 Verified Software Toolchain

This project [29] led by Princeton University deals with verifying software programs and compilers. It is an advancement to CompCert. It uses multi layered kernels to securely verify the programs.

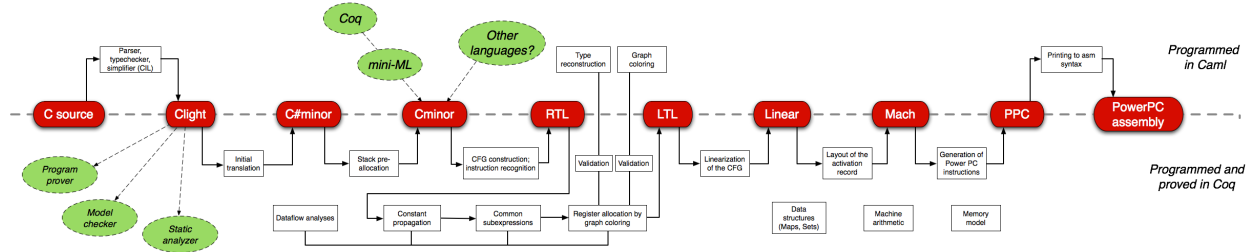


Figure 8.1: Flow diagram of CompCert C (Source: Official Site)

The software toolchain includes static analyzers to check assertions about programs; optimizing compilers to translate programs to machine language operating systems and libraries to supply context for programs. Verified Software Toolchain verifies with machine-checked proofs that the assertions claimed at the top of the toolchain really hold in the machine-language program, running in the operating-system context, on a weakly-consistent-shared-memory machine.

Chapter 9

Complete Programming Stack Verification

A computing programming stack being referred here is basically classical toolchain that includes all the tools from low level to high level. It includes compilers, linkers, debuggers, Database Management System (DBMSS) and interfacing kernels. The idea of stack verification is to check the correctness of all these technologies. Assuming the logical model of each of the software we can use the reasoning of Curry-Howard correspondence and Hoare Logic to construct a verifiable proof for each one.

9.1 DeepSpec Project

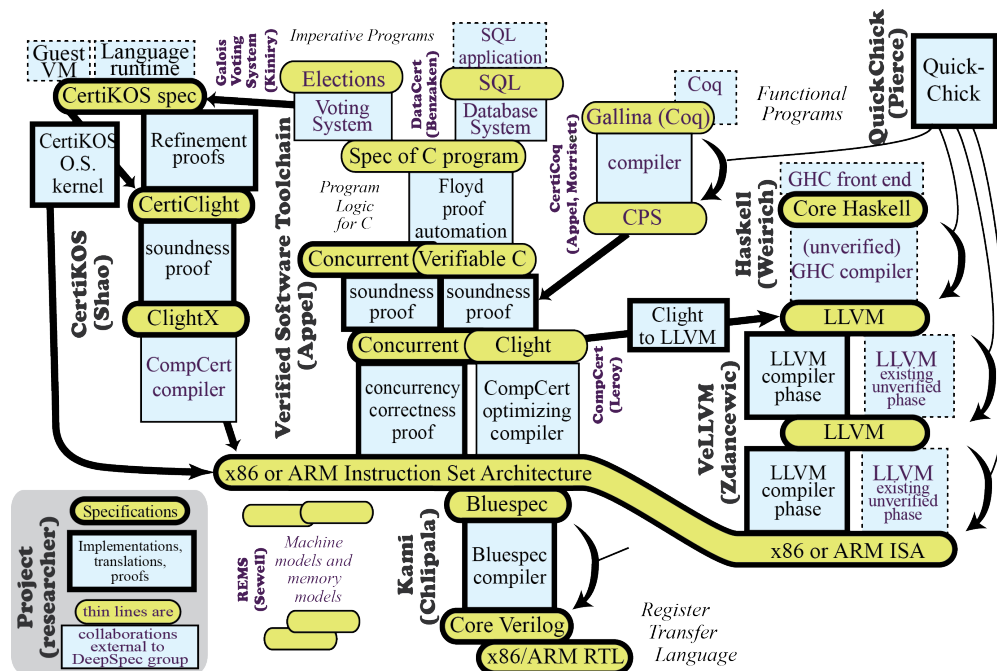


Figure 9.1: The DeepSpec Project (source : Official Site of DeepSpec project)

This project [7] is umbrella project of many important projects like CertiCoq, VST, VELLVM, CertiKOS in the field of software verification. The subprojects deal with checking different software tools and also the operating system semantics. Thus the project DeepSpec overall verifies the computer stack.

A very basic explanation reads as provided in the manual, :

At the top of the diagram are specifications of application-level interfaces such as the semantics of the SQL query language or the rules governing fair Elections. At the bottom is the semantics of the RTL (register-transfer language) implementation of some specific microprocessor, say x86. A voting or database system is implemented as a C program with a specification Spec of C program, written in a program logic called Verifiable C. We prove (in Coq) that the C program correctly implements SQL by applying the program logic to the program, with the assistance of the Floyd proof automation system. Verifiable-C is a set of reasoning rules for program correctness; whatever properties are proved about a program will actually hold when the program runs; this is formalized in Coq as the soundness proof connecting Verifiable C to the operational semantics of C-light. The CompCert optimizing compiler compiles C light to machine language in ARM, PowerPC, or x86-ISA. The correctness of CompCert's translation is proved in Coq. Finally, the hardware is correct: the instruction set architecture x86-ISA is specified in the Bluespec hardware-description language, which itself has a deep specification defining its precise semantics. The Kami synthesizer translates this to register transfers written in Verilog (which, again, itself has a deep specification).

It would be a major breakthrough to completely build a coherent system of specified/verified components whose specification deals only with the semantics of some very basic technologies like for example Verilog and SQL.

Chapter 10

Quantum Computing Overview

$$\boxed{i\hbar \frac{\partial}{\partial t} |\psi(t)\rangle = \hat{H} |\psi(t)\rangle}$$

Quantum Computing [19] is doing computation using quantum-mechanical phenomena like superposition and entanglement. A Quantum computer performs such computing and is very different from a classical computer that depends on digital logics and transistors. Quantum computation uses Quantum bits also called *Qubits* which align in different ways to produce variations of not just 1 or 0 (as in classical computing) but many others. An approximation based on probability of outcome which depends on solving governing physics equations, theoretically allows us to construct subsequent logic of state or data changes.

A Quantum Turing Machine is theoretical accepted model of quantum computer which has been made possible by the initial works of **Richard Feynman**, **David Deutsch** and **Yuri Manin**. The actual implementation model of the quantum computer however is still in a very basic phase because of very absurd Temperature-Pressure conditions required to maintain a very less stable qubit state. The current research in quantum computing hardware focuses on precisely maintaining qubit's state and reducing data loss during state transition. The implementational software aspect of quantum computer is explored even smaller much because of less compatible quantum hardware and the inability of classical computers to simulate large qubit quantum computer.

10.1 Qubits

Quantum bits or Qubits are quantum version of classical binary bit. It is the basic unit of quantum information. Unlike the classical bits the Qubit can maintain information in unique states or in a superposition of states or may be entangled states. The possibility of having so many states improves the parallelism of data computation and hence speed. A very good example is often portrayed by finding the card in which given 4 cards with one card different, we need to find the odd one out. The classical computer takes a maximum of 4 steps but a quantum computer will take always only 1 step to figure it out.

10.1.1 Representation

Quantum State refers to the state of isolated quantum system. It gives a basic idea of possibility of different observable outcomes. A Qubit state can be represented by conventional "Dirac" or

"bra-ket" notation, written as $|0\rangle$ or $|0\rangle$ and read as "ket-0" or "ket-1". two orthonormal *Basis States* $\{ |0\rangle , |1\rangle \}$ form 2-D linear vector space of qubit. Qubit basis states combine together to form *Product Basis Space* like,

$$|00\rangle = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \end{bmatrix} \quad |01\rangle = \begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \end{bmatrix} \quad |10\rangle = \begin{bmatrix} 0 \\ 0 \\ 1 \\ 0 \end{bmatrix} \quad |11\rangle = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

A pure qubit state is a coherent superposition of the basis states. This means that a single qubit can be described by a linear combination of $|0\rangle$ & $|1\rangle$.

$$|\psi\rangle = \alpha|0\rangle + \beta|1\rangle$$

with condition that

$$\alpha^2 + \beta^2 = 1 \dots (i)$$

The probabilities of finding states of values $|0\rangle$ & $|1\rangle$ is proportional to $|\alpha|^2$ and $|\beta|^2$ respectively.

The other type of representation is called **Bloch Sphere Representation** in which α and β are considered as complex numbers with two degrees of freedom and a normalization constraint of equation (i).

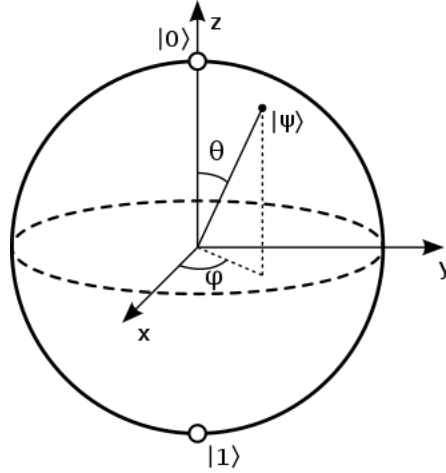


Figure 10.1: Bloch Sphere Representation of a Qubit (source : The Quantum Computing & Quantum Information book)

$$\alpha = e^{i\psi} \cos \frac{\theta}{2} \quad \& \quad \beta = e^{i(\psi+\phi)} \sin \frac{\theta}{2}$$

10.1.2 Quantum Superposition

Superposition is essentially the ability of a quantum system to be in multiple states at the same time that is, something can be here and there, or up and down at the same time.

10.1.3 Quantum Entanglement

It is a physical phenomena which that is said to occur when pairs or groups of particles are generated or they interact or share same spatial proximity such that behaviour of any one of the particles can not be defined independently of the state of others. Entanglement is an extremely strong correlation that exists between quantum particles so strong, in fact, that two or more quantum particles can be inextricably linked in perfect unison, even if separated by great distances. The particles remain perfectly correlated even if separated by great distances. The particles are so intrinsically connected, they can be said to dance in instantaneous, perfect unison, even when placed at opposite ends of the universe. This seemingly impossible connection inspired Einstein to describe entanglement as "spooky action at a distance".

10.2 Quantum Gates & Circuits

A quantum gate or quantum logic gate is a rudimentary quantum circuit operating on a small number of qubits. They are the analogues for quantum computers to classical logic gates for conventional digital computers. Quantum logic gates are reversible, unlike many classical logic gates. Some universal classical logic gates, such as the Toffoli gate, provide reversibility and can be directly mapped onto quantum logic gates. Quantum logic gates are represented by unitary matrices. The action of the gate on a specific quantum state is found by multiplying the vector which represents the state by the matrix representing the gate.

$$\begin{array}{lcl}
 \begin{array}{l} \text{X Gate} \\ \text{Bit-flip, Not} \end{array} & \begin{array}{c} \boxed{\text{X}} \\ \equiv \end{array} & \begin{array}{c} \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} = \beta|0\rangle + \alpha|1\rangle \end{array} \\
 \begin{array}{l} \text{Z Gate} \\ \text{Phase-flip} \end{array} & \begin{array}{c} \boxed{\text{Z}} \\ \equiv \end{array} & \begin{array}{c} \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} = \alpha|0\rangle - \beta|1\rangle \end{array} \\
 \begin{array}{l} \text{H Gate} \\ \text{Hadamard} \end{array} & \begin{array}{c} \boxed{\text{H}} \\ \equiv \frac{1}{\sqrt{2}} \end{array} & \begin{array}{c} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} = \frac{\alpha + \beta|0\rangle + \alpha - \beta|1\rangle}{\sqrt{2}} \end{array} \\
 \begin{array}{l} \text{T Gate} \\ \end{array} & \begin{array}{c} \boxed{\text{T}} \\ \equiv \end{array} & \begin{array}{c} \begin{bmatrix} 1 & 0 \\ 0 & e^{i\pi/4} \end{bmatrix} \begin{bmatrix} \alpha \\ \beta \end{bmatrix} = \alpha|0\rangle + e^{i\pi/4}\beta|1\rangle \end{array} \\
 \begin{array}{l} \text{Controlled Not} \\ \text{Controlled X} \\ \text{CNot} \end{array} & \begin{array}{c} \begin{array}{c} \bullet \\ | \\ \boxed{\text{X}} \end{array} \begin{array}{c} \bullet \\ | \\ \oplus \end{array} \\ \equiv \end{array} & \begin{array}{c} \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} a \\ b \\ c \\ d \end{bmatrix} = \begin{array}{l} a|00\rangle + b|01\rangle + \\ d|10\rangle + c|11\rangle \end{array} \end{array} \\
 \begin{array}{l} \text{Swap} \\ \end{array} & \begin{array}{c} \begin{array}{c} \times \\ | \\ \times \end{array} \\ \equiv \end{array} & \begin{array}{c} \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} a \\ b \\ c \\ d \end{bmatrix} = \begin{array}{l} a|00\rangle + c|01\rangle + \\ b|10\rangle + d|11\rangle \end{array} \end{array}
 \end{array}$$

Figure 10.2: Some common Quantum Logic gates (source: wikipedia)

A **Quantum Circuit** is an ordered logical combination of quantum gates and some initial

Qubits. The quantum circuits are reversible transformations on a quantum mechanical analog of an n-bit register. It is very important for any logic circuit to be reversible to return n bit data output for a n bit data input. In other words the reversible logic gates are bijective mappings from a n bit data set to itself. **Kitaev** mentioned that irreversible circuits increase the physical entropy of the system. Theoretical advancements showed that **Toffoli Gate** is a Universal reversible Gate. this basically mean that we can easily construct a large sizes circuit by simply adding more gates (Toffoli gates as example here) to it.

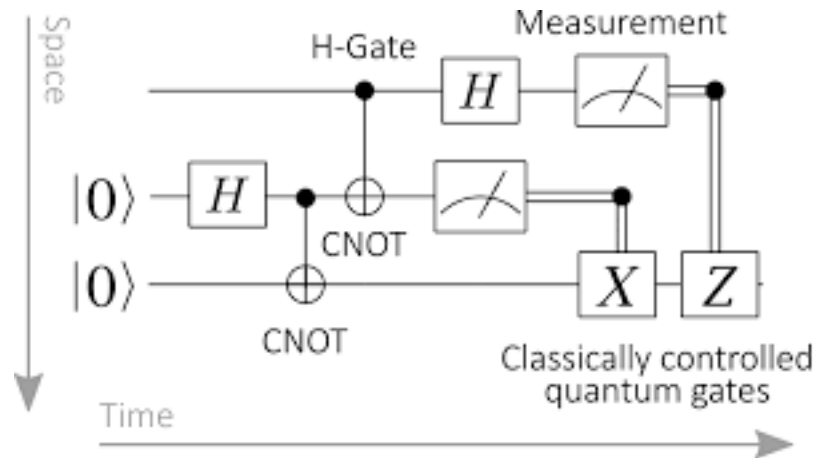


Figure 10.3: An example of a quantum circuit (source : The Quantum Computing & Quantum Information book)

10.2.1 Error Correction

Error Correction is very important for scalability of a quantum computer. An error correction of 10^{-2} means we can add atmost 100 logic gates before running into complete mess and even destroying the qubit chip. Research going on is trying to reduce this error correction to as low as possible.

Chapter 11

Quantum Computing Programming Languages

Quantum Programming is assembling quantum programs capable of running on a quantum computer. A Quantum program is used to express quantum algorithms using higher level and much abstract constructs.

Quantum Instruction Set are quantum equivalent of classical Instruction Set Architecture (ISA). They are machine level 'codes' and are assumed to work on and control functionality of ion traps and Qubits. Ion Traps basically control the very very small energy pulses fed to a superconducting chip.

11.1 Quantum Computing SDKs

Quill[40] and **QASM**[6] are most referred quantum Instruction Set Architectures. QASM has been made open sourced under the QisKit project by the IBM's Q Experience. The Quantum computing SDKs available now are helpful in running some quantum algorithms on a simulator as well as on hardware system (if you have one). Some examples of common one are :

- **Project Q**[46] is the most recent open sourced project developed by ETH Zurich and it uses python programming to develop quantum logics and circuits. The code produced is either tested on a simulator or run on IBM's Quantum cloud.
- **QisKit**[14] is the most documented and used project by IBM and is widely used all over the world. It has tutorial that can be accessed here. It's machine implementation uses QASM.
- **Forest**[24] project developed by Rigetti, which uses the Python programming language to create and manipulate quantum circuits. Results are obtained either using simulators or prototype quantum devices provided by Rigetti. It uses Quill ISA.

11.2 Imperative QC Languages

- **Quantum Computation Language (QCL)**[21] is one of the first implemented quantum programming languages. It is an extension of C programming language and has support for user defined operators and functions. Its data structure is quite similar to C.

- **Q language** is the second implemented quantum programming language and it provides conventional class based support for different quantum logic operations. It is complied and tested on a simulator.
- **QMASM**[17] is a low-level language specific to quantum annealers ¹ such as the D-Wave.

11.3 Functional QC Languages

- **QML** is a Haskell-like quantum programming language by Altenkirch and Grattage. QML introduces both classical and quantum control operators, whereas most other languages rely on classical control.
- **LIQUi|** >[23] is a quantum simulation extension on the F# programming language. It is currently being developed by the Quantum Architectures and Computation Group (QuArC) part of the StationQ efforts at Microsoft Research.

11.4 Ongoing Development & Quantum Stack

The paper [4] extensively details about the actual working aspects of a quantum computer. I here by quoting the self explanatory figure used the authors that explain that a quantum computer would essentially be like an external device that will be controlled by a classical computer. The have further explained how the instruction pipelining is being designed and parallelism of Qubits is used.

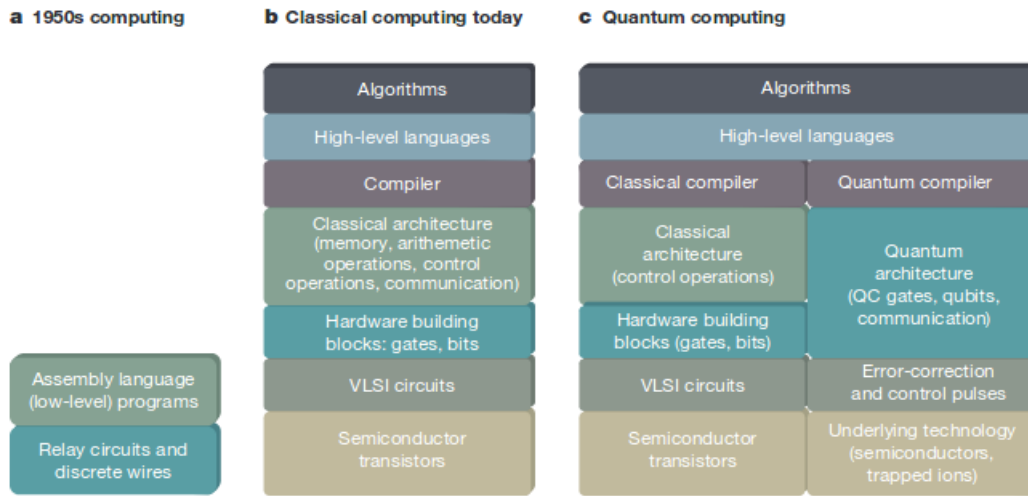


Figure 11.1: The different programming stacks (source : The technical paper by Friedric Chong.[4])

¹Quantum annealing (QA) is a metaheuristic for finding the global minimum of a given objective function over a given set of candidate solutions (candidate states), by a process using quantum fluctuations.

Chapter 12

Quantum Computing and Type Theory

Programming languages are often recognized as Procedural and Descriptive corresponding to performing a given n-step logic in a program. Quantum Programs are verified throughly using Formal methods. A qubits working semantics are completely writtten as mathematical abstractions and then verified with some given initial conditions. Doing QC¹ things this way is helpful as we are still not capable enough to have a practical quantum computer to try out things on. As in case of **Stack Verification** of classical computing systems, complete verification for quantum computers is also done. Some of the most recent works include *Steve Zdancewic's* paper[22] that discusses the **QWire** as a core language for Quantum circuits, an analogue to VHDL and HDL in classical computing. He has verified its correctness too in CoQ[COQ]. He describes Phantom Types as the type system he used for Qwire.

EpiQC[8] is a multi-institutional paradigm focused on reducing the current gap between existing theoretical algorithms and practical quantum computing architectures. They have a very nice video series[9] that nicely explains the application of program verification in Quantum computing. They refer to Technology-Aware Programming Environment as an upgrade to traditional optimization and abstraction techniques to one that is more practical to use. Major focus is upon developing better tools for robustness and scalability.

- **Procedural Implementations** include verifying step-by-step correctness of program semantics. **QCL** as described earlier is a programming language that has Procedural behaviour inherited from classical languages. It is now being verified .
- **Descriptive Implementations** involves verifying the program logic only. For example, a sum function should return summation of input parameters correctly. The procedural verification aspect of same would mean to check the register allocation and machine cycle logic verification.

12.1 Phantom Types

Phantom Types [28] is the lightweight dependent type system used by Qwire. Most of the mathematical work involves matrices and using phantom types have increased the functional expressivity of the same. The complete implementation cna be found here

¹Quantum computing

Chapter 13

Applications in Machine Learning

13.1 Certigrad

A recent paper[26] by *Daniel Selsam* verifies the computation graph of machine learning model. The project **Certigrad**[25] explains that it is very possible that conventional debugging techniques fail to explain ambiguous nature of a ML model failure (like memory overflow and corruption) that may be a result of setting some hyper-parameters or a flaw in program logic. Say for example the one of the functions diverge at a certain point then it might be very difficult to track that simple using a debugger. However when verified using a proof assistant, it correctly suggests that the unexpected behaviour is outcome of mathematical error and not program logic. Similar logic can thus be extended to verify computation graphs of every machine learning model.

13.2 Automated Proving

While correcting ML model using program verification, new kind of proof assistant **Proving Ground**[10] by Professor *Siddhartha Gadgil* is also being developed that uses AI to help reduce steps furthermore.

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