

BiasWorker

Parameterized Width and Debug

Width	Debug	FMax (MHz)	DFFs	6-LUTs
32	0	360	354	493
32	1	381	567	842
64	0	337	462	686
64	1	310	673	974
128	0	261	678	983
128	1	261	887	1300
256	0	166	1110	1683
256	1	166	1319	1952

Test Conditions:

ISE 13.3, XST defaults, Virtex6

SMAadapter

Parameterized Width and Debug

Width	Debug	FMax (MHz)	DFFs	6-LUTs
32	0	270	819	1638
32	1	277	1197	2075
64	0	278	1099	2101
64	1	277	1478	2280
128	0	259	1659	2365
128	1	258	2034	3080
256	0	222	2775	4035
256	1	222	3151	4365

Test Conditions:

ISE 13.3, XST defaults, Virtex6

DelayWorker

Parameterized Width and Debug

Width	Debug	FMax (MHz)	DFFs	6-LUTs
32	0	244	2132	3478
32	1	255	2531	4032
64	0	253	2248	3652
64	1	255	2649	4223
128	0	256	2463	4107
128	1	255	2864	4557
256	0	246	2894	4651
256	1	255	3295	5230

Test Conditions:

ISE 13.3, XST defaults, Virtex6

OCDP

Parameterized Width and Debug

Width	Push, Pull, Debug	FMax (MHz)	DFFs	6-LUTs
32	001	234	2681	4359
32	011	205	3209	5104
32	101	205	3227	5313
32	111	183	3360	5995
64	001	234	2718	4283
64	011	190	3252	5168
64	101	210	3315	5089
64	111	186	3399	5811
128				
256				

Test Conditions:

ISE 13.3, XST defaults, Virtex6