

OpenCPI FPGA Node Logical Hierarchy

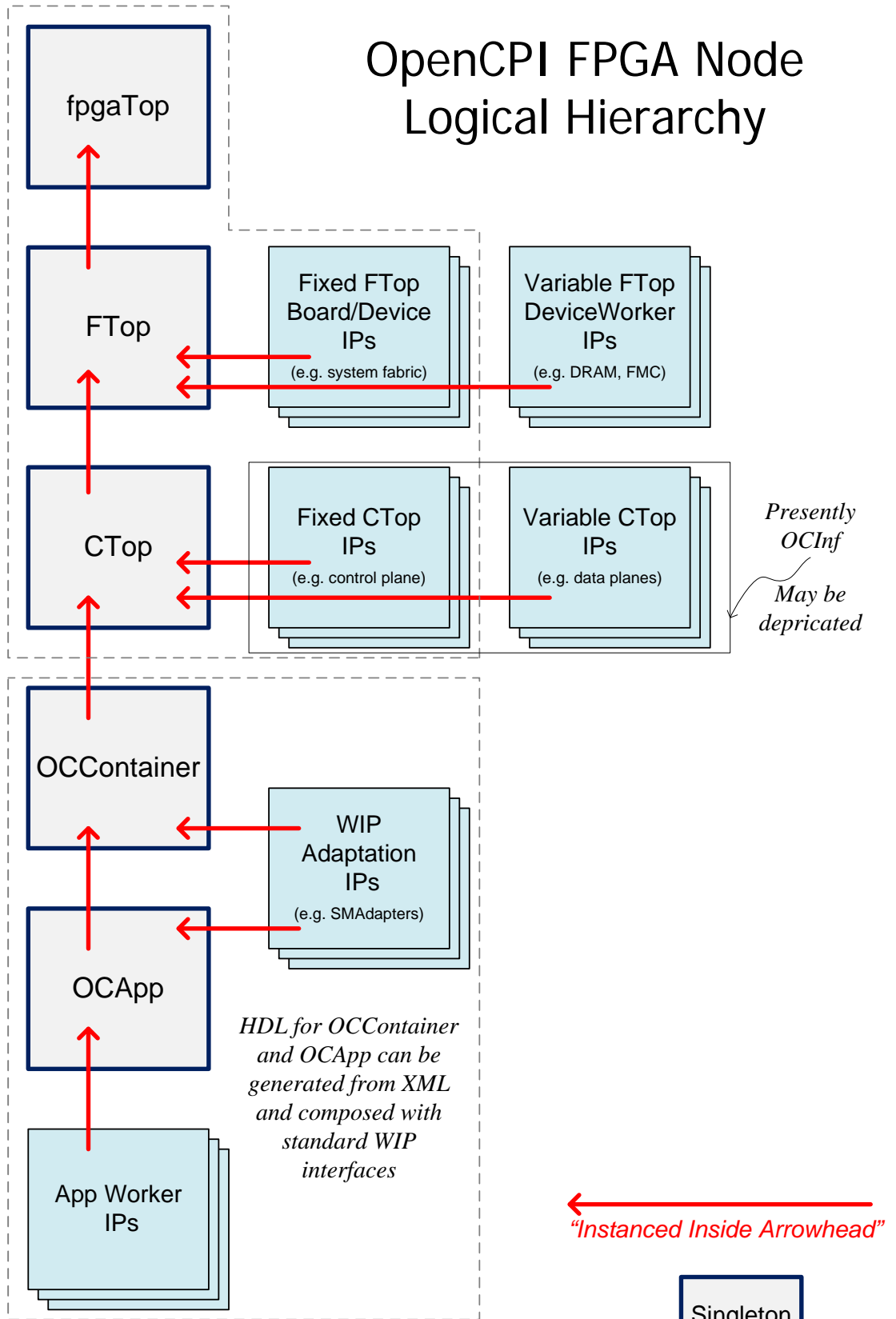
fpgaTop is a HDL wrapper instantiating FTop that normally has no other IP

FTop is the instance of device, board and platform-specific logic

CTop is the instance of portable infrastructure, platform-agnostic IP

The OCContainer is generated to match the application to the CTop platform

OApp is the generated collection of application workers



Dark Blue Box is Singleton per FPGA Node