ECEC-355

Project 5: Pipelining RISC-V Simulation with Data Forwarding

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1 Introduction

You may work on this project in a team of up to two members. This project is due on August 29, 2020.

2 Required Reading

Chapter 4, The Processor, Sections 4.5-4.7

3 Task

Integrate your pipeline simulator with a forwarding unit. Test your simulator using $../cpu_traces/project_five$ with the following configurations:

- x1 = 0; x5 = 26; x6 = -27
- 40(x1) = 100

4 Submission

- 1. Summarize your experiment in 3. Compile your report in PDF format.
- 2. All the source codes.
- 3. Zip above and submit through Bblearn.