TV Circuits



TBA970 Television Video Amplifier

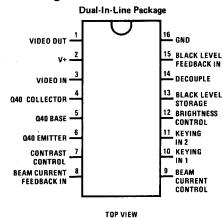
General Description

The TBA970 is a monolithic video amplifier for television receivers. The circuitry includes a video preamplifier, DC contrast control utilizing a linear potentiometer which can be ganged to the chroma gain control, beam current limiting via contrast. Beam current limiting could be obtained with either positive or negative control voltage. Black level control is achieved by a clamped feedback circuit combined with the brightness control. Emitter follower output could be used to directly drive the video output stage. A separate NPN transistor (Q40) is provided on the chip.

Features

- DC contrast control
- DC brightness control
- Black level clamping
- Beam current limiting
- Low impedance output

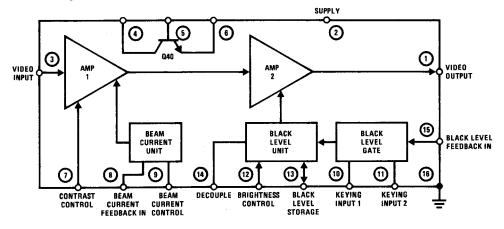
Connection Diagram



Dual-In-Line Package, Order Number TBA970 See NS Package N16A

Quad-In-Line Package, Order Number TBA970Q See NS Package N16C

Block Diagram



11

Absolute Maximum Ratings

 Supply Voltage
 15.5V

 Internal Power Dissipation
 750 mW

 Collector Current Q40
 10 mA

 Power Dissipation Q40
 20 mW

 V_{CEO} Q40
 13.2V

VCES Q40 Operating Temperature Range Storage Temperature Range Lead Temperature (Soldering, 10 seconds) 15.5V -20°C to +45°C -55°C to +125°C 260°C

Electrical Characteristics T_A = 25°C, V⁺ = 15V, See Test Circuit, unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNITS |
|--------|--|------------------|-----|-----|-----|-------|
| 12 | Supply Current | (Note 1) | | 27 | 36 | mA |
| V3 p-p | Peak-to-Peak Input Voltage | (Note 2) | | | 1.6 | Vp-p |
| R3 | Input Resistance | | | 12 | | kΩ |
| | Voltage Gain | | | 2.4 | | |
| | 3.0 dB Bandwidth | | | 6.0 | | MHz |
| | 6.0 dB Bandwidth | | | 9.0 | | MHz |
| | Linearity of Black-to-White Video Output Signal | | 0.9 | | | |
| V15 | Low Black Level Voltage | | | | 0.2 | V |
| V15 | High Black Level Voltage | | 3.0 | | | V |
| | Contrast Control Range | 1.5V ≤ V7 ≤ 4.5V | 36 | | | dB |
| R12 | Input Resistance for Brightness Control | | | 200 | | kΩ |
| ΔV15 | Change of Black Level | (Note 3) | | | 20 | mV |
| V8, V9 | DC Voltage for Beam Current Limiting Inputs | (Note 4) | | 2.0 | | V. |
| | Separate Transistor Q40 Gain | IC = I4 = 1.0 mA | 40 | | | |

Note 1: No input signal and at minimum brightness.

Note 2: With negative-going synchronizing pulse.

Note 3: With constant brightness setting, due to change of picture content, contrast control setting and change in ambient temperature (ΔT_A =

20°C); black level clamping with t_{c} = 1 $\mu s,$ $l_{10} \geq 0.25$ mA, V11 $\leq 0.3 V.$

Note 4: Beam current limiting occurs at V8 ≥ V9.

Test Circuit

