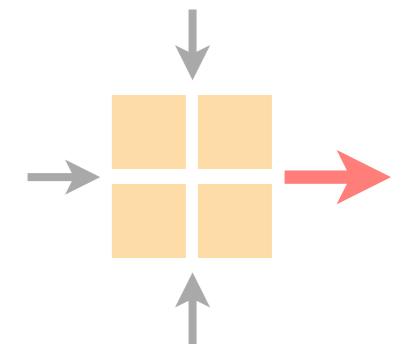


Advanced Topics in Communication Networks

Programming Network Data Planes



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Oct 8 2019

Materials inspired from p4.org

Last week on
Advanced Topics in Communication Networks

P4
environment

P4
language

P4
in practice

What is needed to
program in P4?

P4₁₆ introduces the concept of an *architecture*

P4 Target

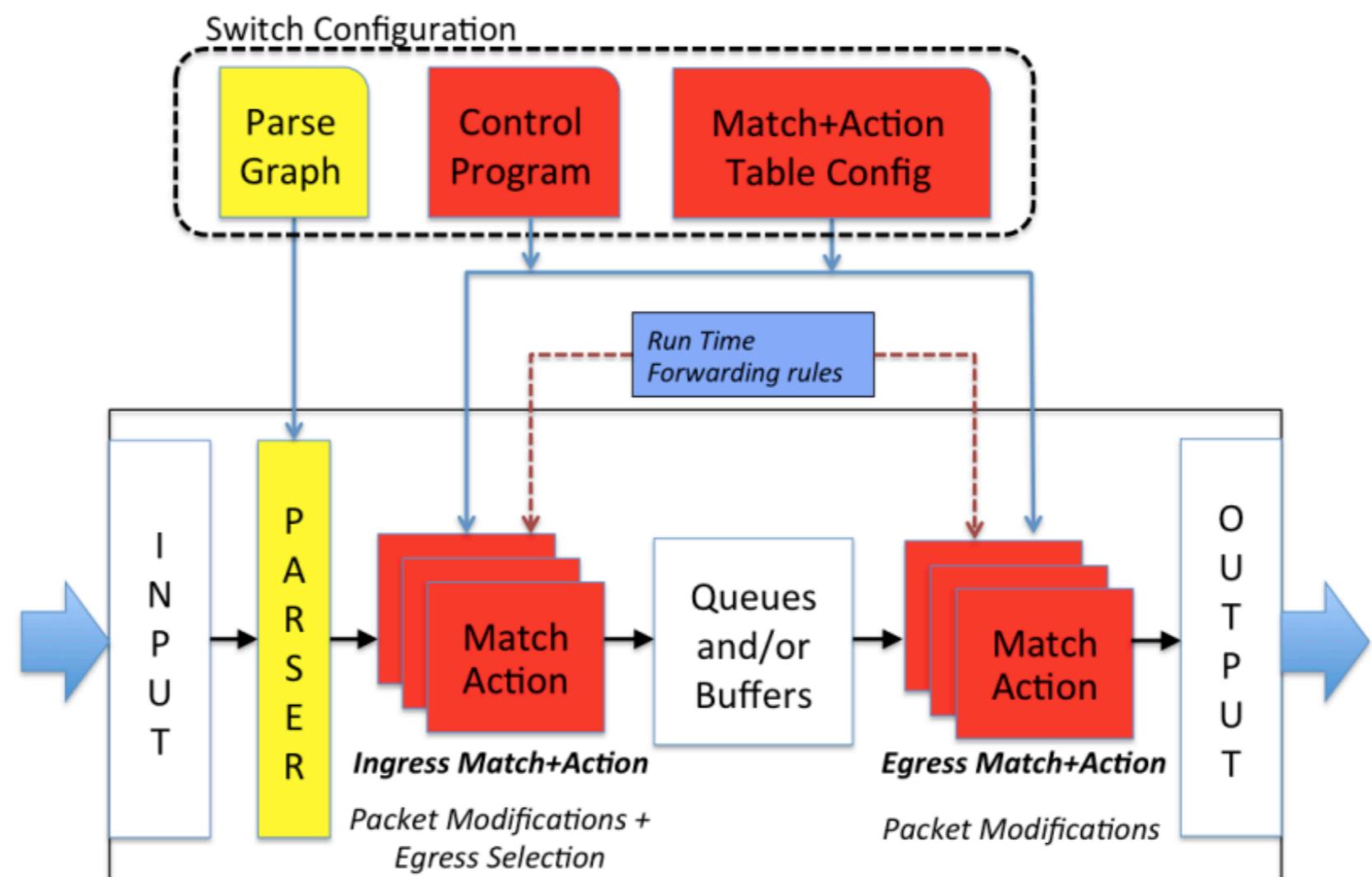
a model of a specific
hardware implementation

P4 Architecture

an API to program a target

We'll rely on a simple P4₁₆ switch architecture (v1model) which is roughly equivalent to "PISA"

v1model/
simple switch



source

<https://p4.org/p4-spec/p4-14/v1.0.4/tex/p4.pdf>

Each architecture also defines a list of "externs",
i.e. blackbox functions whose interface is known

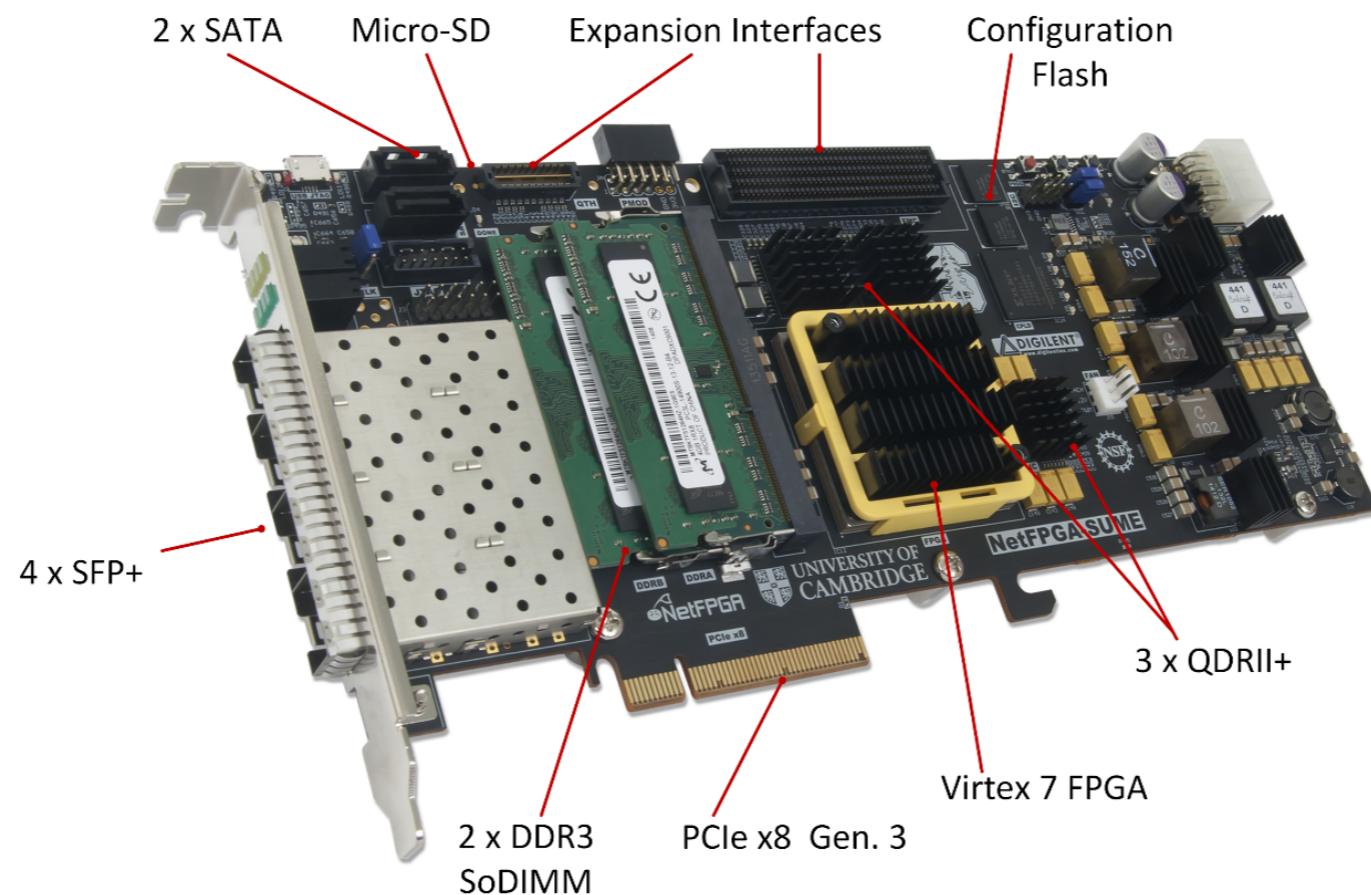
Most targets contain specialized components
which cannot be expressed in P4 (e.g. complex computations)

At the same time, P4₁₆ should be target-independent
In P4₁₄ almost 1/3 of the constructs were target-dependent

Think of externs as Java interfaces
only the signature is known, not the implementation

\neq architectures \rightarrow \neq metadata & \neq externs

NetFPGA-SUME



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more info <http://isfpga.org/fpga2018/slides/FPGA-2018-P4-tutorial.pdf>

P4
environment

P4
language

P4
in practice

Deeper dive into
the language constructs (*)

(*) full info <https://p4.org/p4-spec/docs/P4-16-v1.0.0-spec.html>

P4₁₆ is a statically-typed language with base types and operators to derive composed ones

bool	Boolean value
bit<w>	Bit-string of width W
int<w>	Signed integer of width W
varbit<w>	Bit-string of dynamic length $\leq W$
match_kind	describes ways to match table keys
error	used to signal errors
void	no values, used in few restricted circumstances
float	not supported
string	not supported

P4₁₆ is a statically-typed language with
base types and operators to derive composed ones

Header

```
header Ethernet_h {  
    bit<48> dstAddr;  
    bit<48> srcAddr;  
    bit<16> etherType;  
}
```

Header stack

```
header Mpls_h {  
    bit<20> label;  
    bit<3> tc;  
    bit     bos;  
    bit<8> ttl;  
}  
  
Mpls_h[10] mpls;
```

Array of up to
10 MPLS headers

Header union

```
header_union IP_h {  
    IPv4_h v4;  
    IPv6_h v6;  
}
```

Either IPv4 or IPv6
header is present

only one alternative

P4₁₆ is a statically-typed language with
base types and operators to derive composed ones

Struct

Unordered collection
of named members

```
struct standard_metadata_t {  
    bit<9> ingress_port;  
    bit<9> egress_spec;  
    bit<9> egress_port;  
    ...  
}
```

Tuple

Unordered collection
of unnamed members

```
tuple<bit<32>, bool> x;  
x = { 10, false };
```

P4 operations are similar to C operations and vary depending on the types (unsigned/signed ints, ...)

- arithmetic operations +, -, *
- logical operations ~, &, |, ^, >>, <<
- non-standard operations [m:1] Bit-slicing
 ++ Bit concatenation
- ✗ no division and modulo (can be approximated)

Variables have local scope and their values are not maintained across subsequent invocations

important

variables *cannot* be used to maintain state between different network packets

instead

to maintain state

you can only use two stateful constructs

- **tables** modified by control plane
 - **extern objects** modified by control plane & data plane

This week on

Advanced Topics in Communication Networks

stateful
programming

How do you build
stateful apps?

statefulness
in practice

fast network
convergence

[USENIX NSDI'19]

probabilistic
data structures

bloom
filters

part 1

stateful
programming

statefulness
in practice

probabilistic
data structures

How do you build
stateful apps?

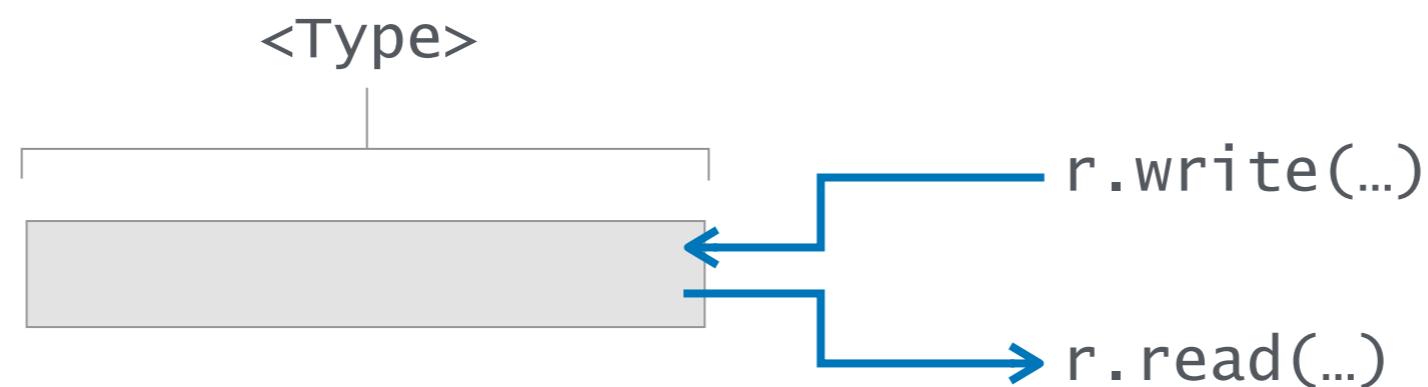
Stateful objects in P4

- Table managed by the control plane
 - Register store arbitrary data
 - Counter count events
 - Meter rate-limiting
 -
- 
- externs in v1model

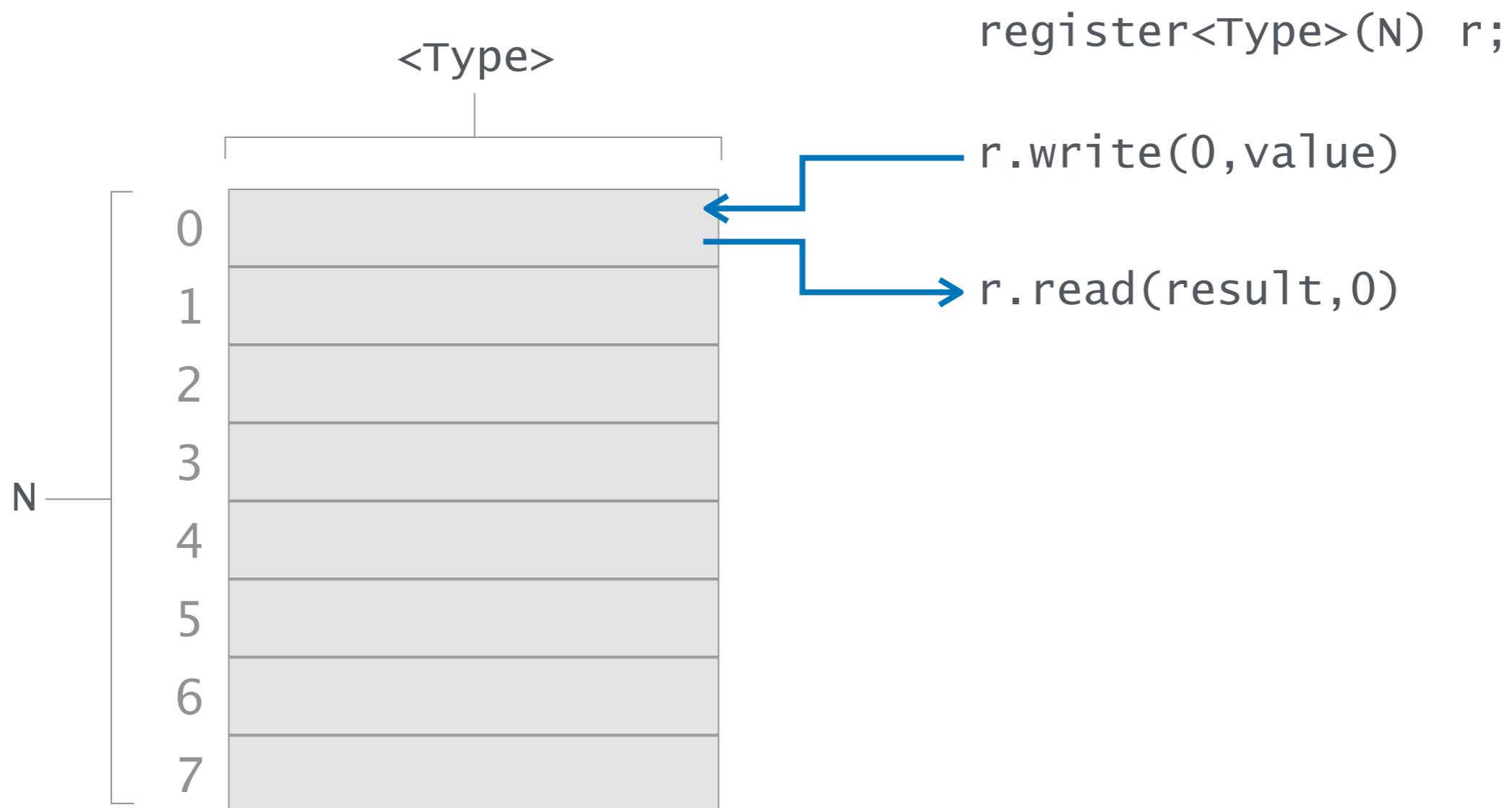
Stateful objects in P4

- Table managed by the control plane
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 -
- 
- externs in v1model

Registers are useful for storing
(small amounts of) arbitrary data



Registers are assigned in arrays



Example: Calculating inter packet gap

```
register<bit<48>>(16384) last_seen;

action get_inter_packet_gap(out bit<48> interval, bit<32> flow_id)
{
    bit<48> last_pkt_ts;

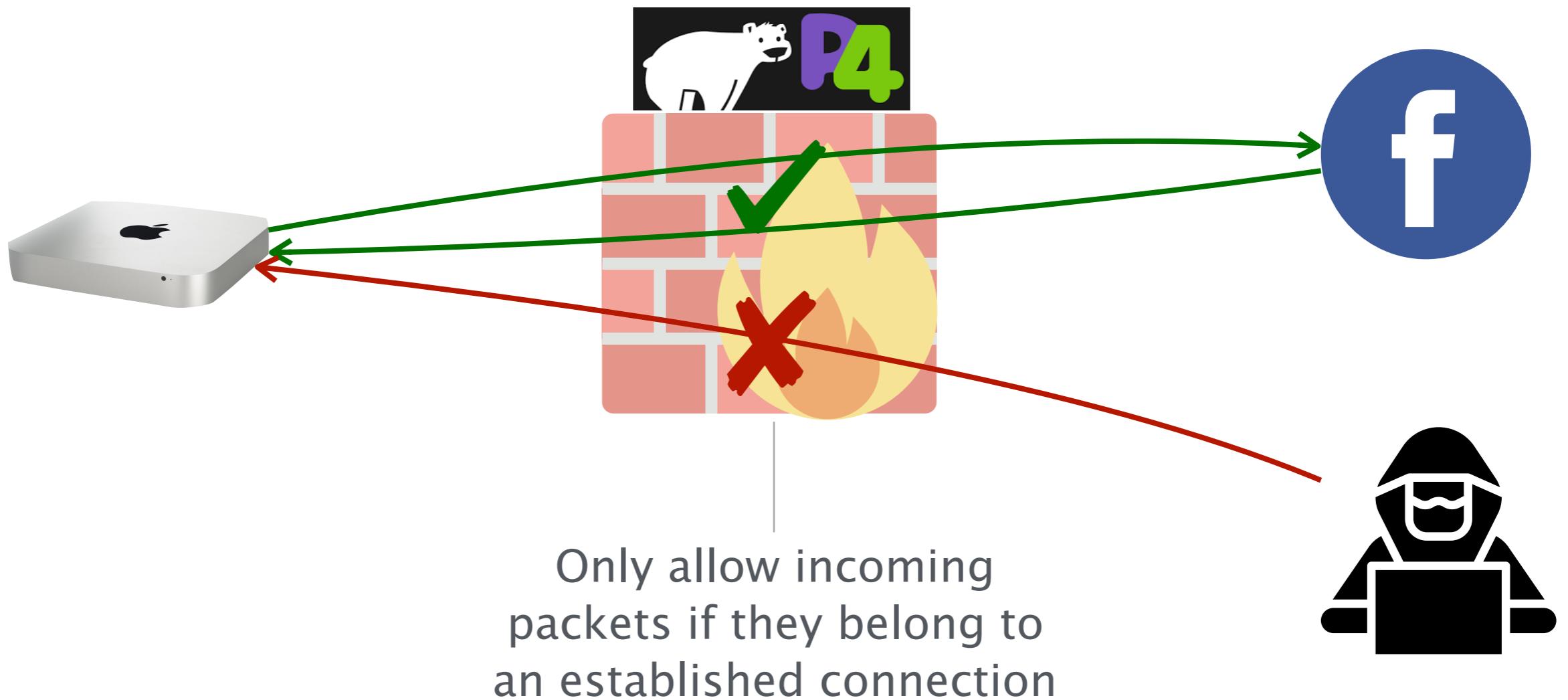
    /* Get the time the previous packet was seen */
    last_seen.read(last_pkt_ts, flow_id);

    /* calculate the time interval */
    interval = standard_metadata.ingress_global_timestamp - last_pkt_ts;

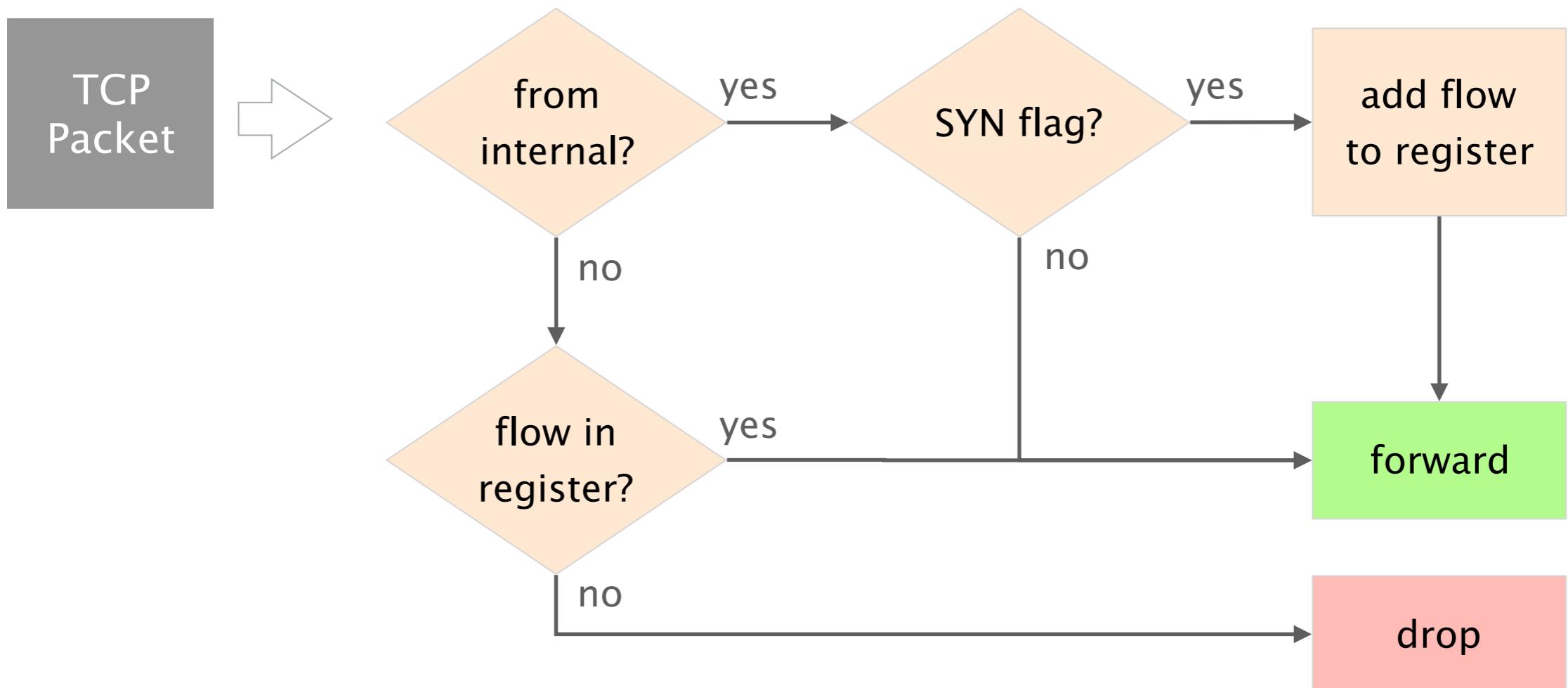
    /* update the register with the new timestamp */
    last_seen.write(flow_id, standard_metadata.ingress_global_timestamp);

    ...
}
```

Example: Stateful firewall



Example: Stateful firewall



Example: Stateful firewall

```
control MyIngress(...) {  
    register<bit<1>>(4096) known_flows;  
    ...  
    apply {  
        meta.flow_id = ... // hash(5-tuple)  
        if (hdr.ipv4.isValid()) {  
            if (hdr.tcp.isValid()) {  
                if (standard_metadata.ingress_port == 1) {  
                    if (hdr.tcp.syn == 1) {  
                        known_flows.write(meta.flow_id, 1);  
                    }  
                }  
            }  
            if (standard_metadata.ingress_port == 2) {  
                known_flows.read(meta.flow_is_known, meta.flow_id);  
                if (meta.flow_is_known != 1) {  
                    drop(); return;  
                }  
            }  
        }  
        ipv4_lpm.apply();  
    }  
}
```

register to memorize established connections

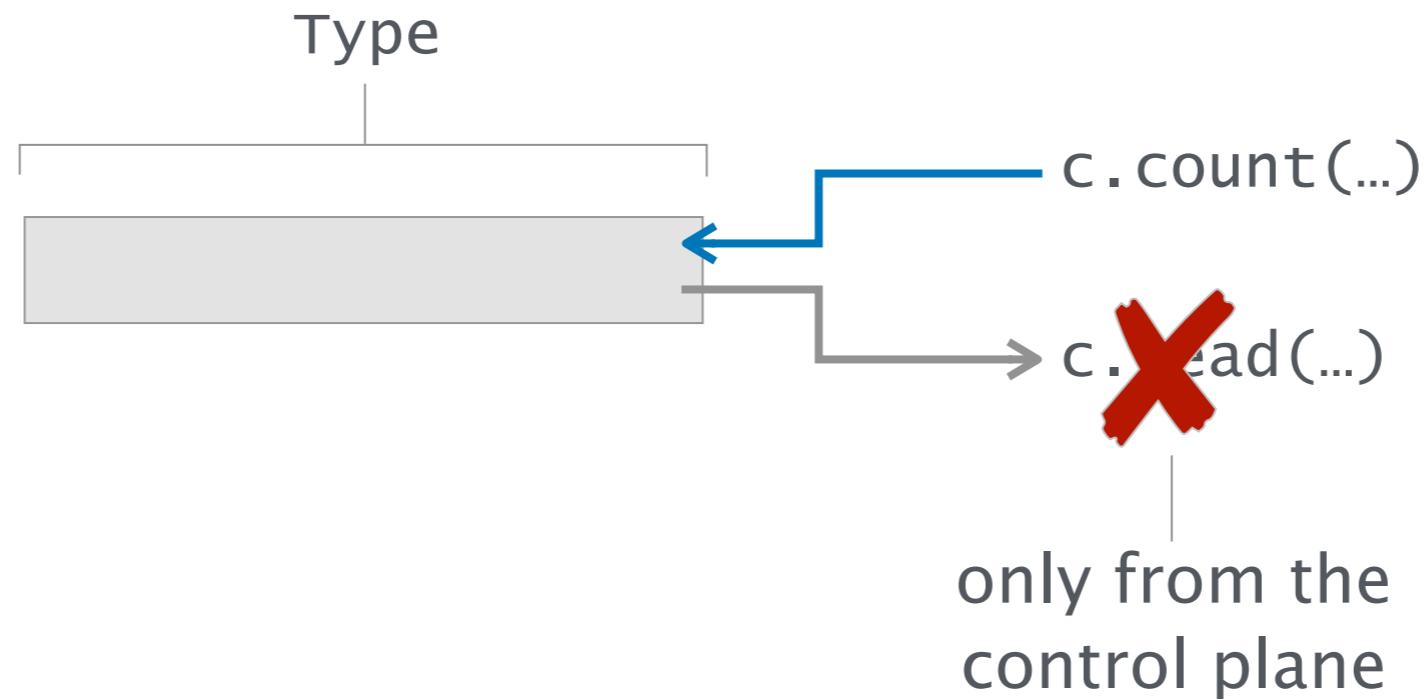
add to register if it is a SYN packet from internal

drop if the packet does not belong to a known flow and comes from outside

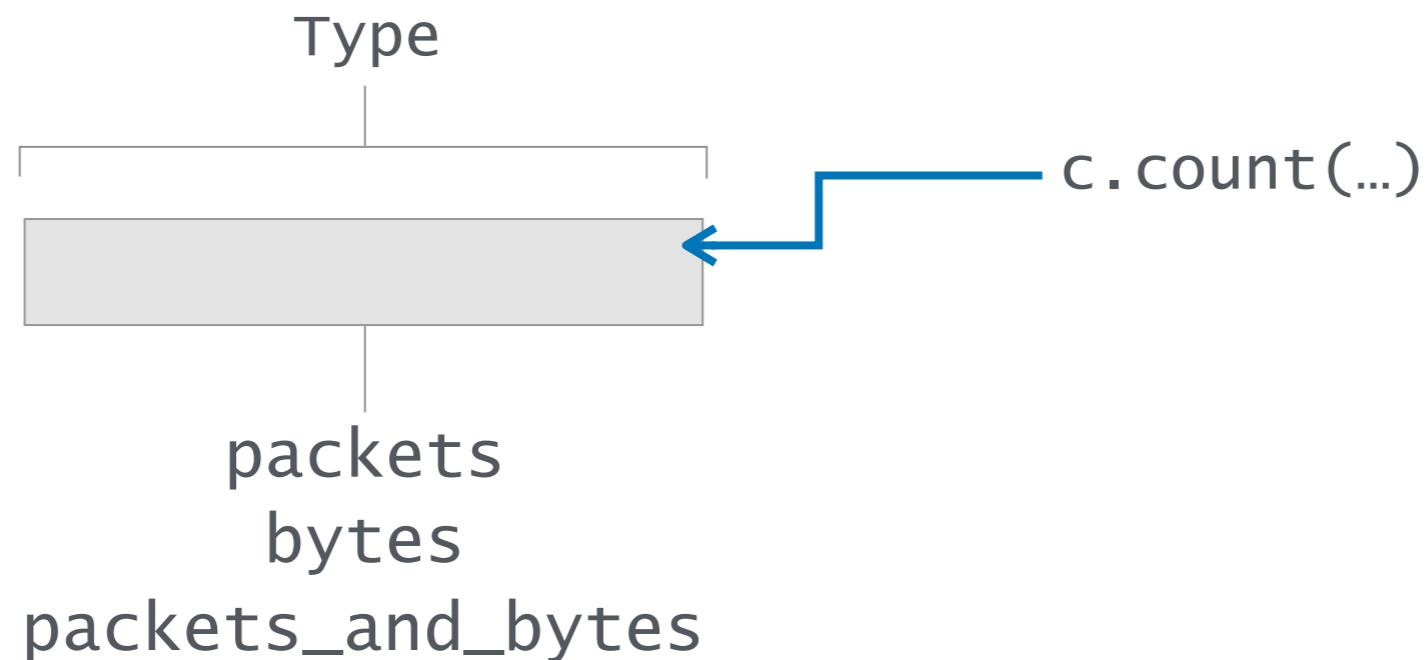
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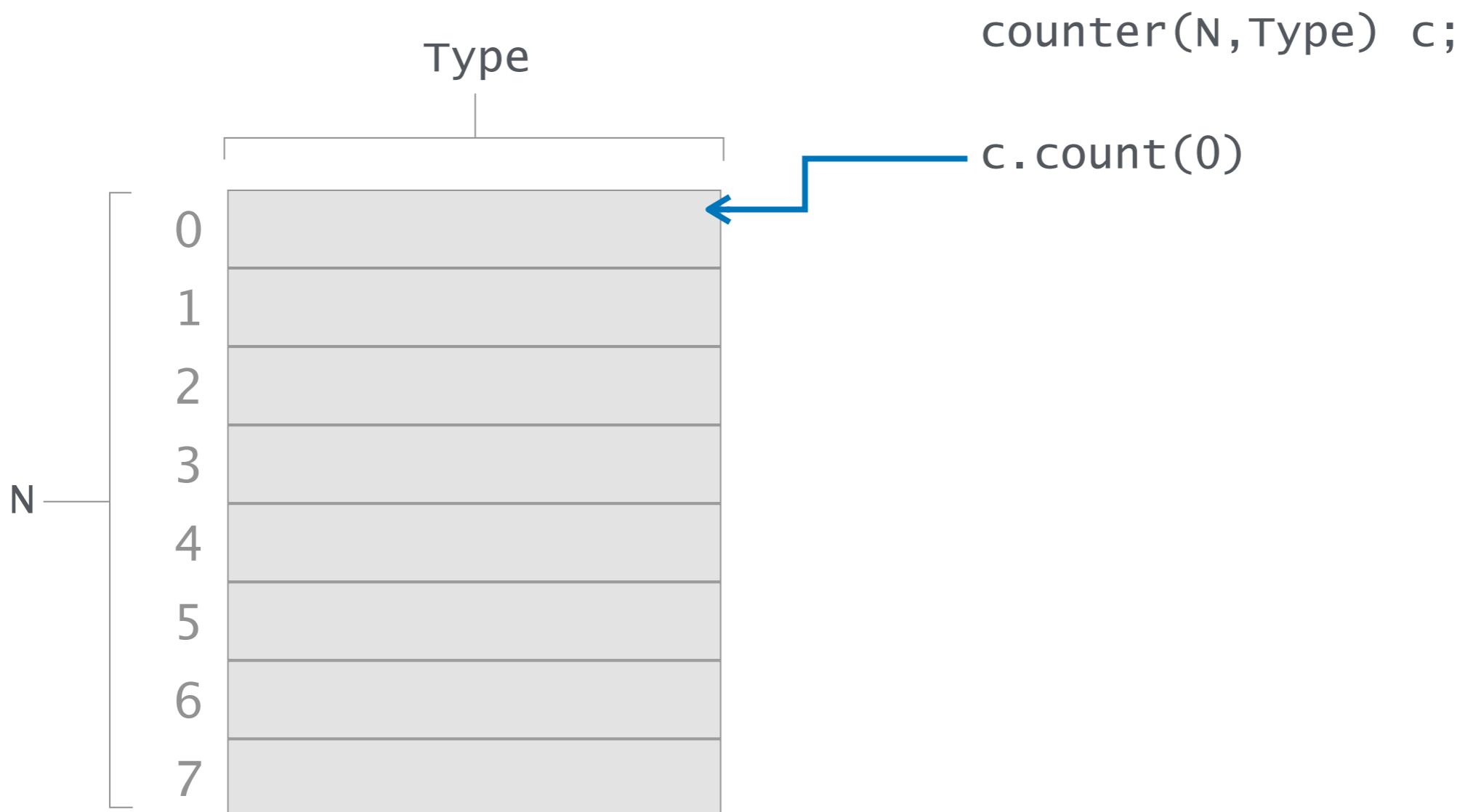
Counters are useful for... counting



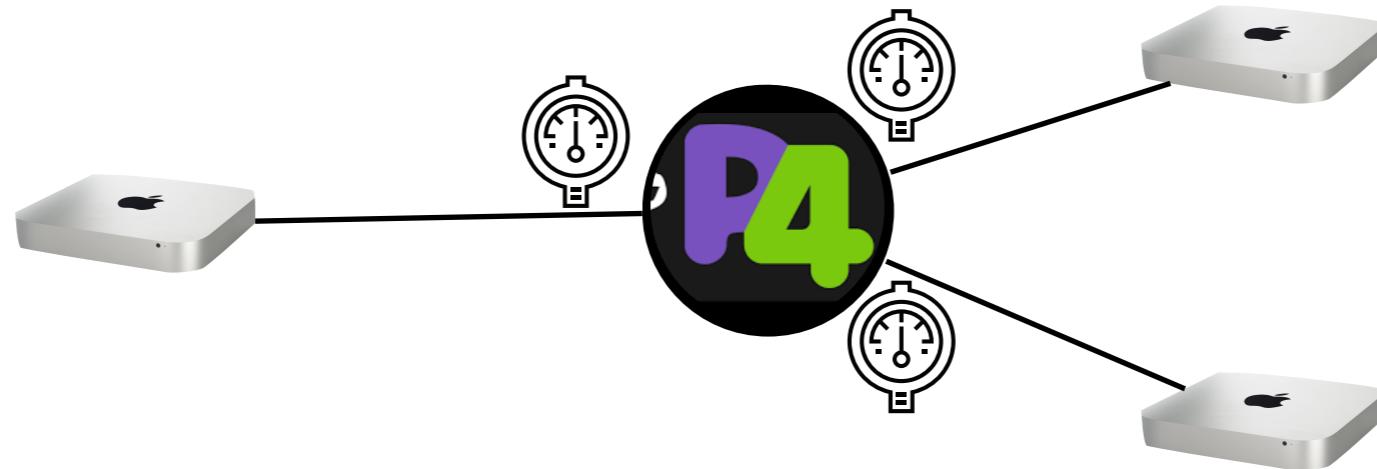
Counters can be of three different types



Like registers, counters
are assigned in arrays



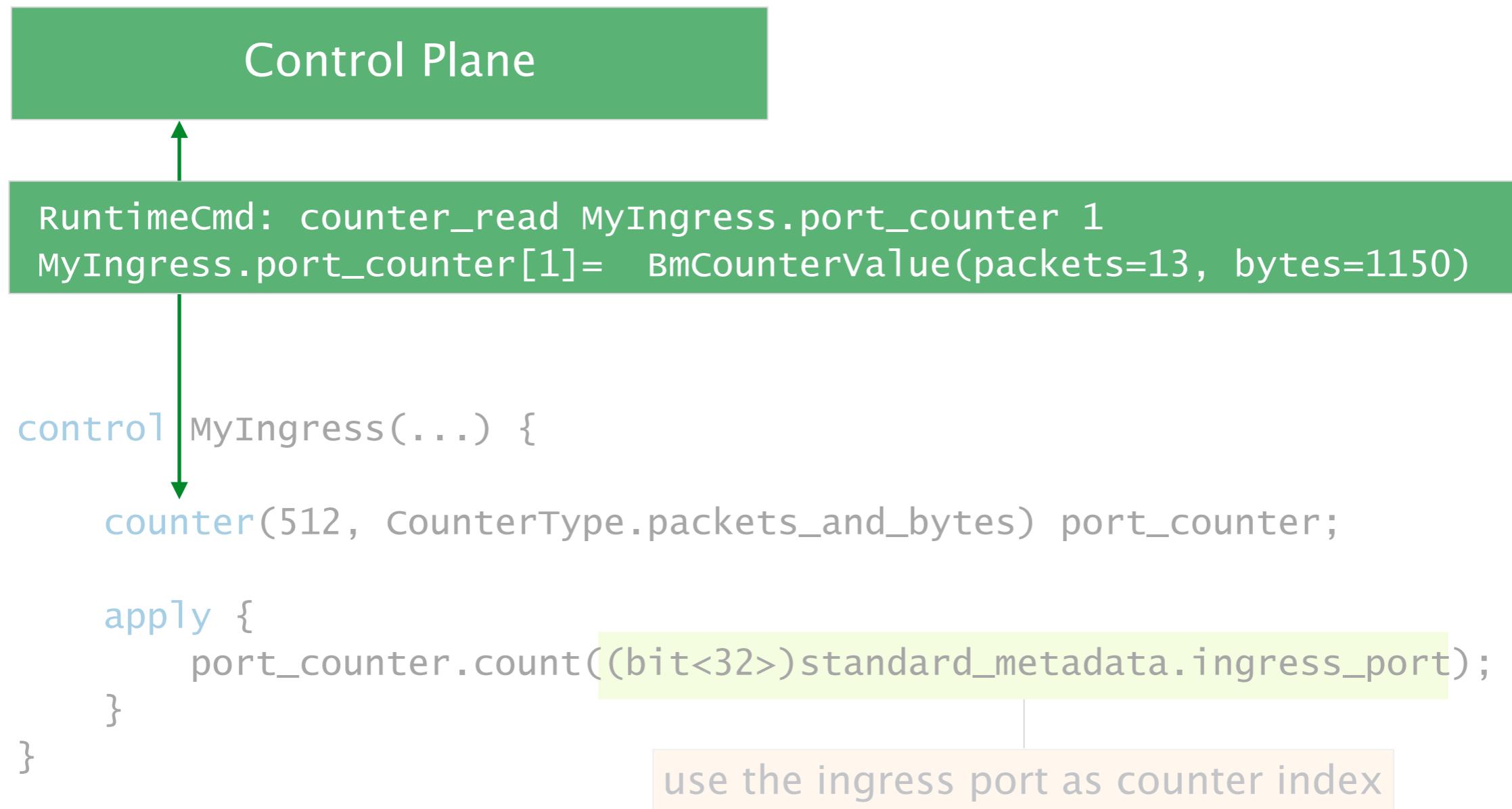
Example: Counting packets and bytes arriving at each port



```
control MyIngress(...) {  
    counter(512, CounterType.packets_and_bytes) port_counter;  
  
    apply {  
        port_counter.count((bit<32>)standard_metadata.ingress_port);  
    }  
}
```

use the ingress port as counter index

Example: Reading the counter values from the control plane



Direct counters are a special kind of counters that are attached to tables

Match Key	Action ID	Action Data	Counter
Default			

Each entry has a counter cell that counts when the entry matches

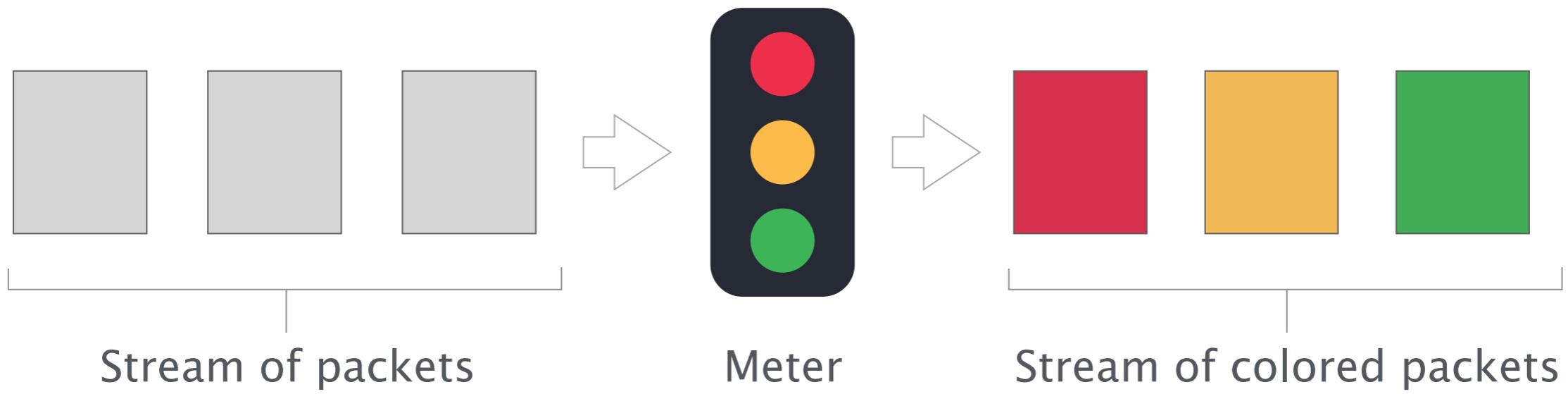
Example: Counting packets and bytes arriving at each port *using a direct counter*

```
control MyIngress(...) {  
  
    direct_counter(CounterType.packets_and_bytes) direct_port_counter;  
  
    table count_table {  
        key = {  
            standard_metadata.ingress_port: exact;  
        }  
        actions = {  
            NoAction;  
        }  
        default_action = NoAction;  
        counters = direct_port_counter; ────────── attach counter to table  
        size = 512;  
    }  
  
    apply {  
        count_table.apply();  
    }  
}
```

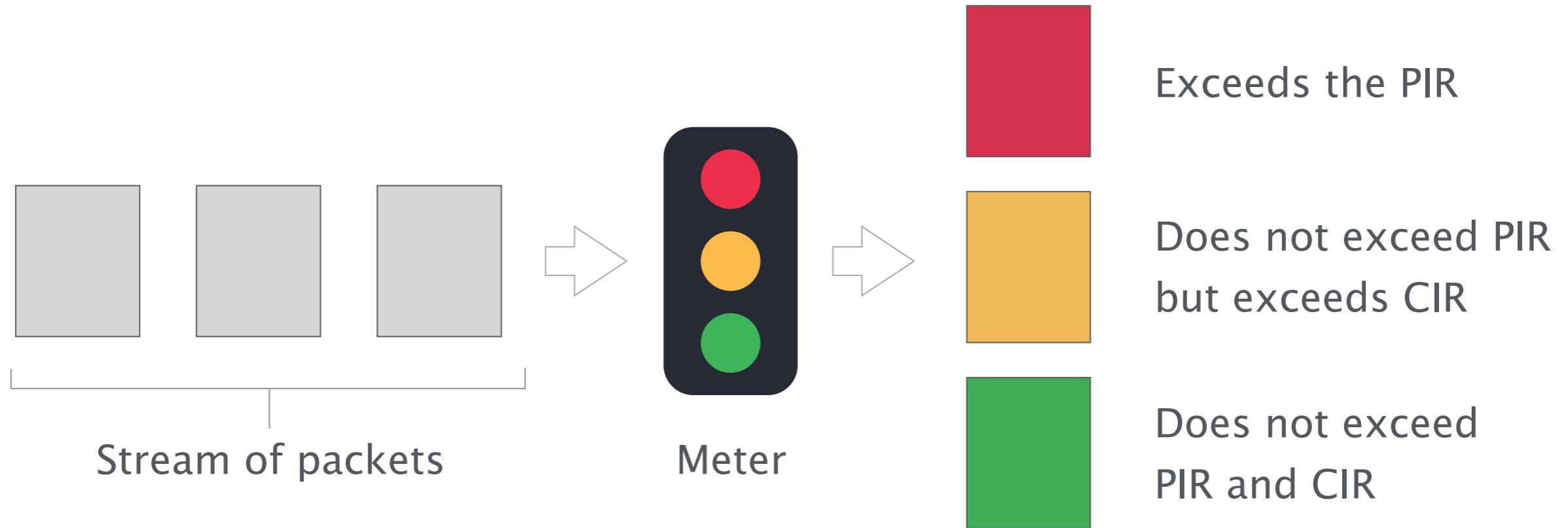
Stateful objects in P4

- Table managed by the control plane
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 - Meter rate-limiting
 -
- 
- externs in v1model

Meters



Meters

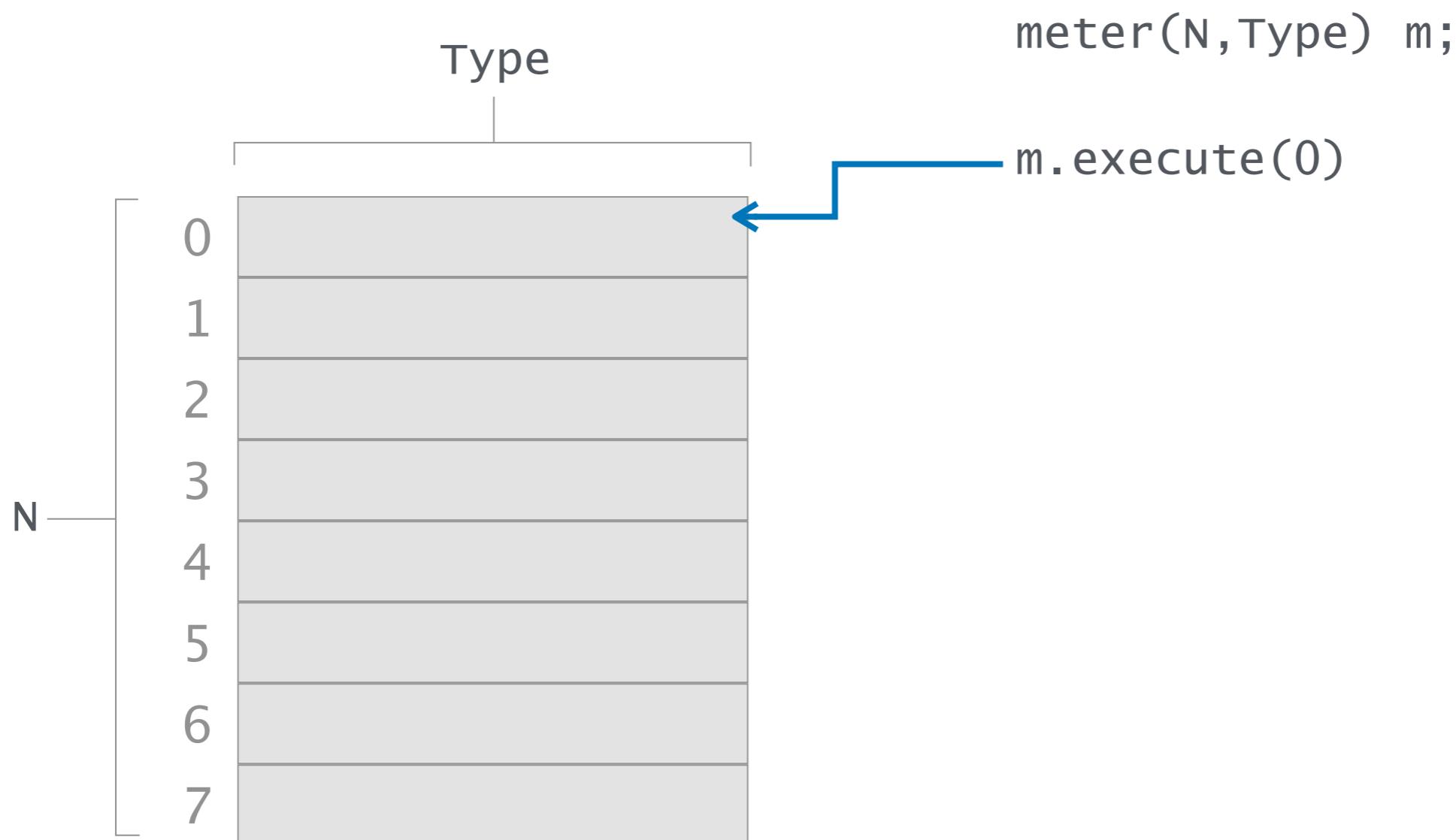


Parameters:

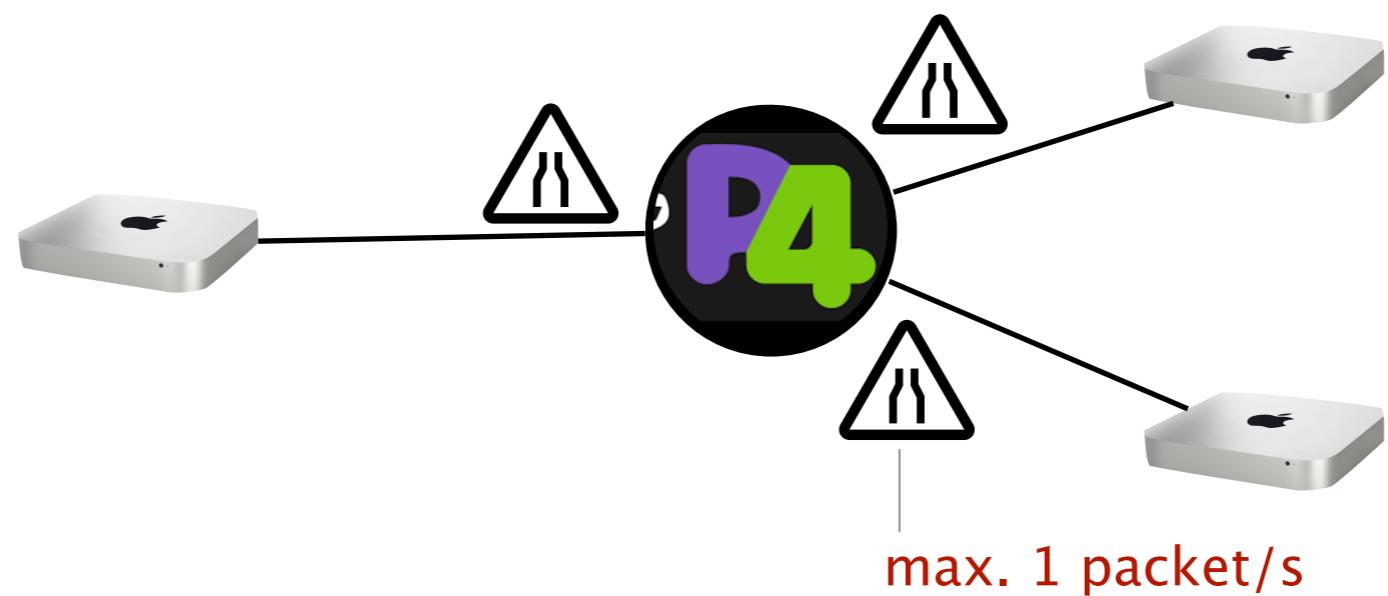
PIR	Peak Information Rate
CIR	Committed Information Rate

[bytes/s] or [packets/s]
[bytes/s] or [packets/s]

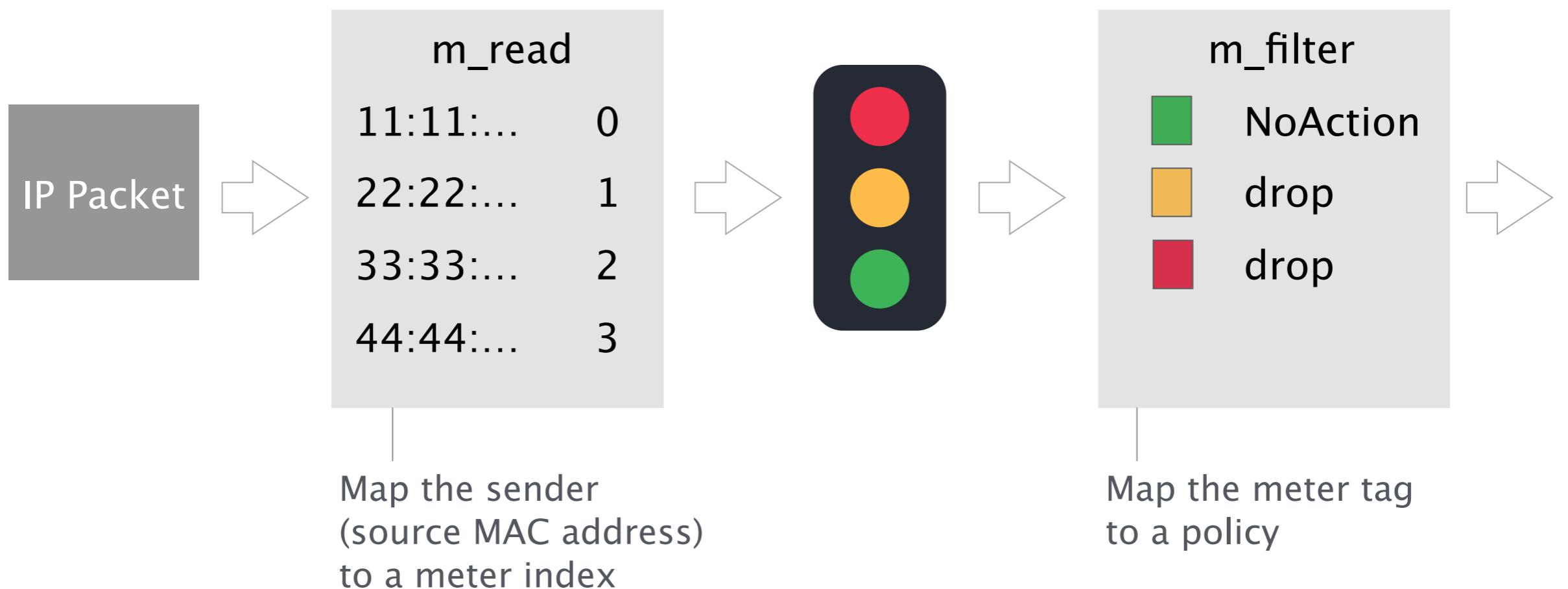
Like registers and counters, meters
are assigned in arrays



Example: Using a meter for rate-limiting



Example: Using a meter for rate-limiting



Example: Using a meter for rate-limiting

```
control MyIngress(...) {
    meter(32w16384, MeterType.packets) my_meter; packet meter

    action m_action(bit<32> meter_index) {
        my_meter.execute_meter<bit<32>>(meter_index, meta.meter_tag);
    } execute meter

    table m_read {
        key = { hdr.ethernet.srcAddr: exact; }
        actions = { m_action; NoAction; }

        ...
    }
    table m_filter {
        key = { meta.meter_tag: exact; }
        actions = { drop; NoAction; }

        ...
    }

    apply {
        m_read.apply();
        m_filter.apply();
    }
}
```

packet meter

execute meter

handle packets
depending on
meter tag

Direct meters are a special kind of meters that are attached to tables

Match Key	Action ID	Action Data	Meter
Default			

Each entry has a meter cell that is executed when the entry matches

Example: Using a meter for rate-limiting

```
control MyIngress(...) {
    direct_meter<bit<32>>(MeterType.packets) my_meter;
}

action m_action(bit<32> meter_index) {
    my_meter.read(meta.meter_tag);
}

table m_read {
    key = { hdr.ethernet.srcAddr: exact; }
    actions = { m_action; NoAction; }
    meters = my_meter;
    ...
}

table m_filter { ... }

apply {
    m_read.apply();
    m_filter.apply();
}
```

Summary

	Data plane interface		Control plane interface	
Object	read	modify/write	read	modify/write
Table	apply()	—	yes	yes
Register	read()	write()	yes	yes
Counter	—	count()	yes	reset
Meter	execute()		configuration only	

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[USENIX NSDI'19]

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part 1

Programming more advanced stateful data structures

Programming more advanced stateful data structures

We are provided with built-in stateful data structures such as arrays of registers, counters or meters

We need to deal with severe limitations such as a limited number of operations and memory

Programming more advanced stateful data structures

We are provided with built-in stateful data structures such as arrays of registers, counters or meters

We need to deal with severe limitations such as a limited number of operations and memory

Today: how can we implement a set with its usual methods i.e., add an element, membership query, delete an element, lookup, listing

There are two common strategies
to implement a set

	strategy #1	strategy #2
output	Deterministic	Probabilistic
number of required operations	Probabilistic	Deterministic

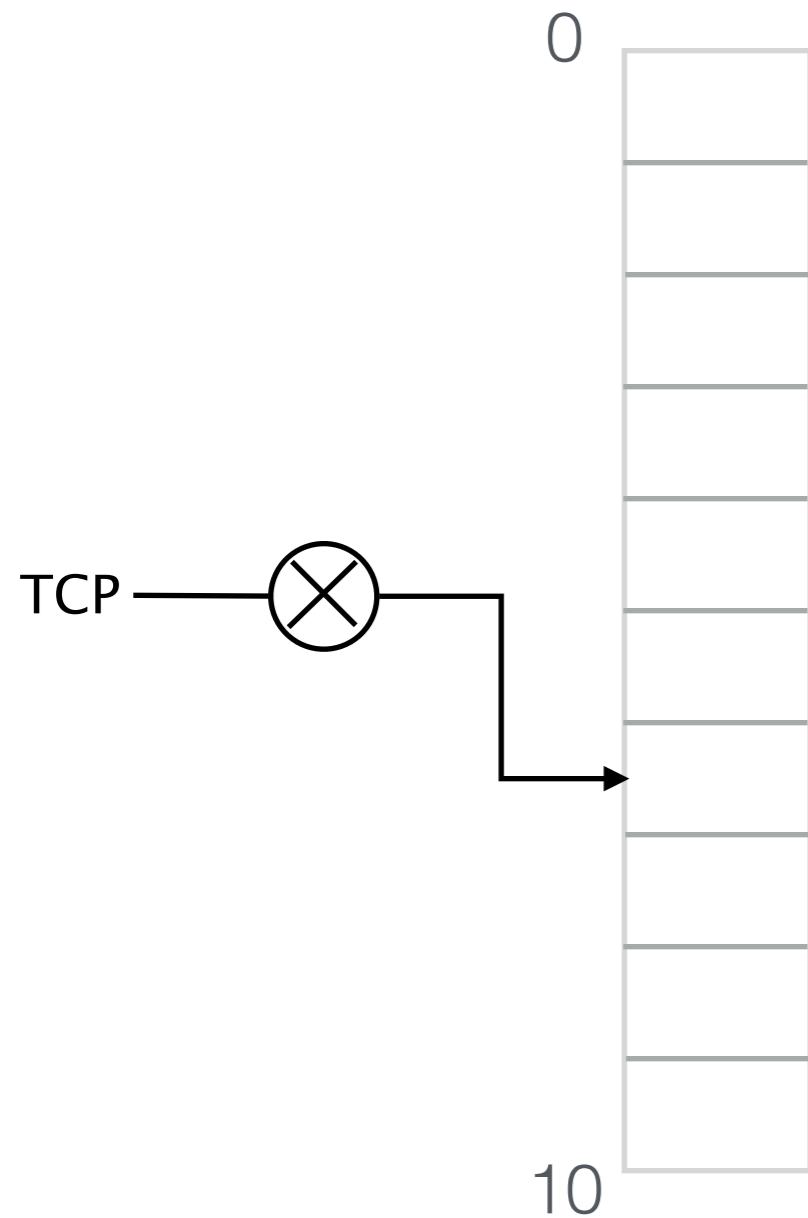
There are two common strategies
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Intuitive implementation of a **set**

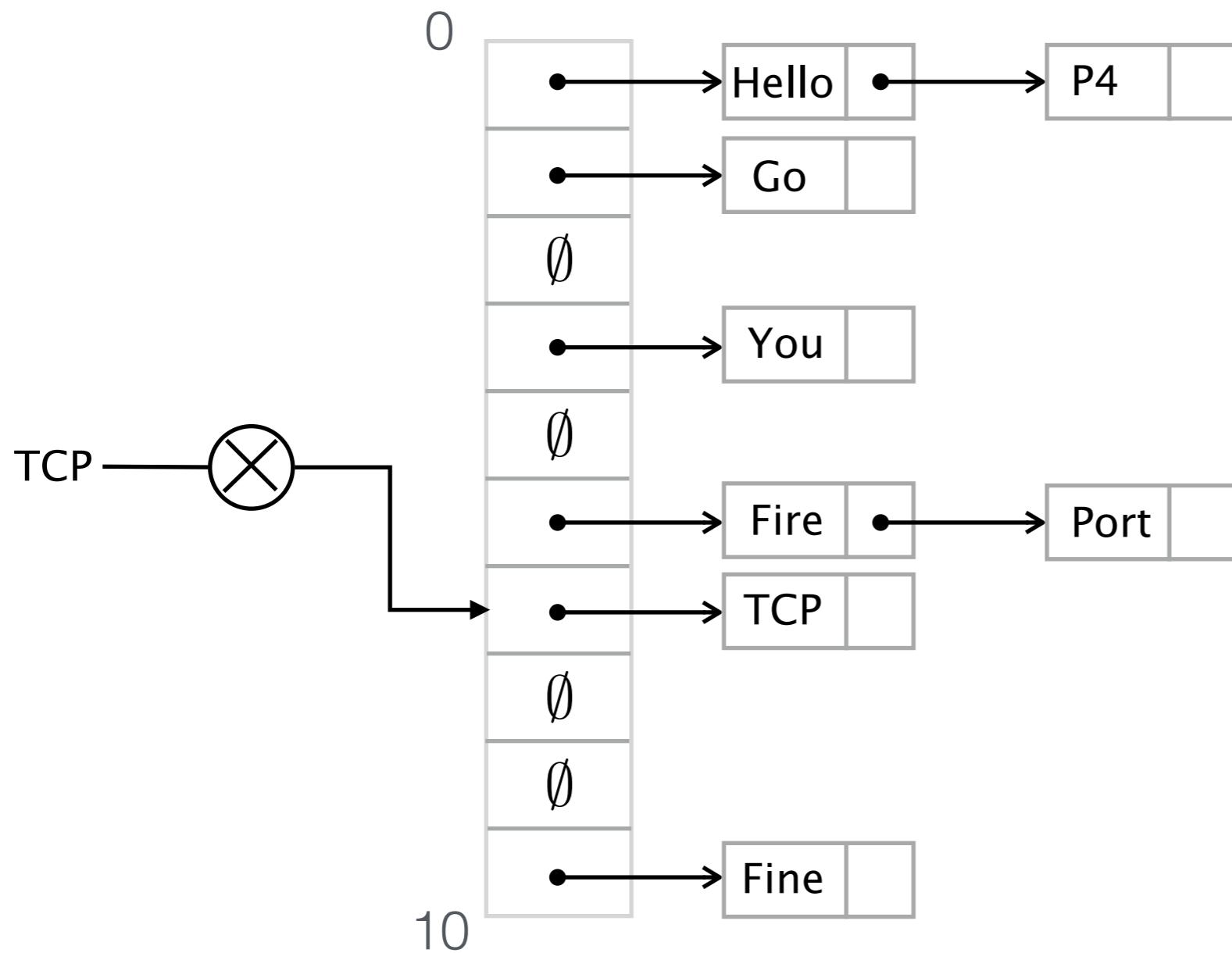
Intuitive implementation of a **set**

Separate-chaining



Intuitive implementation of a set

Separate-chaining



Intuitive implementation of a **set**

Separate-chaining

N elements and M cells

	list size
average	N/M
worse-case	N

Intuitive implementation of a **set**

Separate-chaining

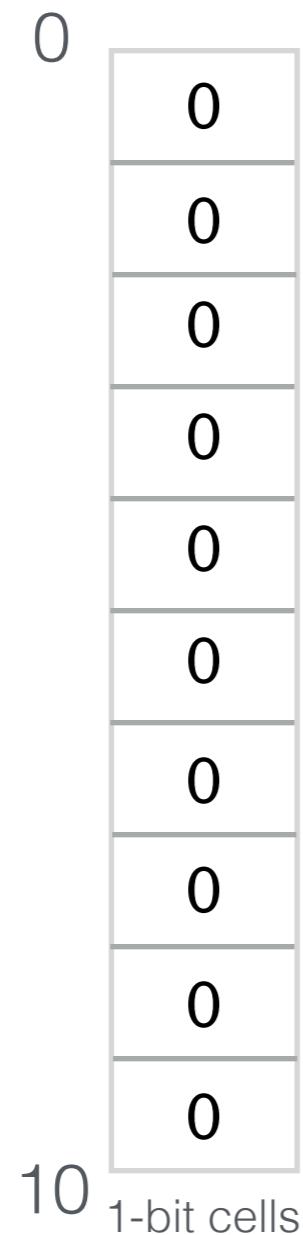
Pros: accurate and fast in the average case

Con: only works in hardware if there is a low number of elements (e.g. < 100)

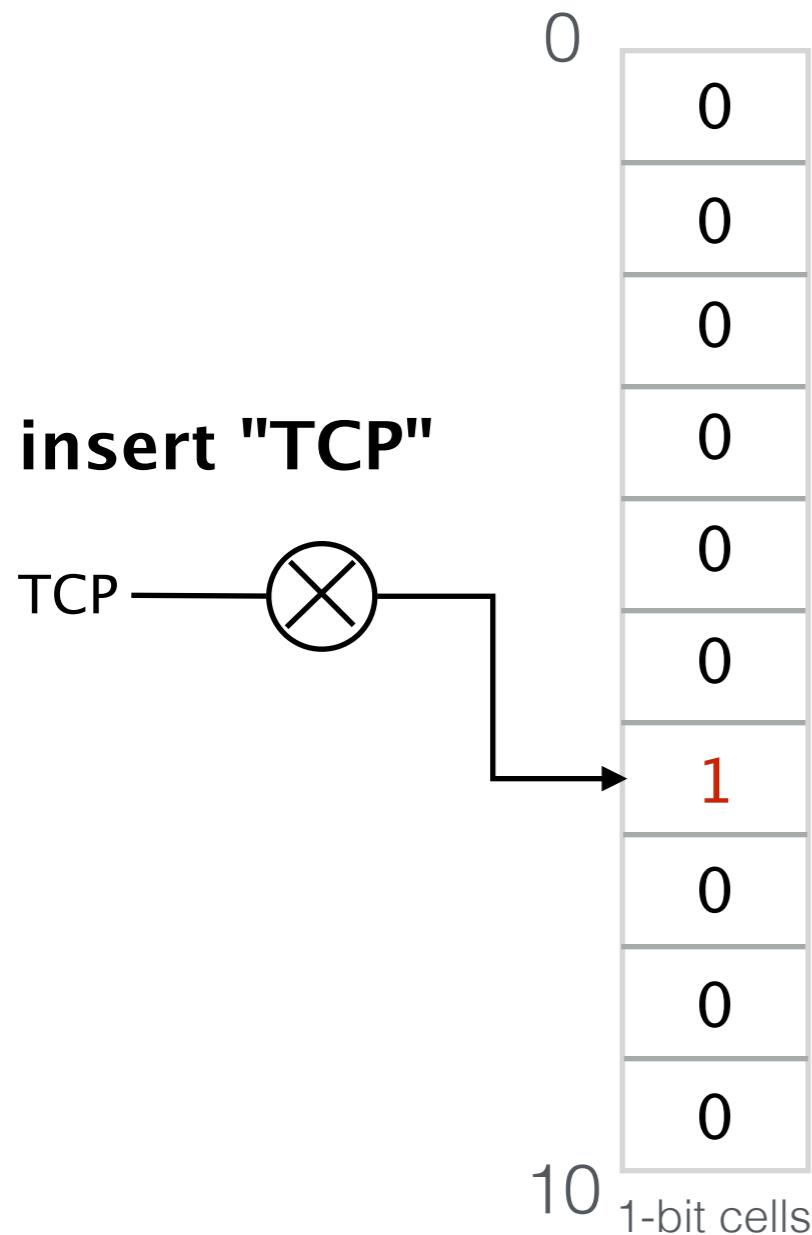
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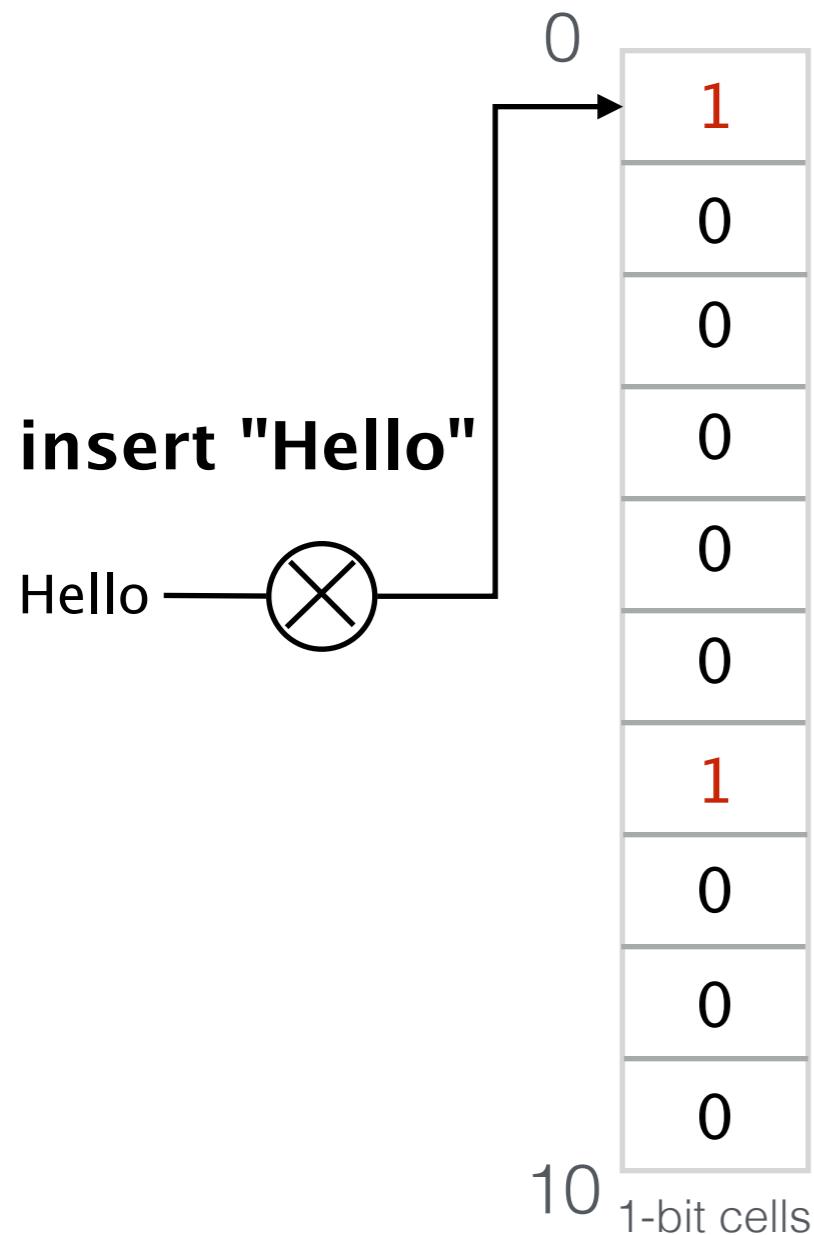
A simple approach for insertions and membership queries



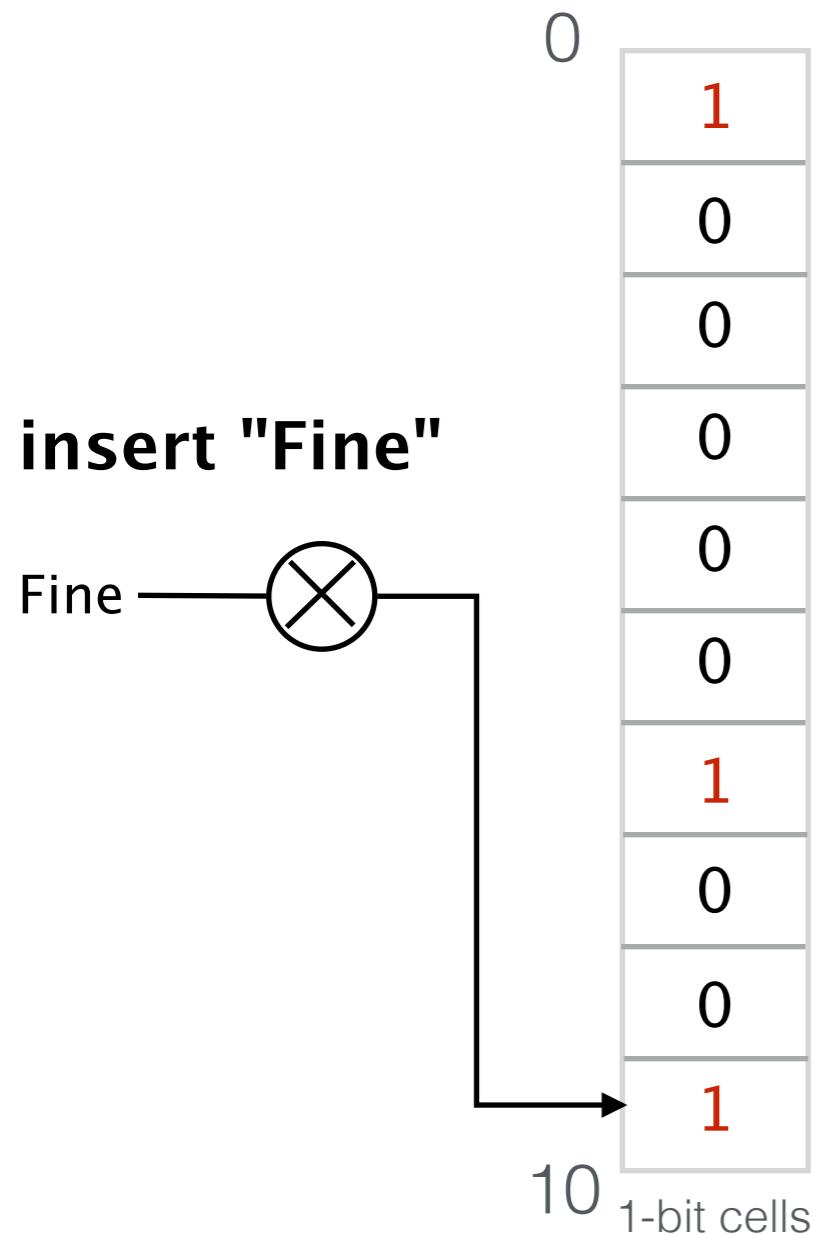
A simple approach for insertions and membership queries



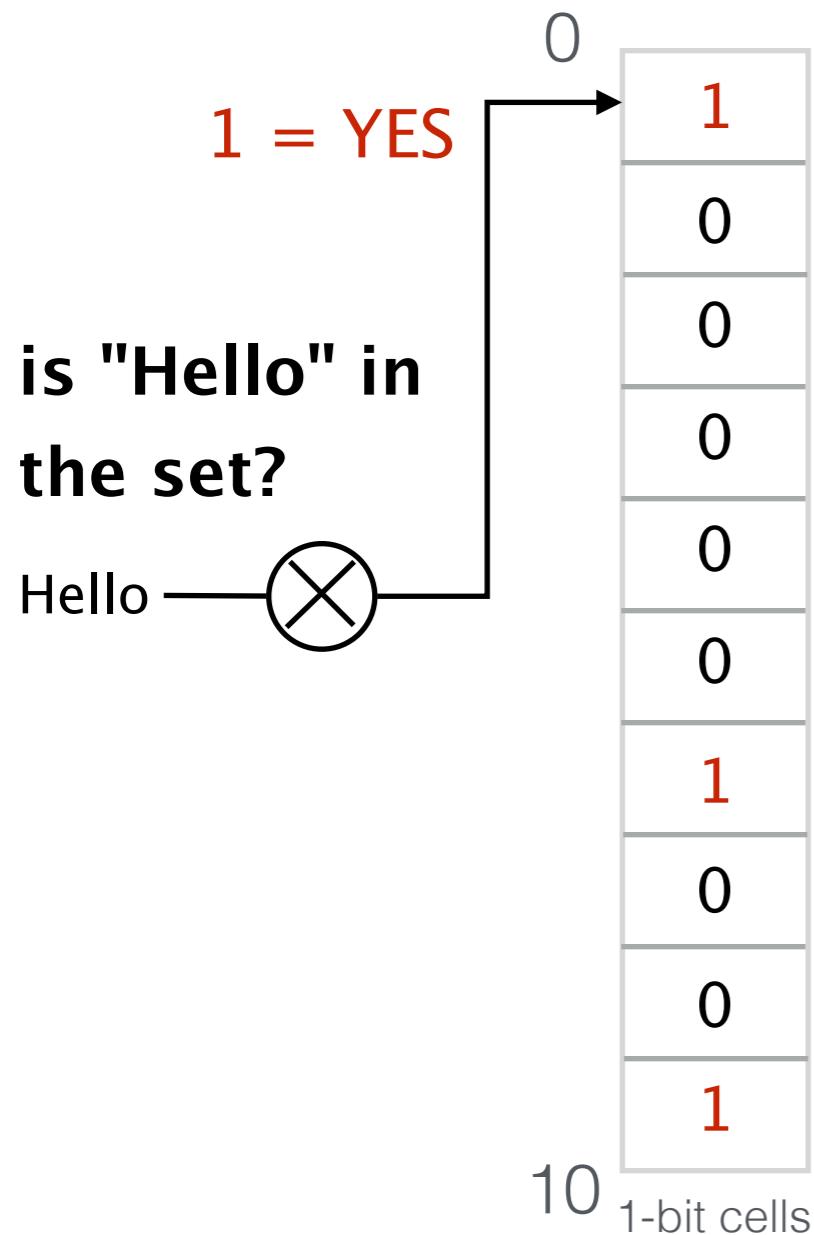
A simple approach for insertions and membership queries



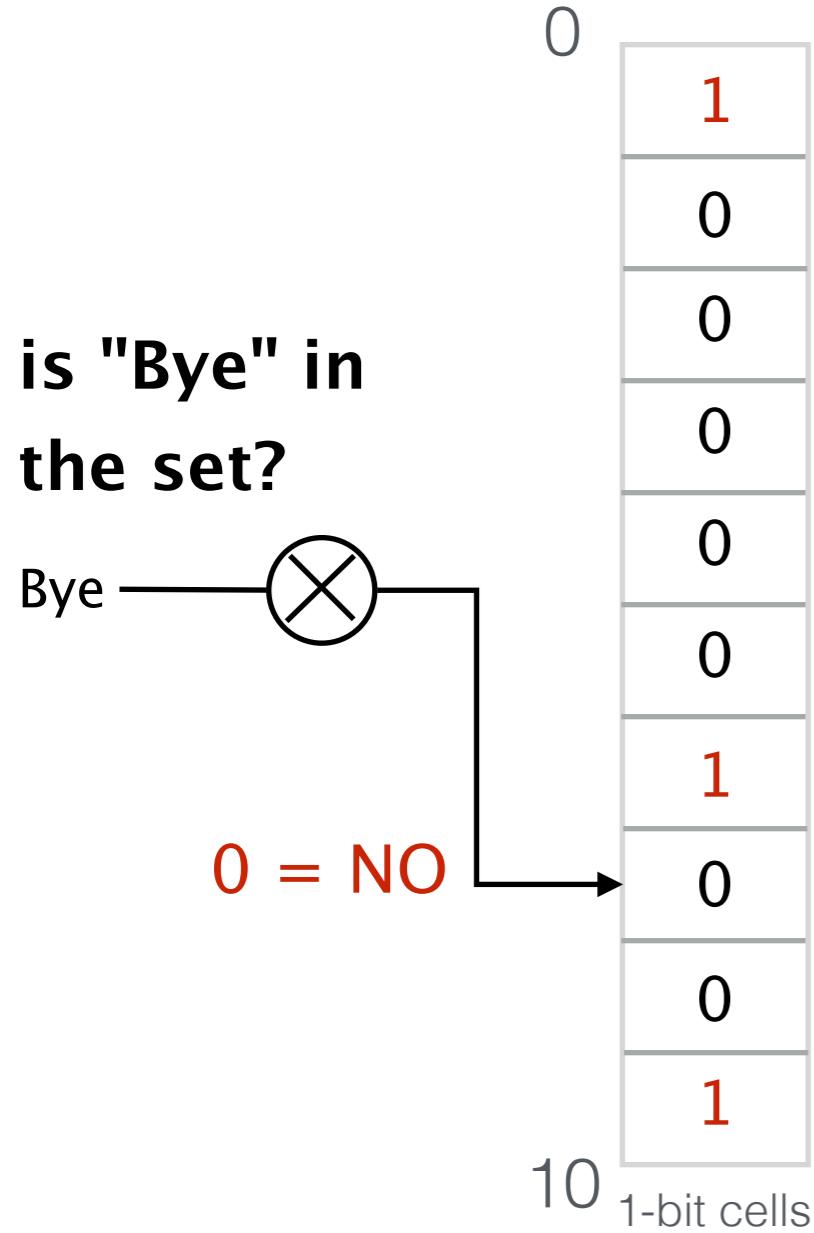
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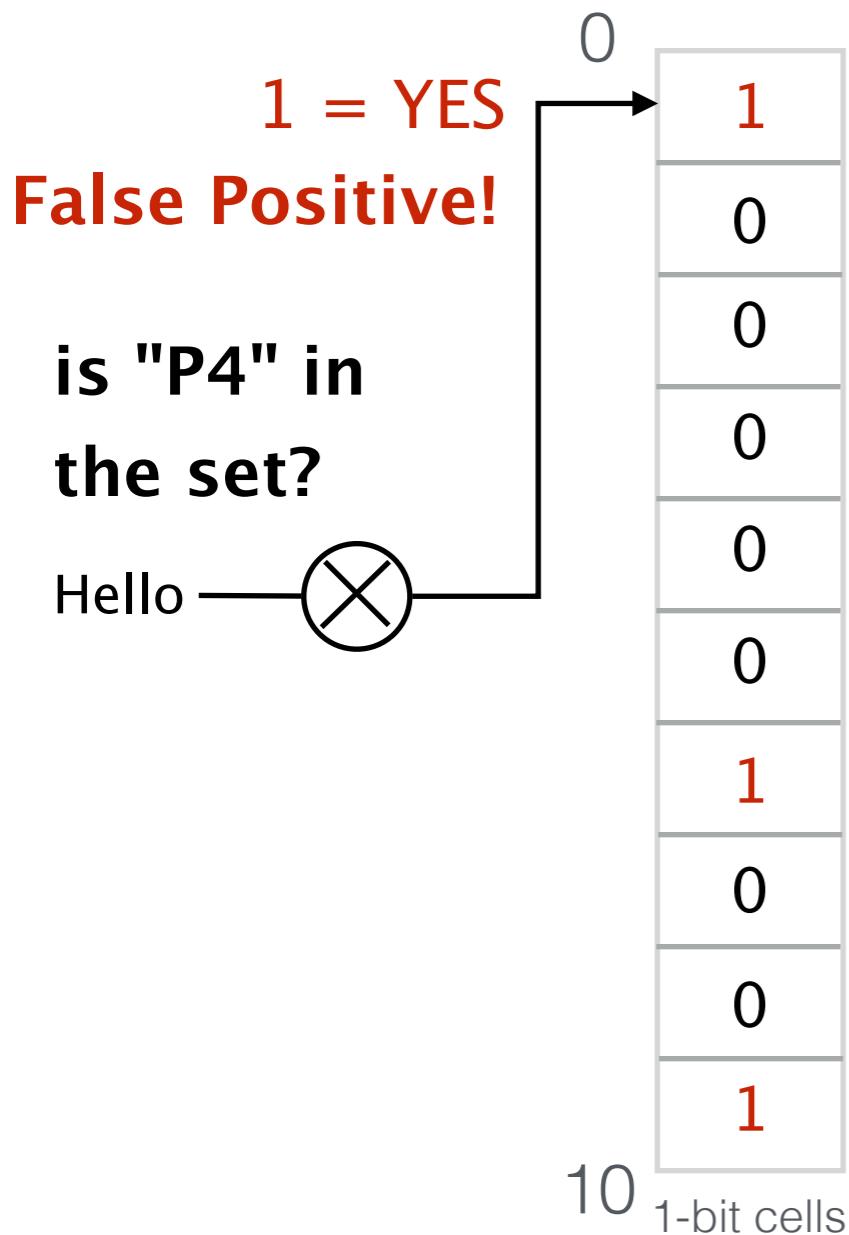
A simple approach for insertions and membership queries



A simple approach for insertions and membership queries



A simple approach for insertions and membership queries



A simple approach for insertions and membership queries

N elements and M cells

probability of an element to be
mapped into a particular cell

$$\frac{1}{M}$$

probability of an element not to
be mapped into a particular cell

$$1 - \frac{1}{M}$$

probability of a cell to be 0

$$(1 - \frac{1}{M})^N$$

false positive rate (FPR)

$$1 - (1 - \frac{1}{M})^N$$

false negative rate

$$0$$

A simple approach for insertions and membership queries

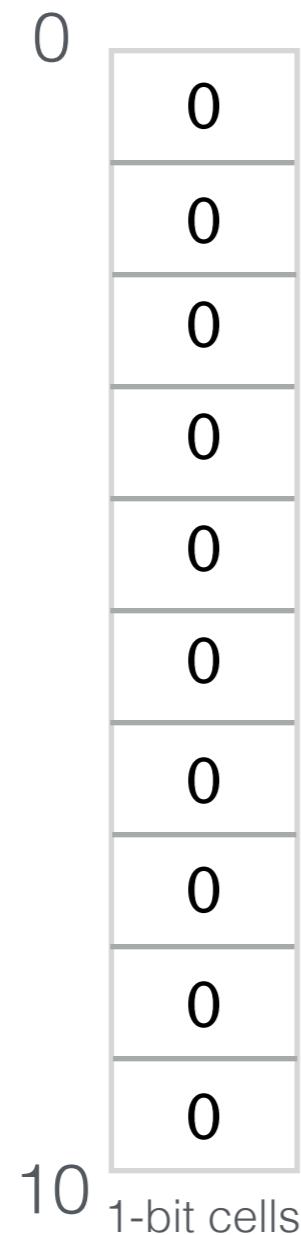
# of elements	# of cells	FPR
1000	10000	9.5%
1000	100000	1%

A simple approach for insertions and membership queries

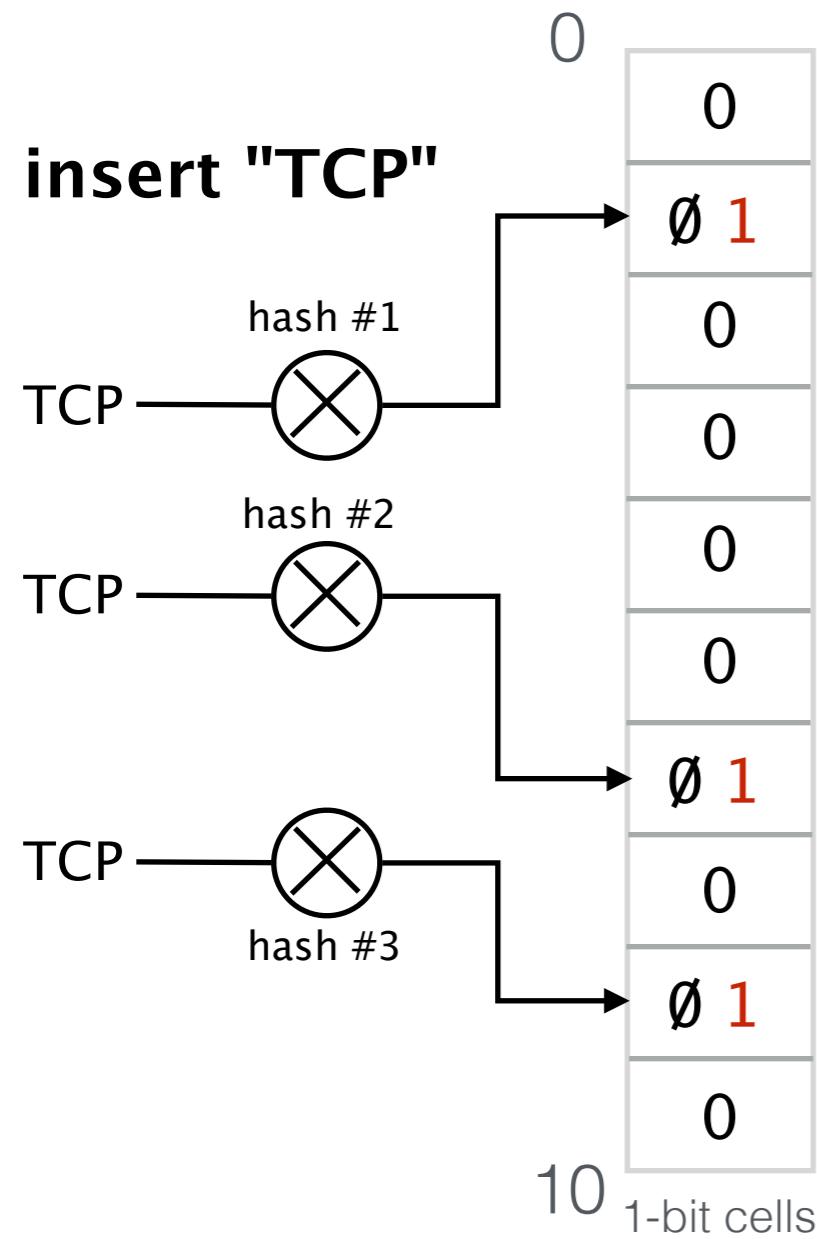
Pros: simple and only one operation per insertion or query

Con: roughly 100x more cells are required than the number of element we want to store for a 1% false positive rate

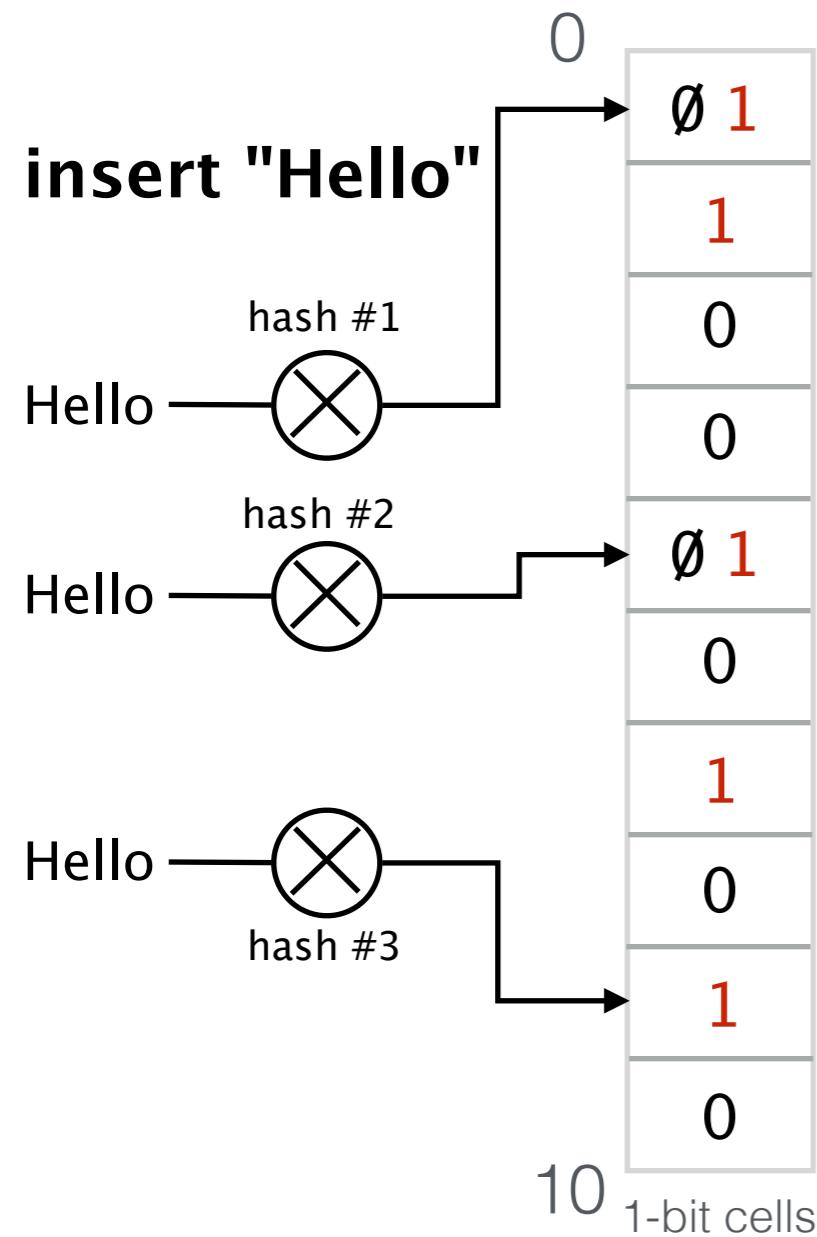
Bloom Filters: a more memory-efficient approach for insertions and membership queries



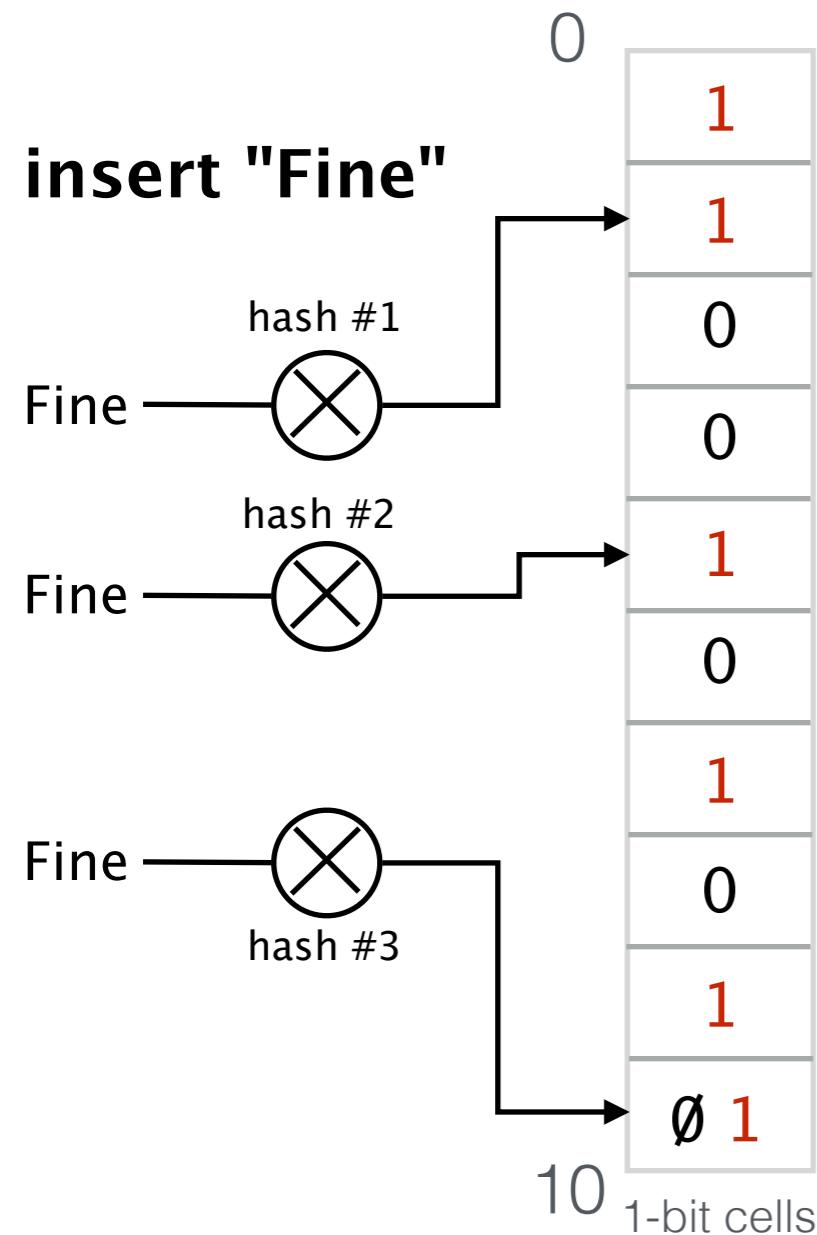
Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**



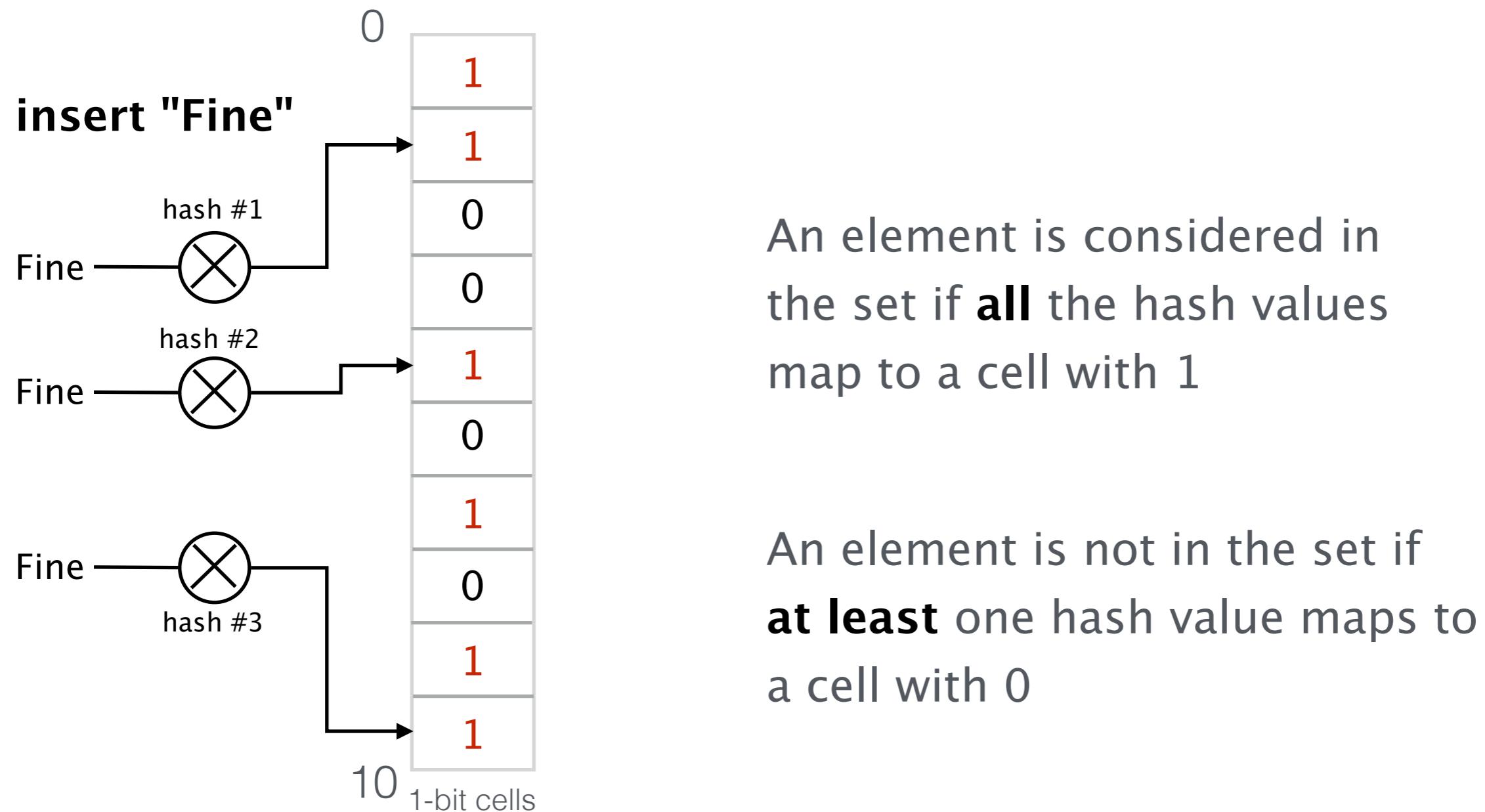
Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**



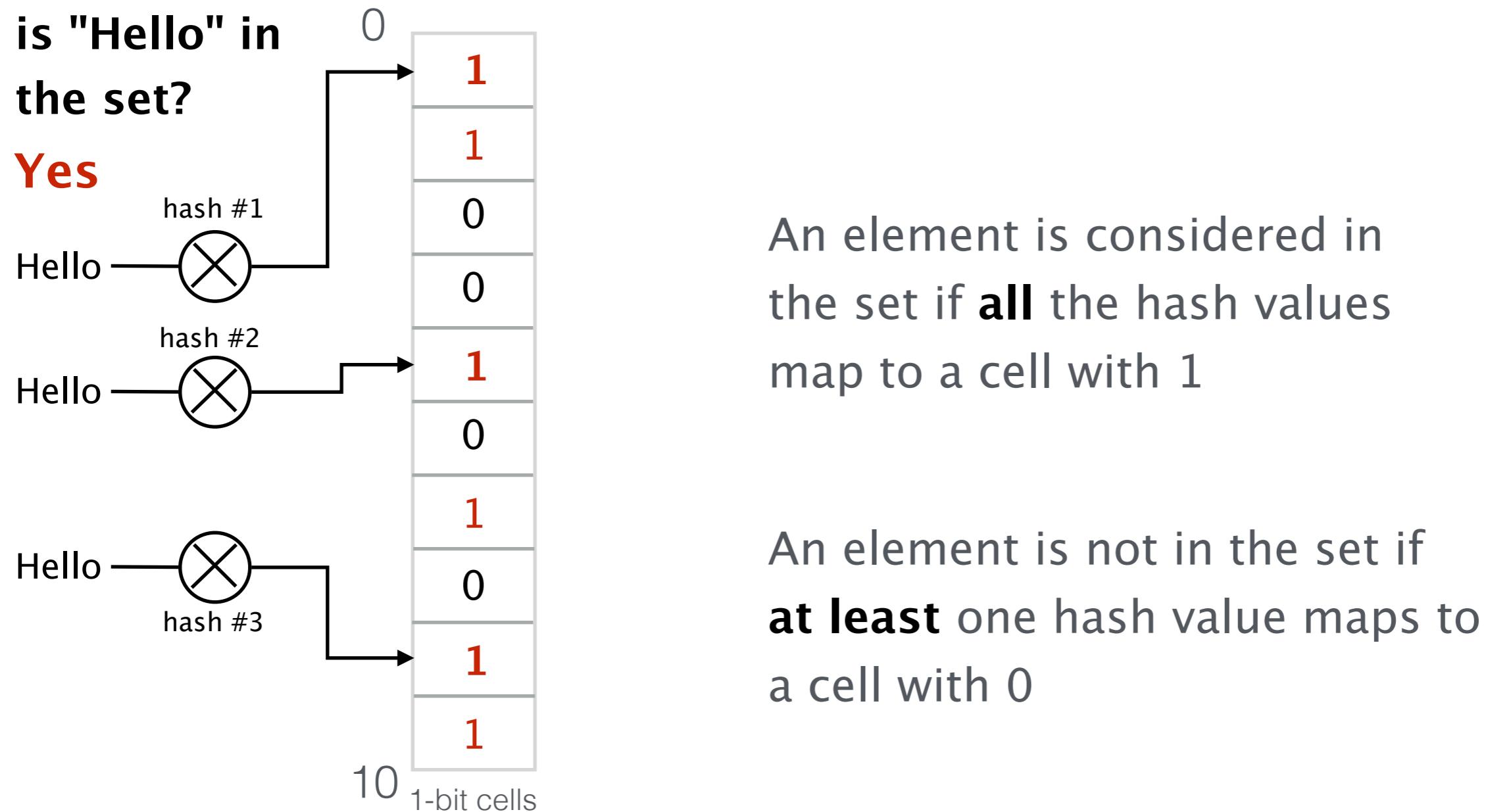
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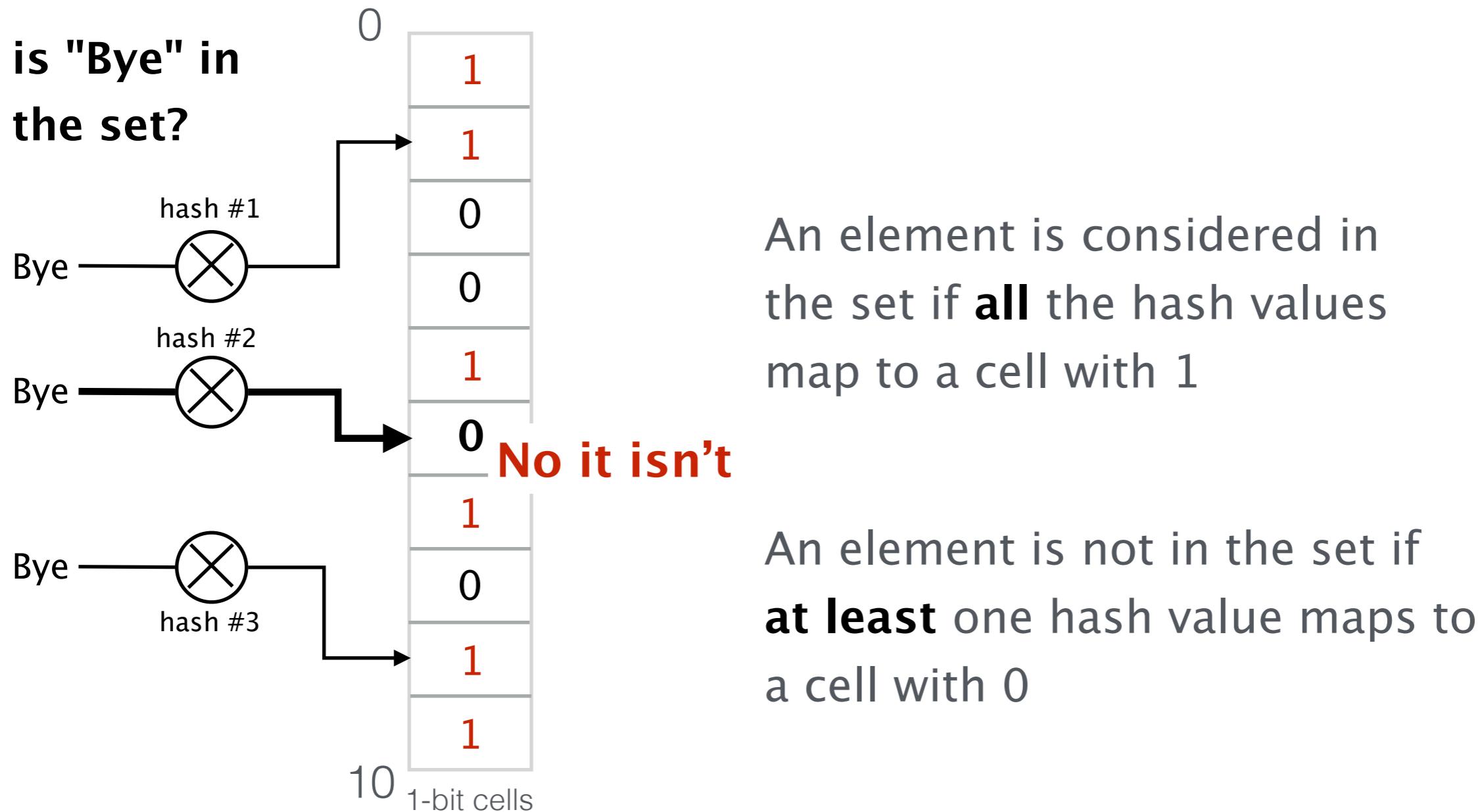
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Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**



Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**



Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**

False Positive!

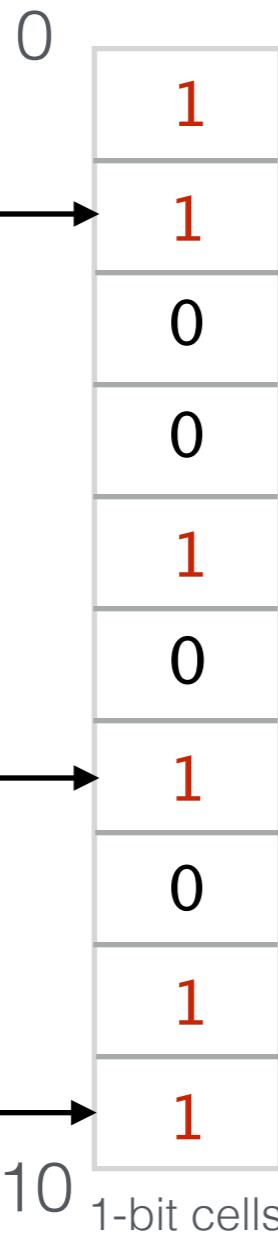
is "Fire" in
the set?

Yes

Bye

Bye

Bye



An element is considered in
the set if **all** the hash values
map to a cell with 1

An element is not in the set if
at least one hash value maps to
a cell with 0

Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**

N elements, M cells and K hash functions

probability that one hash function
returns the index of a particular cell $\frac{1}{M}$

probability that one hash function does
not return the index of a particular cell $1 - \frac{1}{M}$

probability of a cell to be 0 $(1 - \frac{1}{M})^{KN}$

false positive rate $(1 - (1 - \frac{1}{M})^{KN})^K$

false negative rate 0

Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**

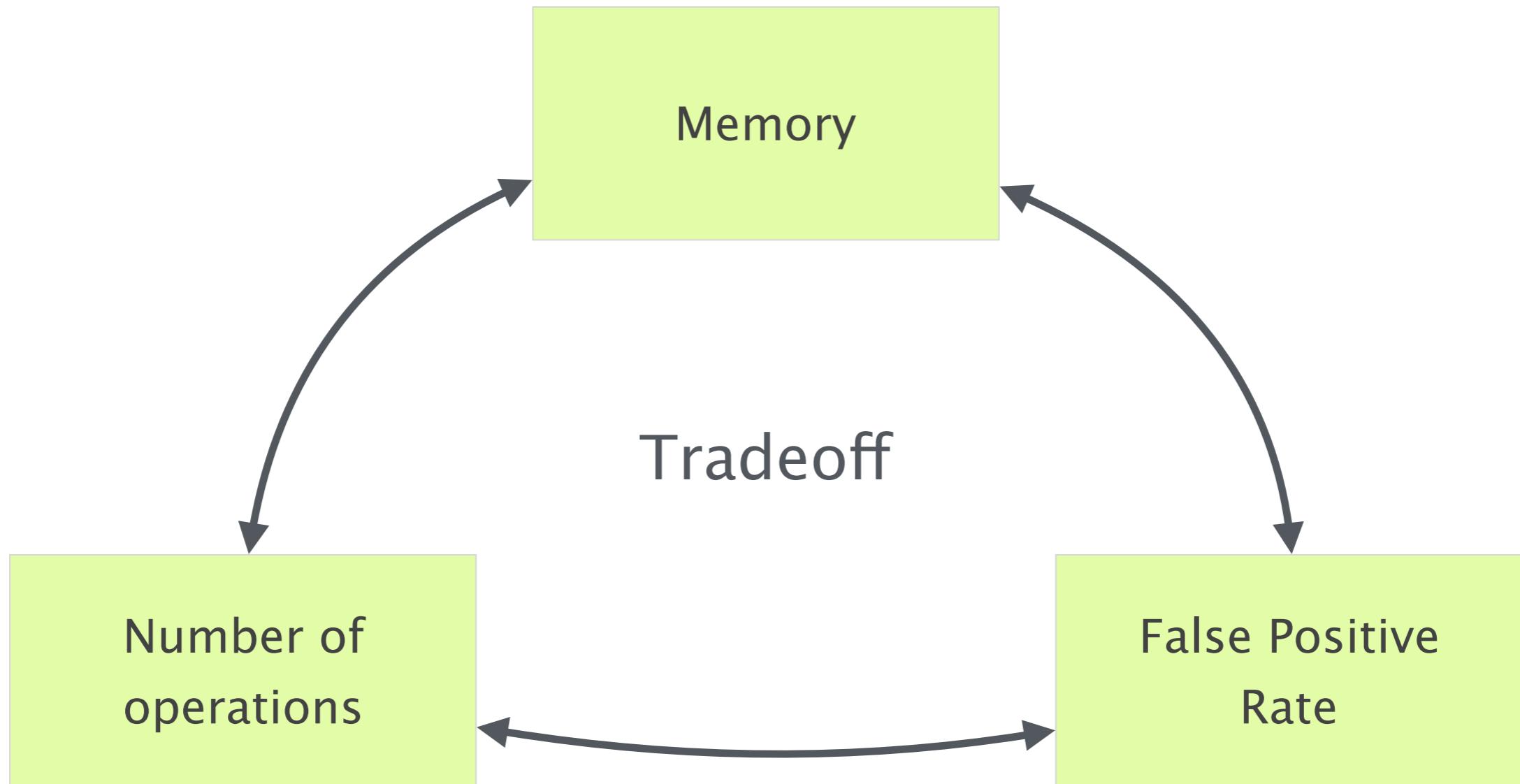
# of elements	# of cells	# hash functions	FPR
1000	10000	7	0.82%
1000	100000	7	≈ 0%

Bloom Filters: a more memory-efficient approach for **insertions** and **membership queries**

Pro: consumes roughly 10x less memory than
the simple approach

Con: Requires slightly more operations than the
simple approach (7 hashes instead of just 1)

Dimension your Bloom Filter



Dimension your Bloom Filter

N elements

M cells

K hash functions

FP false positive rate

Dimension your Bloom Filter

N elements

M cells

K hash functions

FP false positive rate

asymptotic approx.

$$FP = \left(1 - \left(1 - \frac{1}{M}\right)^{KN}\right)^K \approx \left(1 - e^{-KN/M}\right)^K$$

with calculus you can
dimension your bloom filter

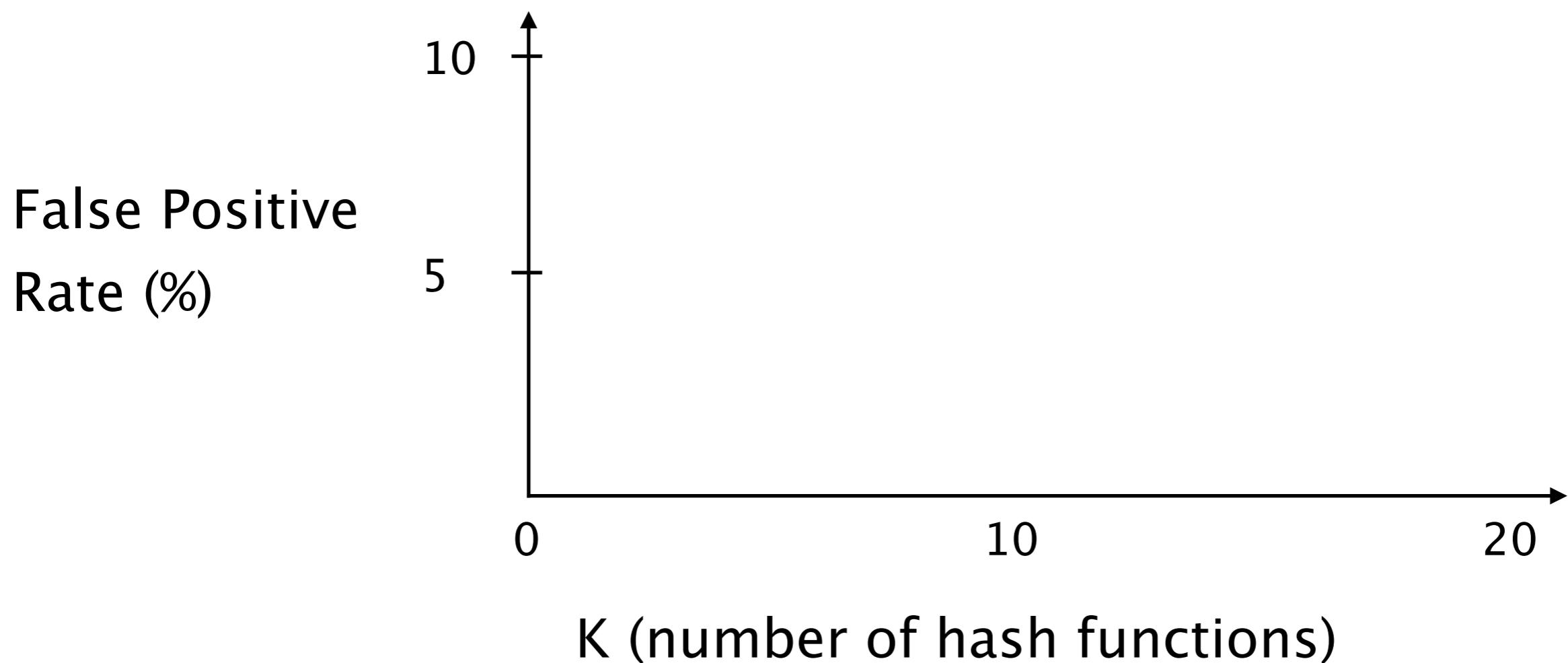
Dimension your Bloom Filter

$N = 1000$

$M = 10000$

K hash functions

FP false positive rate



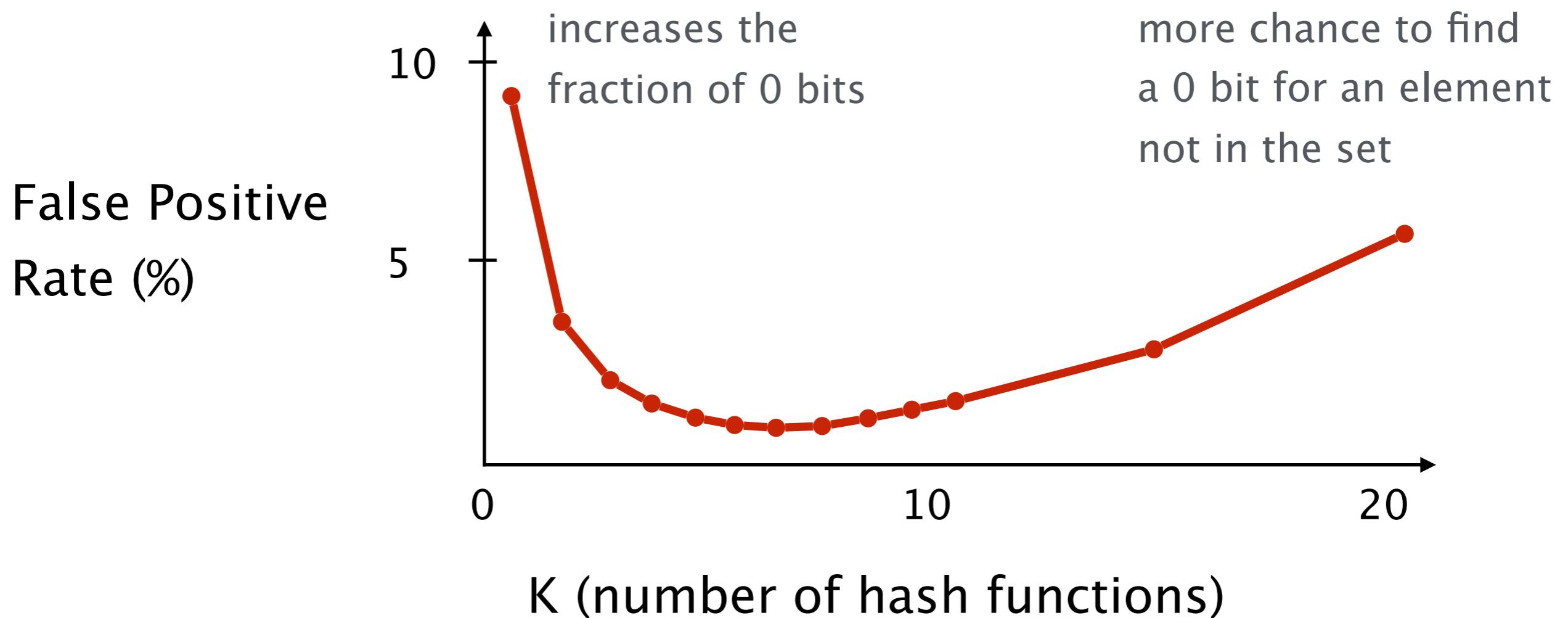
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$N = 1000$

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Dimension your Bloom Filter

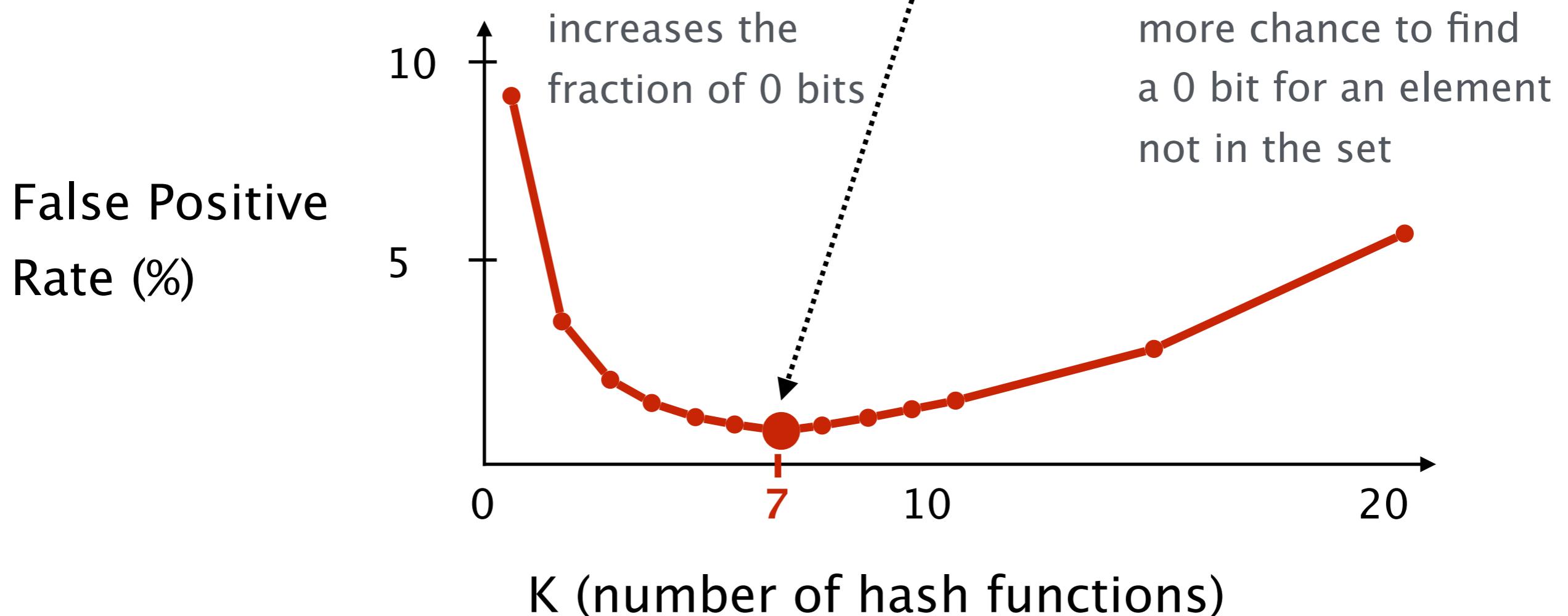
$N = 1000$

$M = 10000$

K hash functions

FP false positive rate

there is always a global minimum when $K = \ln 2 * (M/N)$ found by taking the derivative of $\approx (1 - e^{-KN/M})^K$



Implementation of a Bloom Filter in P4₁₆

You will have to use hash functions

v1model

```
enum HashAlgorithm {  
    crc32,  
    crc32_custom,  
    crc16,  
    s,  
    random,  
    identity,  
    csum16,  
    xor16  
}
```

```
extern void hash<O, T, D, M>(out O result,  
    in HashAlgorithm algo, in T base, in D data, in M max);
```

Implementation of a Bloom Filter in P4₁₆

You will have to use hash functions, as well as registers

v1model

```
extern register<T> {  
    register(bit<32> size);  
  
    void read(out T result, in bit<32> index);  
    void write(in bit<32> index, in T value);  
}
```

Implementation of a Bloom Filter in P4₁₆ with 2 hash functions

```
control MyIngress(...) {  
  
    register register<bit<1>>(NB_CELLS) bloom_filter;
```

Implementation of a Bloom Filter in P4₁₆

with 2 hash functions

```
control MyIngress(...) {  
  
    register register<bit<1>>(NB_CELLS) bloom_filter;  
  
    apply {  
        hash(meta.index1, HashAlgorithm.my_hash1, 0,  
              {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
        hash(meta.index2, HashAlgorithm.my_hash2, 0,  
              {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
    }  
}
```

Implementation of a Bloom Filter in P4₁₆ with 2 hash functions

```
control MyIngress(...) {  
  
    register register<bit<1>>(NB_CELLS) bloom_filter;  
  
    apply {  
        hash(meta.index1, HashAlgorithm.my_hash1, 0,  
              {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
        hash(meta.index2, HashAlgorithm.my_hash2, 0,  
              {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
  
        if (meta.to_insert == 1) {  
            bloom_filter.write(meta.index1, 1);  
            bloom_filter.write(meta.index2, 1);  
        }  
  
        if (meta.to_query == 1) {  
            bloom_filter.read(meta.query1, meta.index1);  
            bloom_filter.read(meta.query2, meta.index2);  
  
            if (meta.query1 == 0 || meta.query2 == 0) {  
                meta.is_stored = 0;  
            }  
            else {  
                meta.is_stored = 1;  
            }  
        }  
    }  
}
```

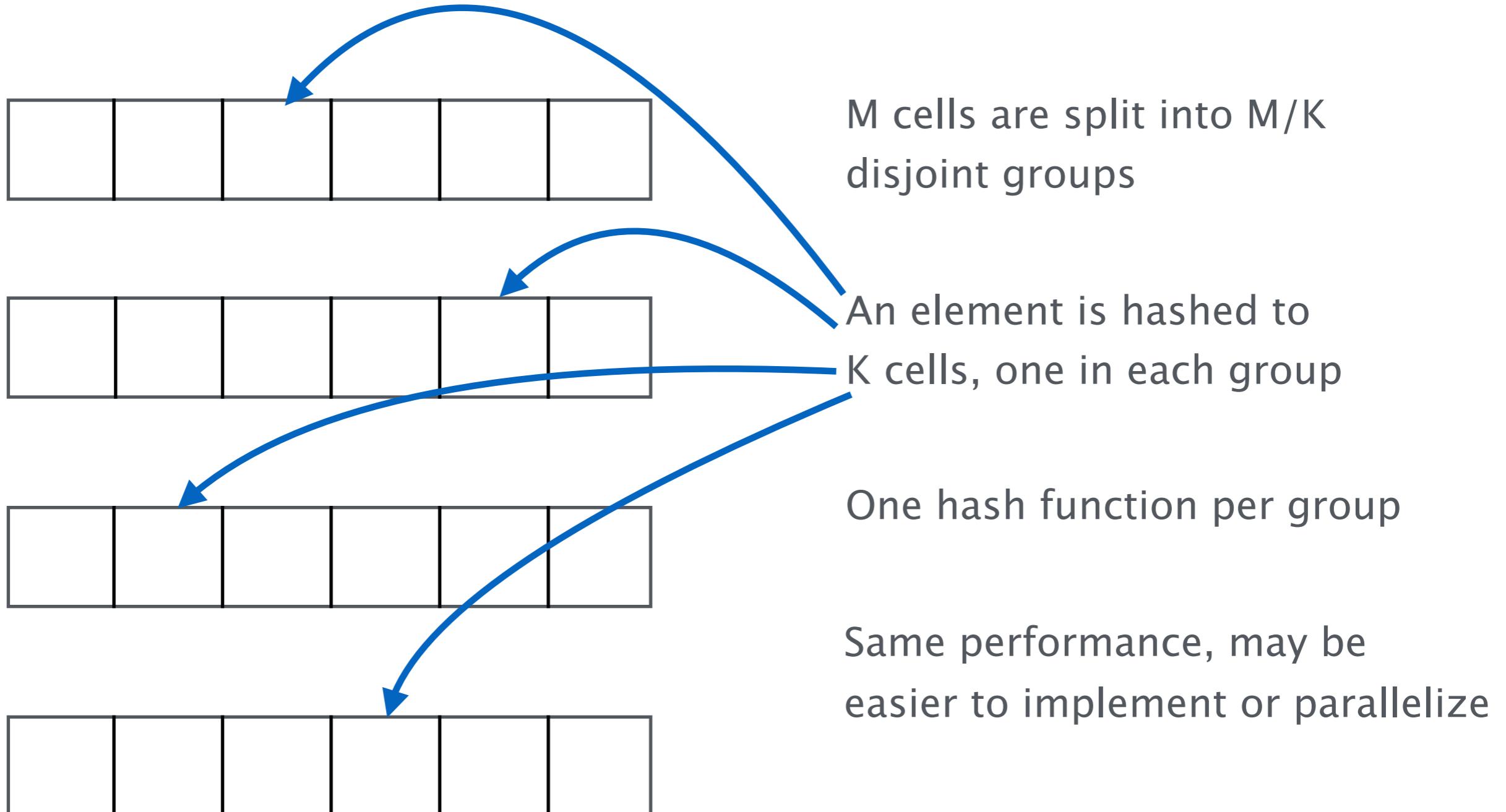
Implementation of a Bloom Filter in P4₁₆

with 2 hash functions

```
control MyIngress(...) {  
  
    register register<bit<1>>(NB_CELLS) bloom_filter;  
    apply {  
        hash(meta.index1, HashAlgorithm.my_hash1, 0,  
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
        hash(meta.index2, HashAlgorithm.my_hash2, 0,  
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);  
  
        if (meta.to_insert == 1) {  
            bloom_filter.write(meta.index1, 1);  
            bloom_filter.write(meta.index2, 1);  
        }  
  
        if (meta.to_query == 1) {  
            bloom_filter.read(meta.query1, meta.index1);  
            bloom_filter.read(meta.query2, meta.index2);  
  
            if (meta.query1 == 0 || meta.query2 == 0) {  
                meta.is_stored = 0;  
            }  
            else {  
                meta.is_stored = 1;  
            }  
        }  
    }  
}
```

Everything in bold
red must be adapted
for your program

Depending on the hardware limitations,
splitting the bloom filter might be required



Because deletions are not possible, the controller may need to regularly **reset** the bloom filters

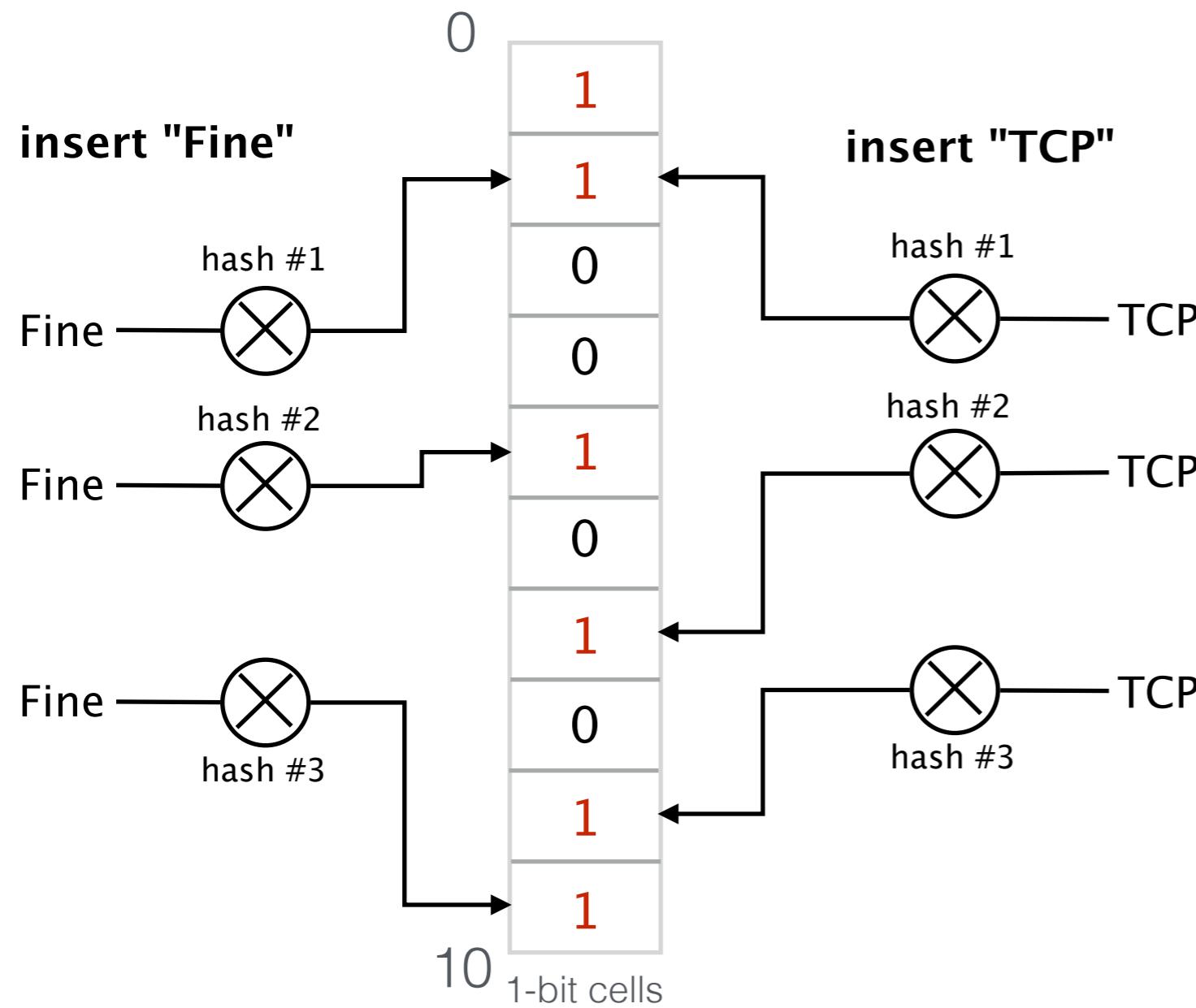
Resetting a bloom filter takes some time during which it is not usable

Common trick: use two bloom filters and use one when the controller resets the other one

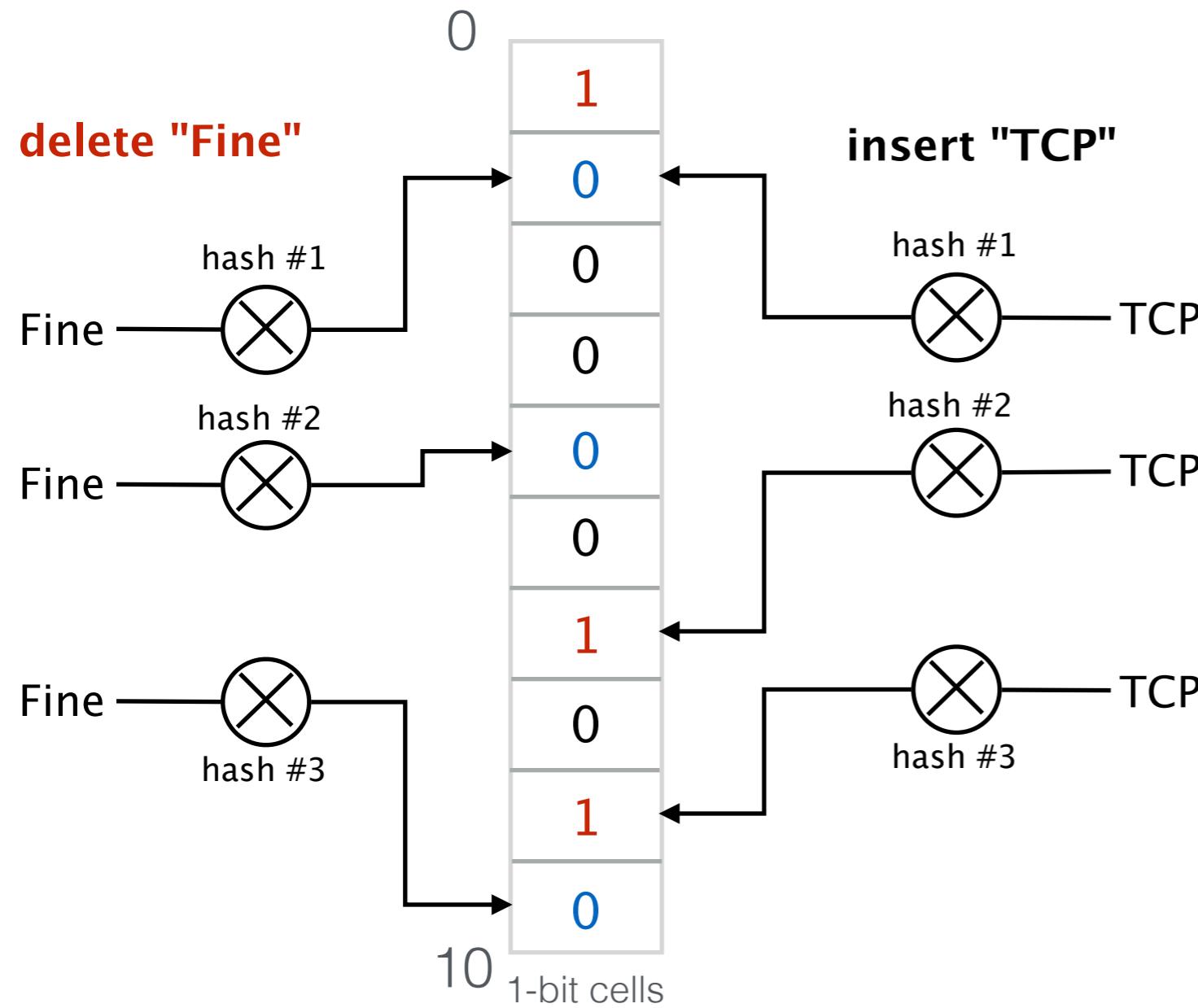
So far we have seen how to do insertions and membership queries

	strategy #1	strategy #2
output	Deterministic	Probabilistic
number of required operations	Probabilistic	Deterministic
		Bloom Filters

However Bloom Filters do not handle **deletions**



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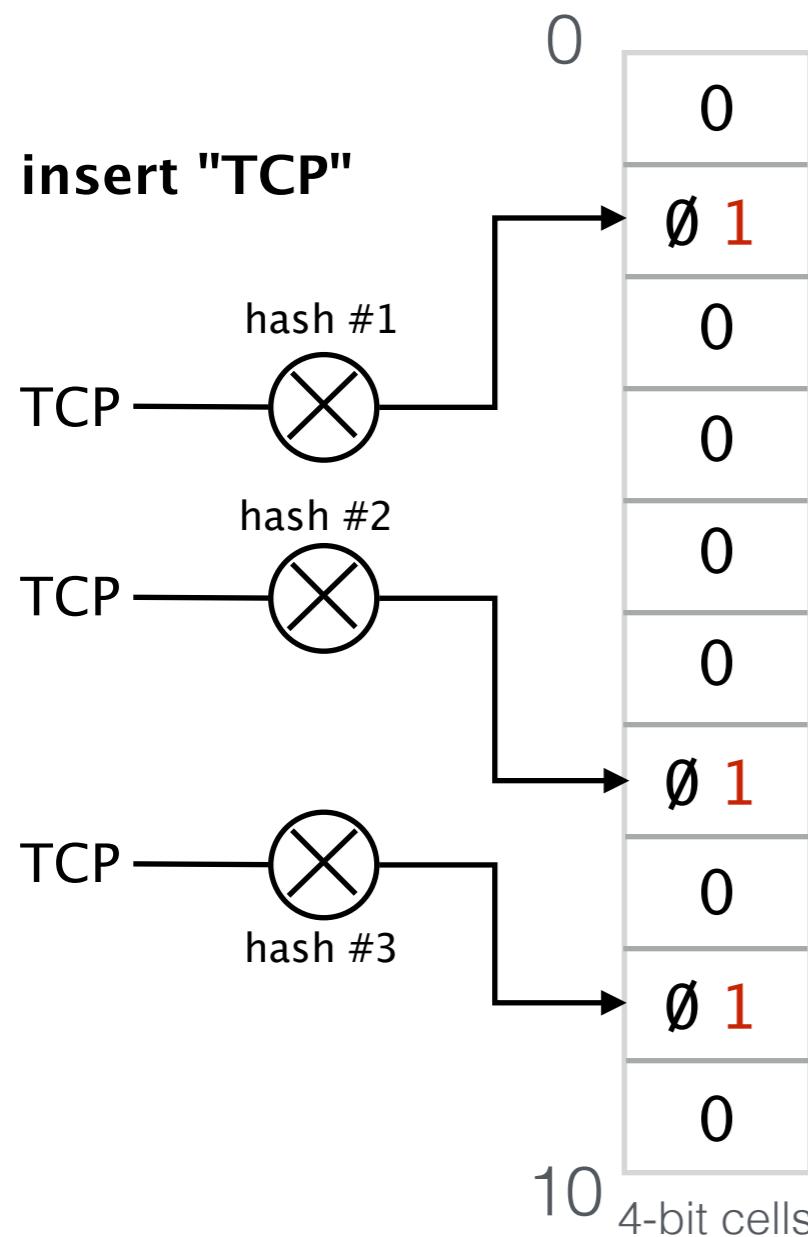
If deleting an element means resetting 1s to 0s, then deleting "Fine" also deletes "TCP"

But we can easily extend them to handle deletions

This extended version is called a **Counting Bloom Filter**

But we can easily extend them to handle deletions

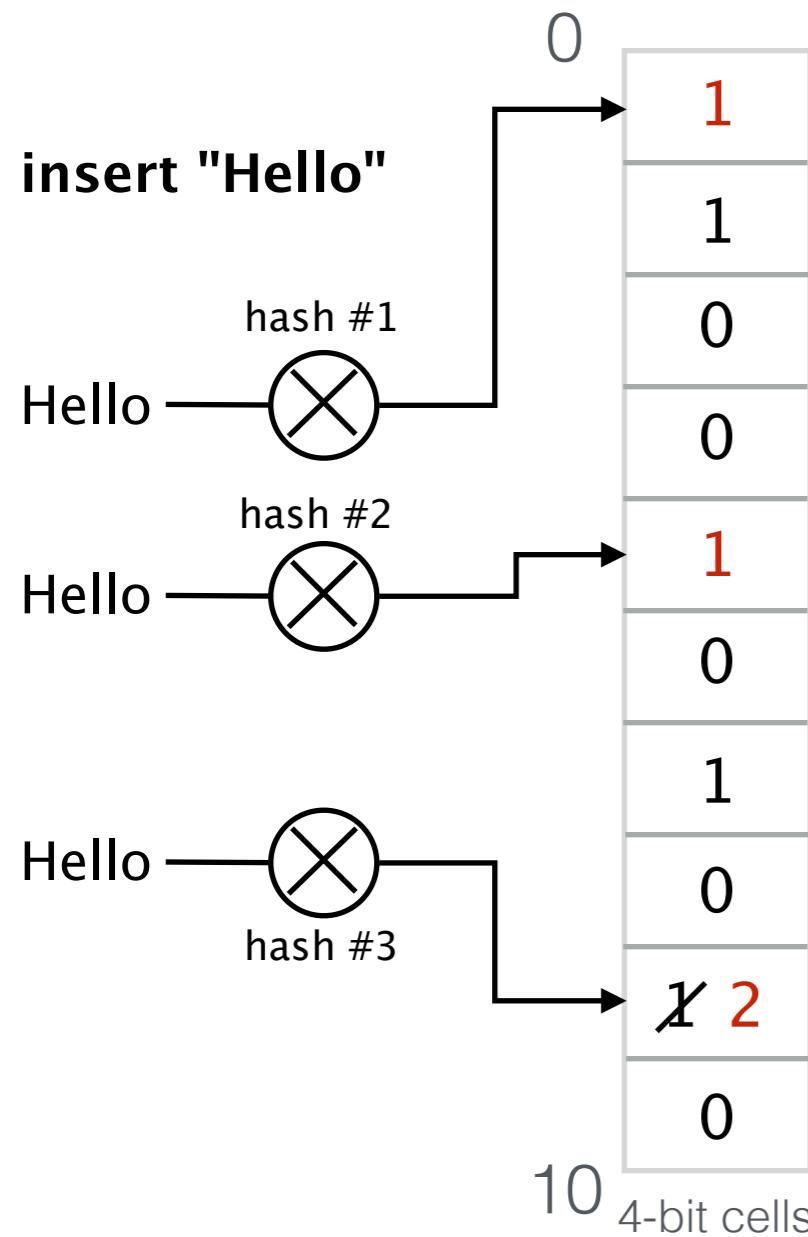
This extended version is called a **Counting Bloom Filter**



To add an element, increment
the corresponding counters

But we can easily extend them to handle deletions

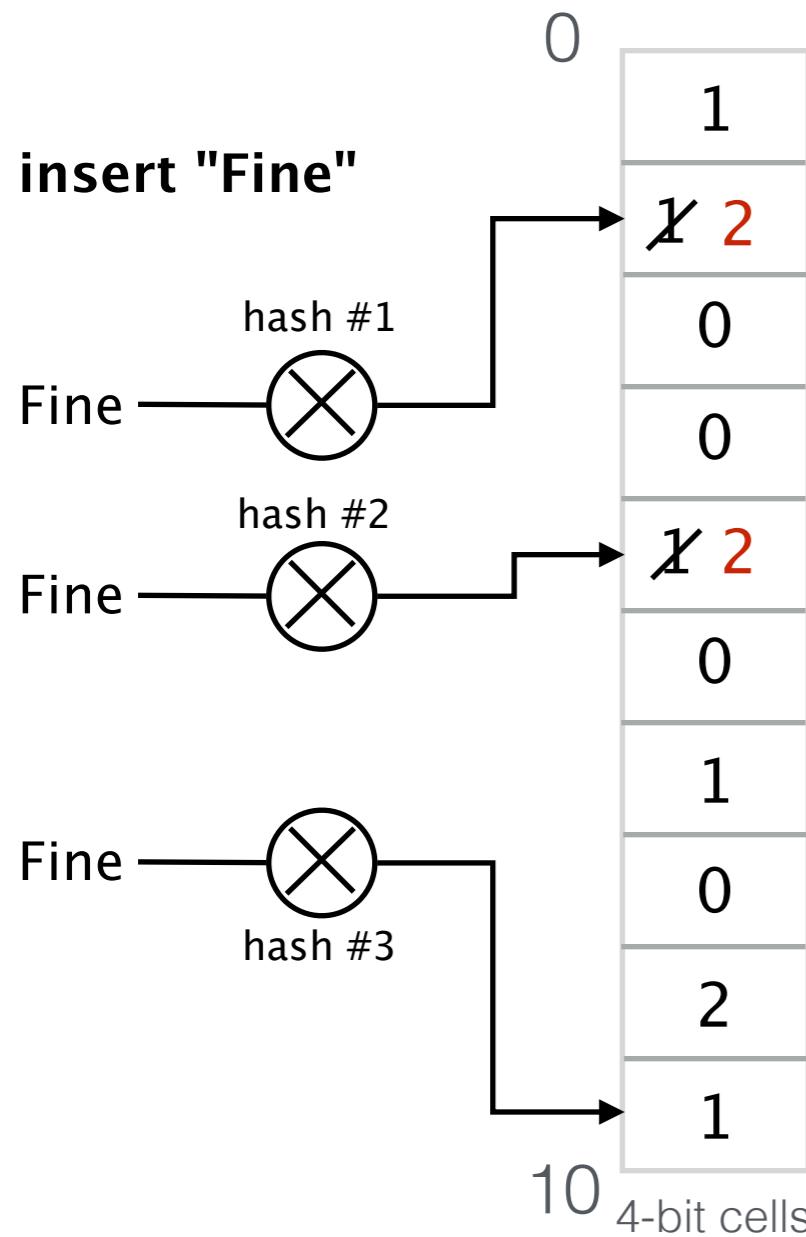
This extended version is called a **Counting Bloom Filter**



To add an element, increment
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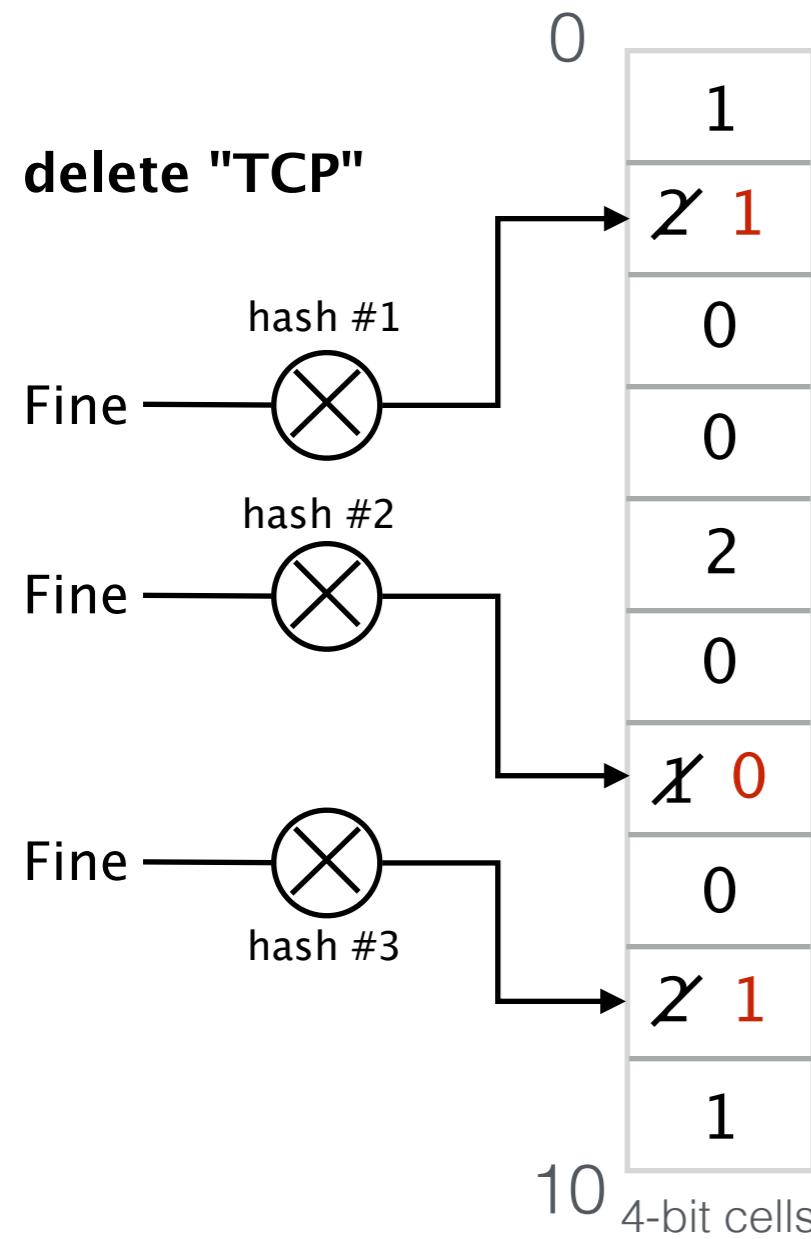
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To add an element, increment
the corresponding counters

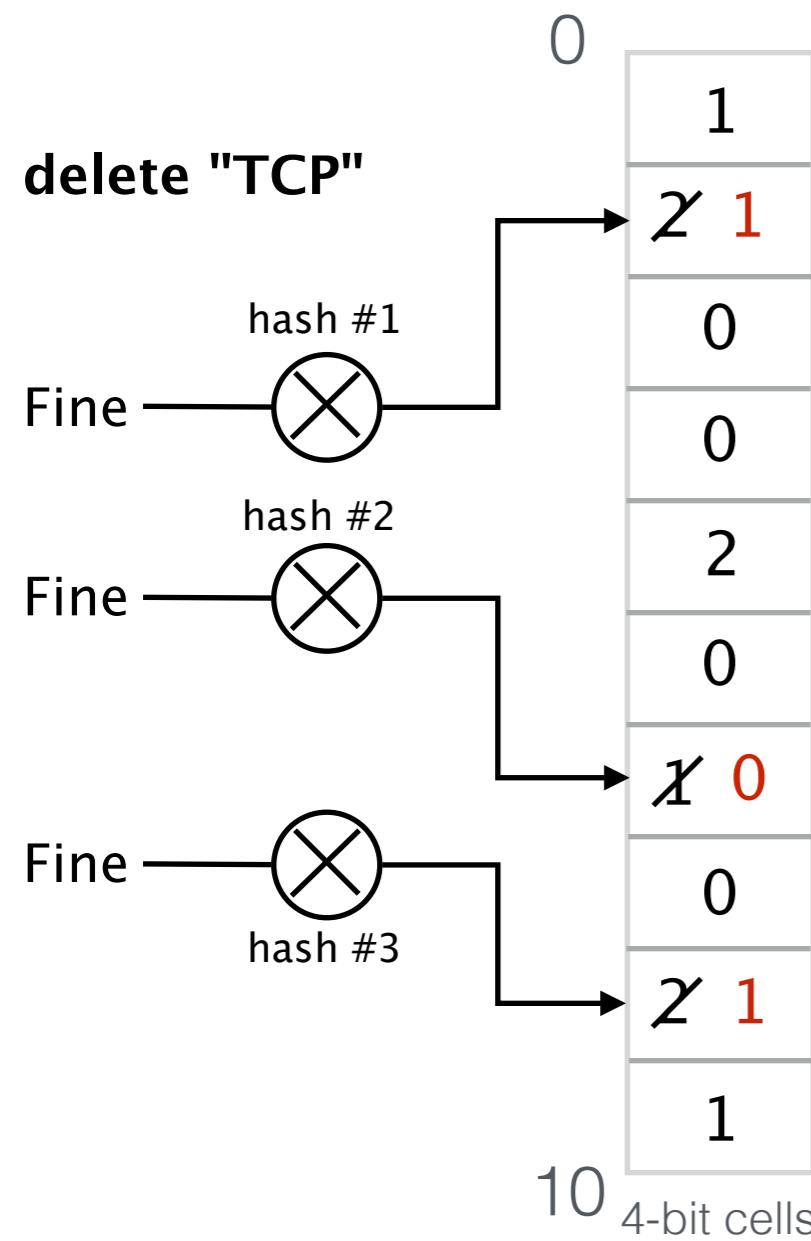
But we can easily extend them to handle deletions
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To add an element, increment the corresponding counters

To delete an element, decrement the corresponding counters

But we can easily extend them to handle deletions
This extended version is called a **Counting Bloom Filter**



To add an element, increment the corresponding counters

To delete an element, decrement the corresponding counters

All of our prior analysis for standard bloom filters applies to counting bloom filters

Counting Bloom Filters do handle **deletions**
at the price of using **more memory**

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Counters must be large enough to avoid overflow
If a counter eventually overflows, the filter may yield
false negatives

Counting Bloom Filters do handle **deletions**
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Counters must be large enough to avoid overflow
If a counter eventually overflows, the filter may yield
false negatives

Poisson approximation suggests 4 bits/counter
The average load (i.e., $\frac{NK}{M}$) is $\ln 2$ assuming $K = \ln 2 * (M/N)$
With $N=10000$ and $M=80000$ the probability that some
counter overflows if we use b -bit counters is at most
 $M * Pr(Poisson(\ln 2) \geq 2^b) = 1.78e-11$

Implementation of a Counting Bloom Filter in P4₁₆ with 2 hash functions

Add a new element

```
control MyIngress(...) {

    register register<bit<4>>(NB_CELLS) bloom_filter;

    apply {
        hash(meta.index1, HashAlgorithm.my_hash1, 0,
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);
        hash(meta.index2, HashAlgorithm.my_hash2, 0,
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);

        // Add a new element if not yet in the set
        bloom_filter.read(meta.query1, meta.index1);
        bloom_filter.read(meta.query2, meta.index2);

        if (meta.query1 == 0 || meta.query2 == 0) {
            bloom_filter.write(meta.index1, meta.query1 + 1);
            bloom_filter.write(meta.index2, meta.query2 + 1);
        }
    }
}
```

Implementation of a Counting Bloom Filter in P4₁₆ with 2 hash functions

Delete an element

```
control MyIngress(...) {

    register register<bit<32>>(NB_CELLS) bloom_filter;

    apply {
        hash(meta.index1, HashAlgorithm.my_hash1, 0,
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);
        hash(meta.index2, HashAlgorithm.my_hash2, 0,
            {meta.dstPrefix, packet.ip.srcIP}, NB_CELLS);

        // Delete a element only if it is in the set
        bloom_filter.read(meta.query1, meta.index1);
        bloom_filter.read(meta.query2, meta.index2);

        if (meta.query1 > 0 && meta.query2 > 0) {
            bloom_filter.write(meta.index1, meta.query1 - 1);
            bloom_filter.write(meta.index2, meta.query2 - 1);
        }
    }
}
```

So far we have seen how to do insertions, deletions and membership queries

	strategy #1	strategy #2
output	Deterministic	Probabilistic
number of required operations	Probabilistic	Deterministic
		Bloom Filters Counting Bloom Filters

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Each cell contains three fields

count which counts the number of entries mapped to this cell

keySum which is the sum of all the keys mapped to this cell

valueSum which is the sum of all the values mapped to this cell

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Add a new key-value pair (assuming it is not in the set yet)

For each hash function
hash the key to find the index

Then at this index
increment the count by one
add key to keySum
add value to valueSum

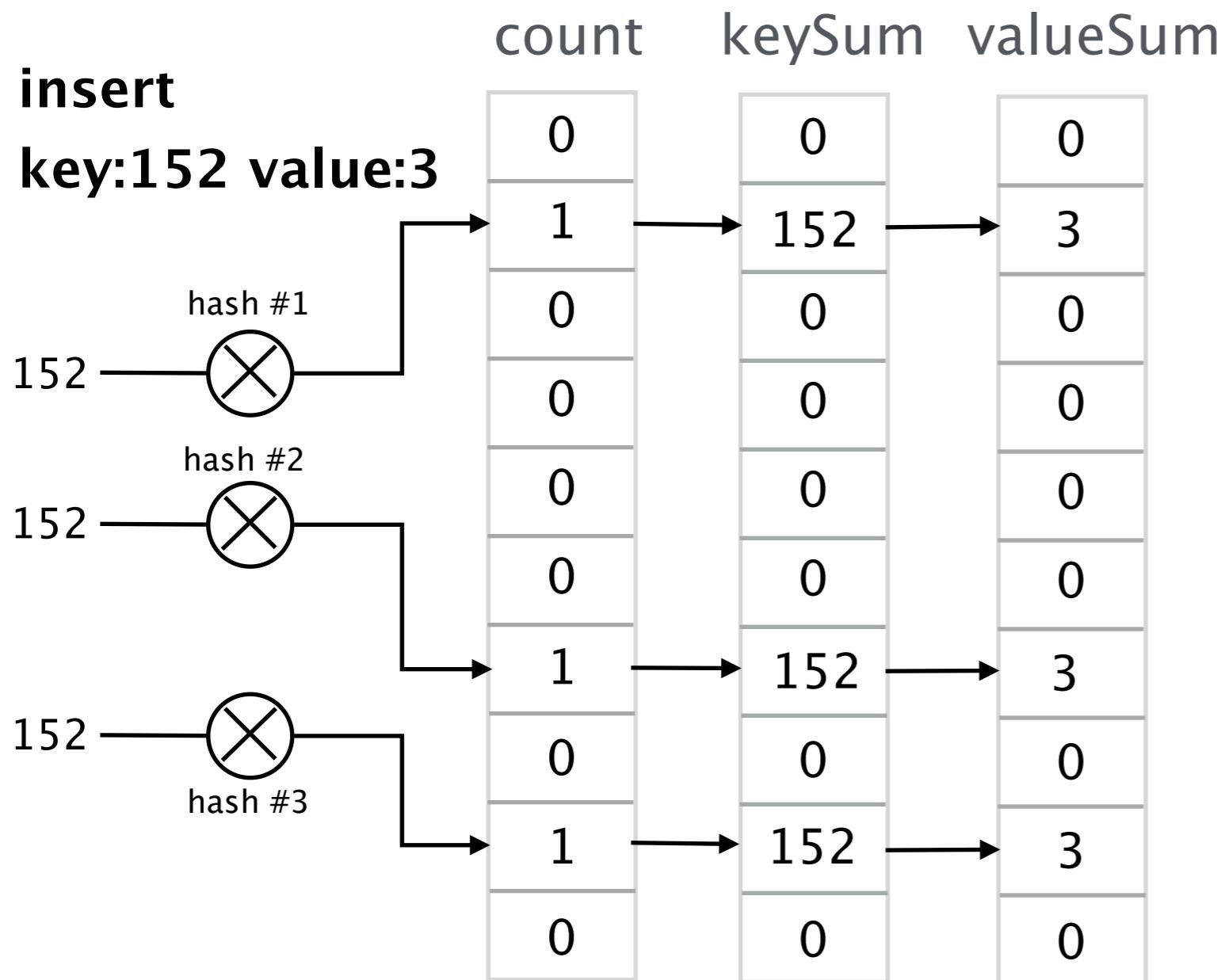
Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Delete a key-value pair (assuming it is in the set)

For each hash function
hash the key to find the index

Then at this index
subtract one to the count
subtract key to keySum
subtract value to valueSum

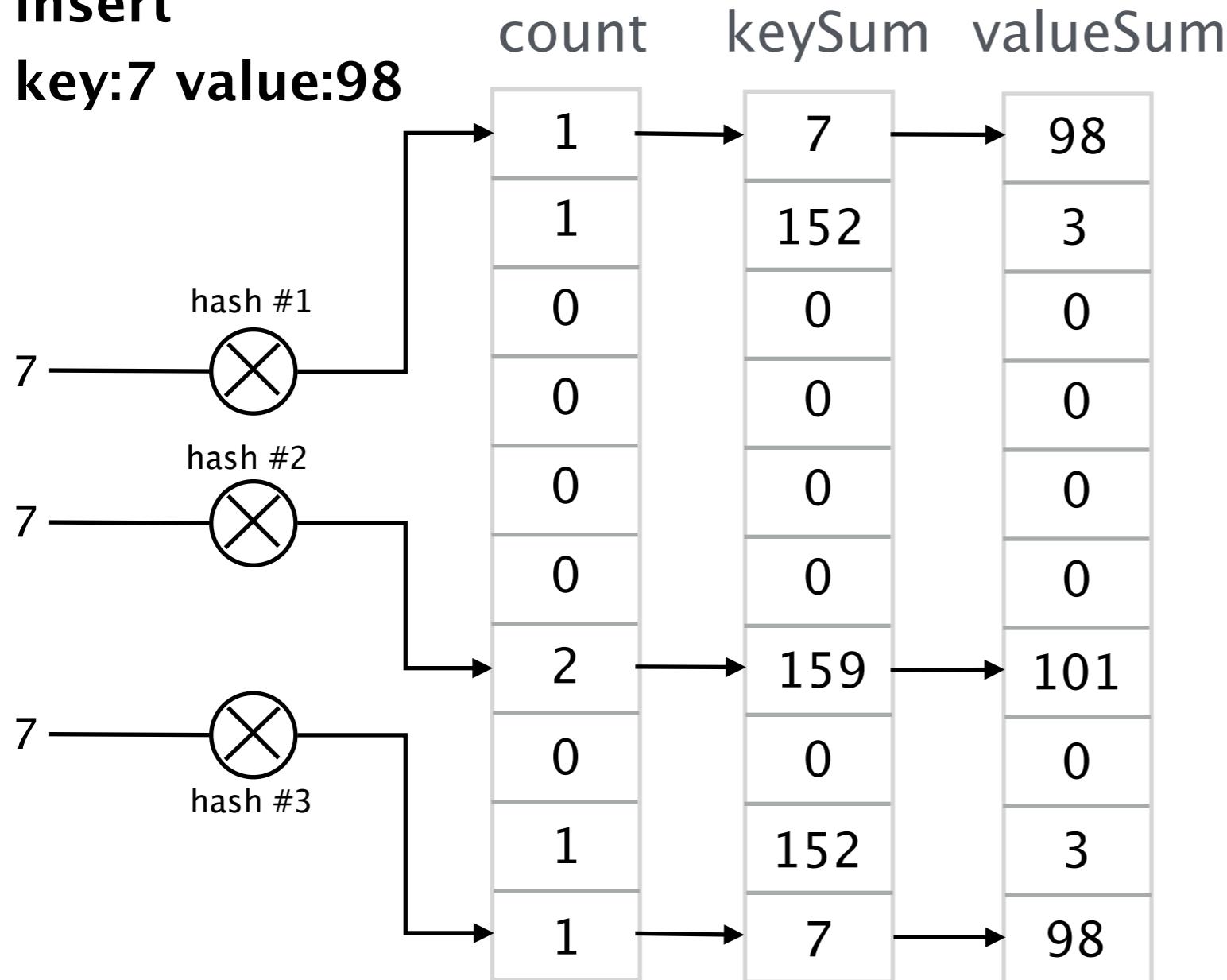
Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**



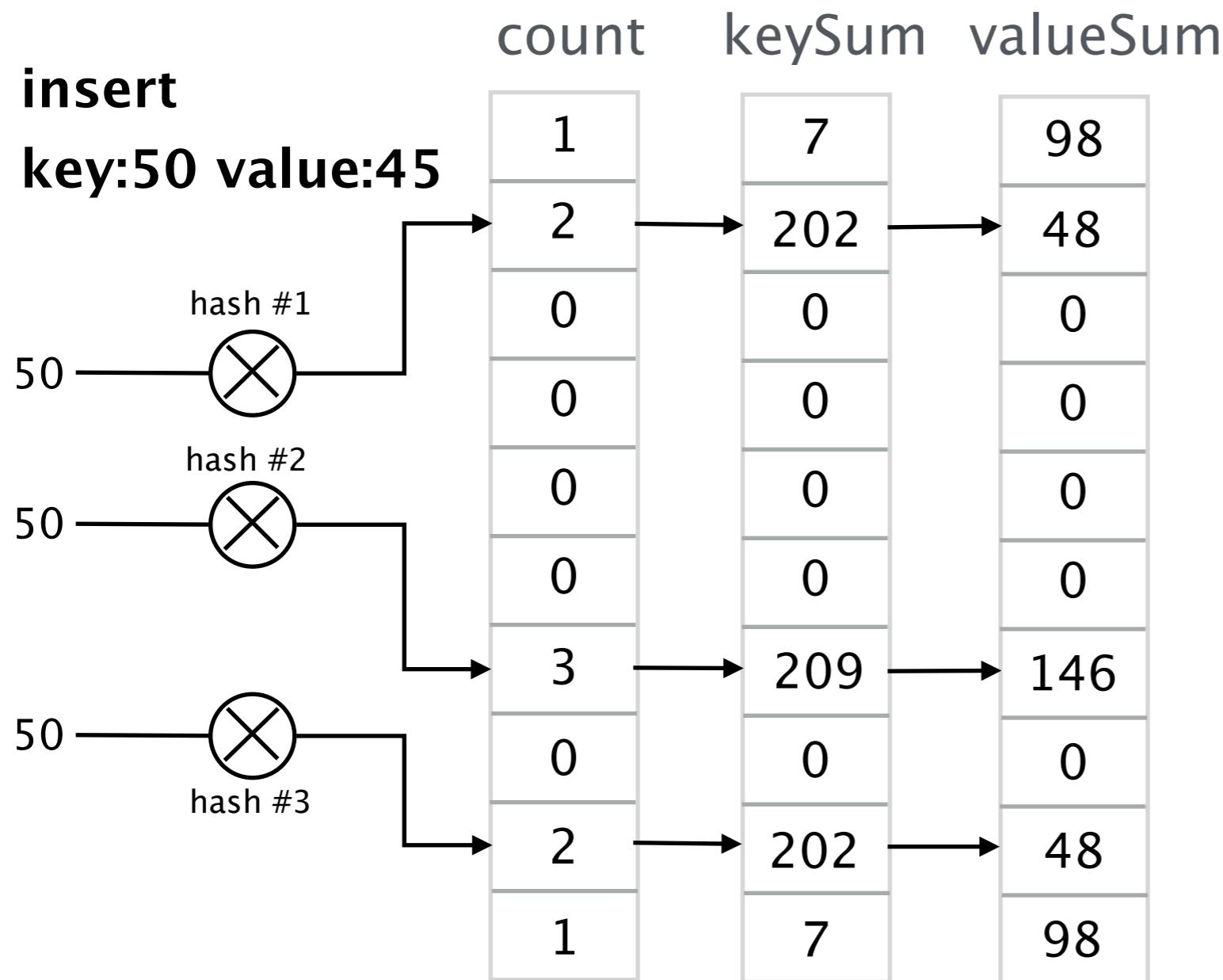
Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

insert

key:7 value:98



Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**



Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

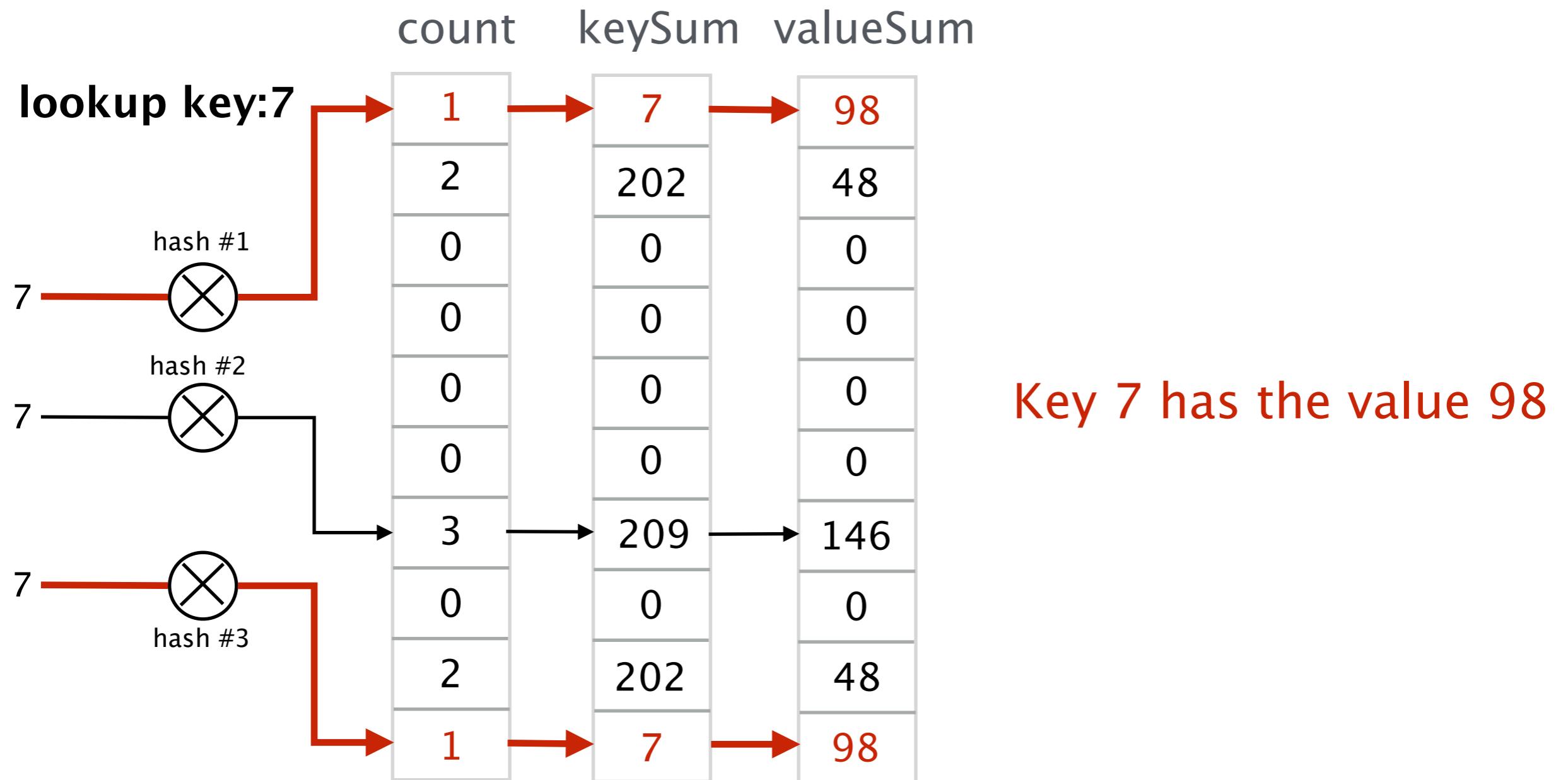
Key-value pair **lookup**

The value of a key can be found if the key is associated to **at least** one cell with a count = 1

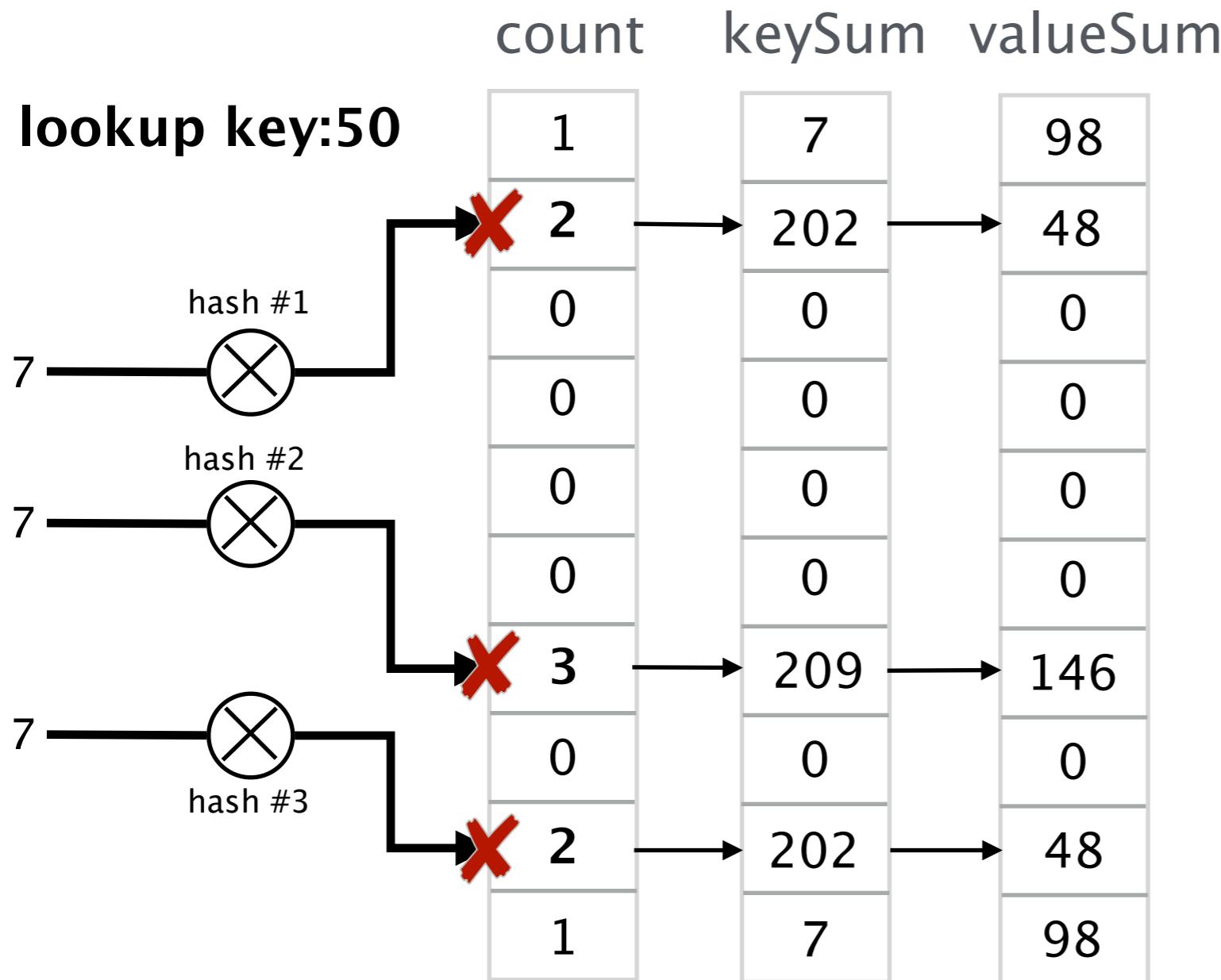
Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

count	keySum	valueSum
1	7	98
2	202	48
0	0	0
0	0	0
0	0	0
0	0	0
3	209	146
0	0	0
2	202	48
1	7	98

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**



Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**



The value for the key 50
can't be found

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Listing the IBLT

While there is an index for which count = 1
 Find the corresponding key-value pair and return it
 Delete the corresponding key-value pair

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

Listing the IBLT

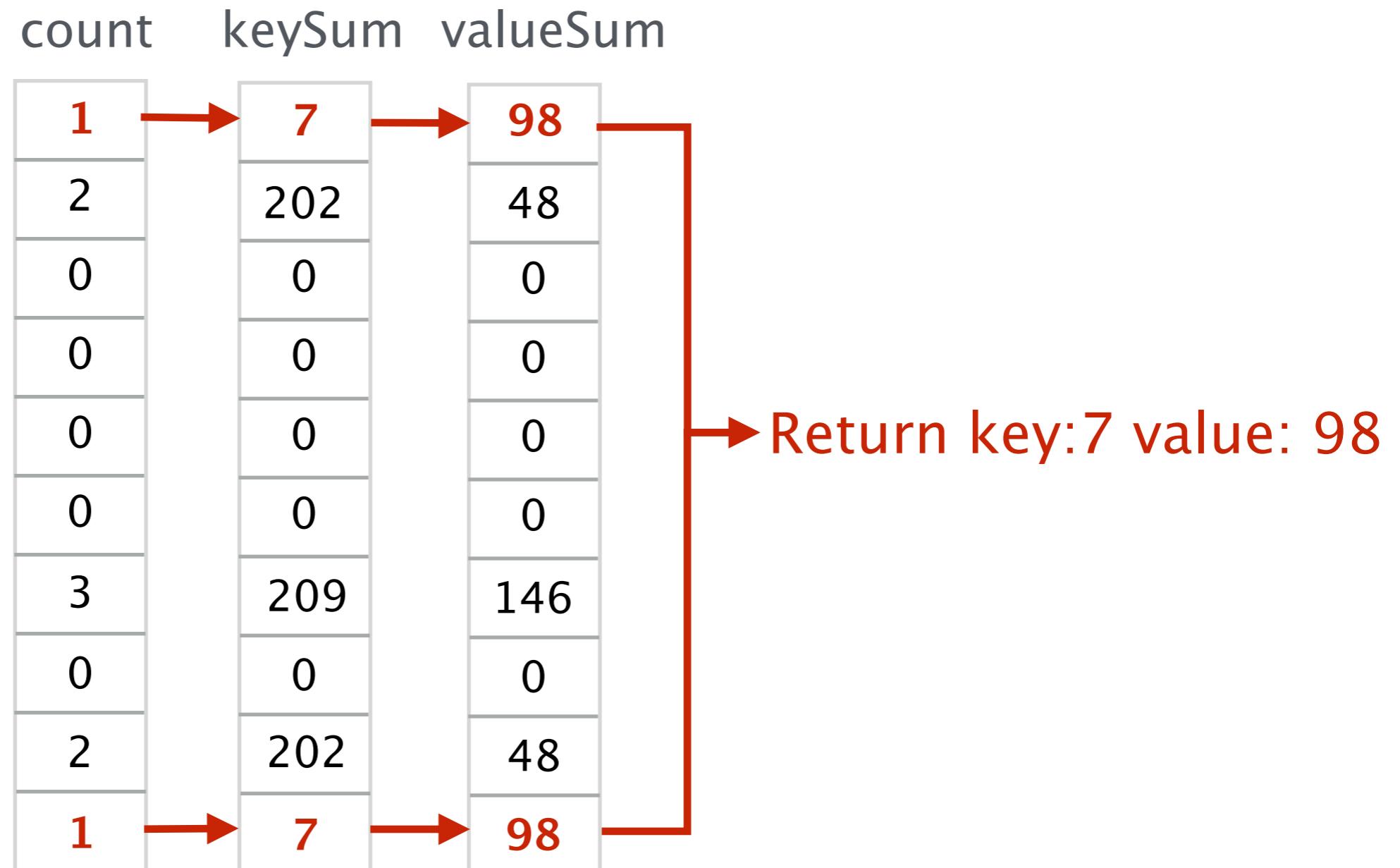
While there is an index for which count = 1
 | Find the corresponding key-value pair and return it
 | Delete the corresponding key-value pair

Unless the number of iterations is very low, loops
can't be implemented in hardware
The listing is done by the controller

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

count	keySum	valueSum
1	7	98
2	202	48
0	0	0
0	0	0
0	0	0
0	0	0
3	209	146
0	0	0
2	202	48
1	7	98

Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**



Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

count	keySum	valueSum
0	0	0
2	202	48
0	0	0
0	0	0
0	0	0
0	0	0
2	202	48
0	0	0
2	202	48
0	0	0

Delete key:7 value: 98

The diagram illustrates the state of an Invertible Bloom Lookup Table (IBLT) after a deletion. The table consists of three columns: count, keySum, and valueSum. Red arrows indicate the transformation of each row from its initial state to its new state after the deletion of key 7 with value 98.

- Row 1: Initial values (0, 0, 0) become (2, 202, 48).
- Row 2: Initial values (0, 0, 0) become (2, 202, 48).
- Row 3: Initial values (0, 0, 0) become (0, 0, 0).

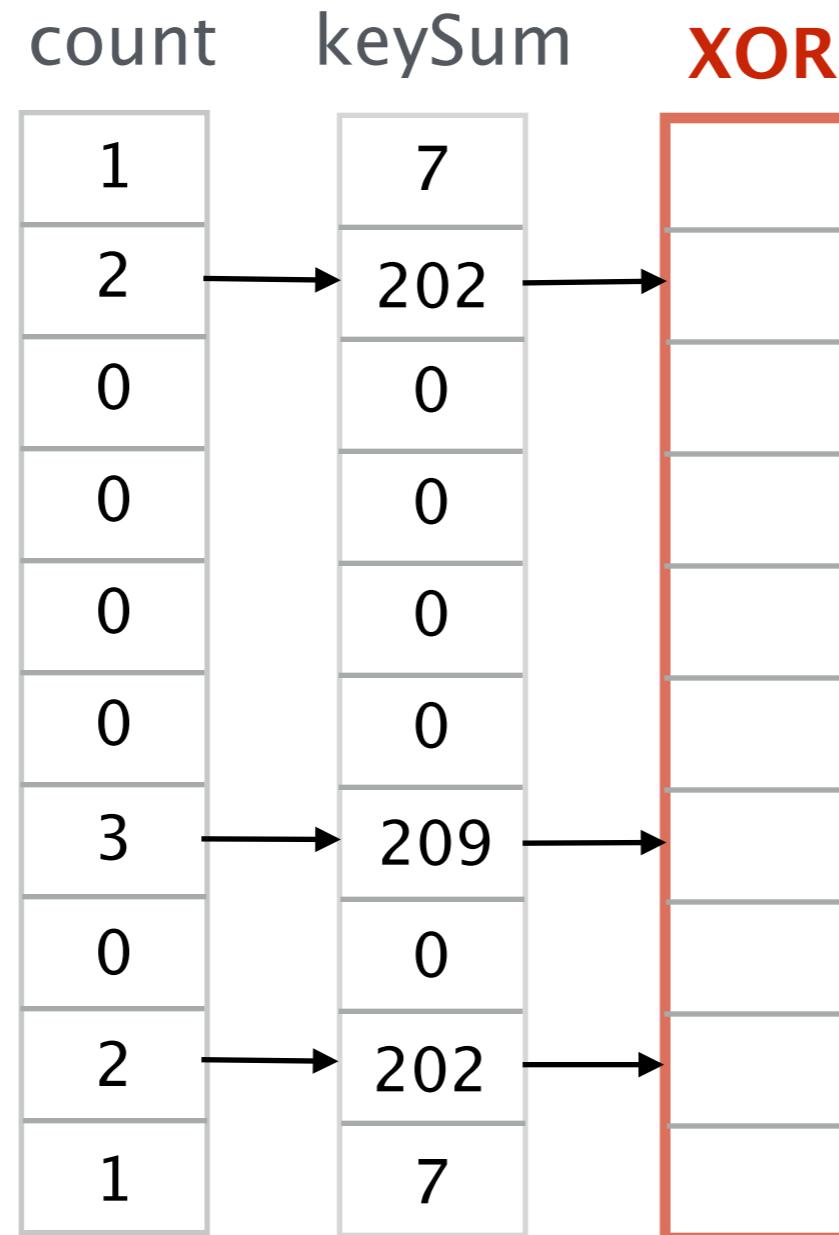
Invertible Bloom Lookup Tables (IBLT) stores key-value pairs and allows for **lookups** and a complete **listing**

count	keySum	valueSum
0	0	0
2	202	48
0	0	0
0	0	0
0	0	0
0	0	0
2	202	48
0	0	0
2	202	48
0	0	0

In this example, a complete listing is not possible

In many settings, we can use XORs in place of sums

For example to avoid overflow issues



For further information about Bloom Filters, Counting Bloom Filters and IBLT

Space/Time Trade-offs in Hash Coding with Allowable Errors. Burton H. Bloom. 1970.

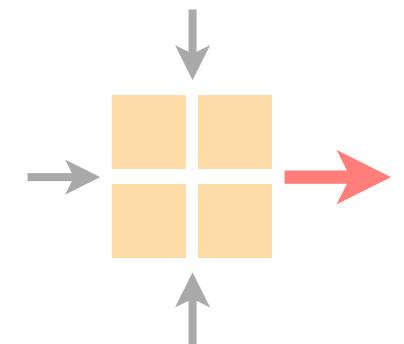
Network Applications of Bloom Filters: A Survey.
Andrei Broder and Michael Mitzenmacher. 2004.

Invertible Bloom Lookup Tables.
Michael T. Goodrich and Michael Mitzenmacher. 2015.

FlowRadar: A Better NetFlow for Data Centers
Yuliang Li et al. NSDI 2016.

Advanced Topics in Communication Networks

Programming Network Data Planes



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