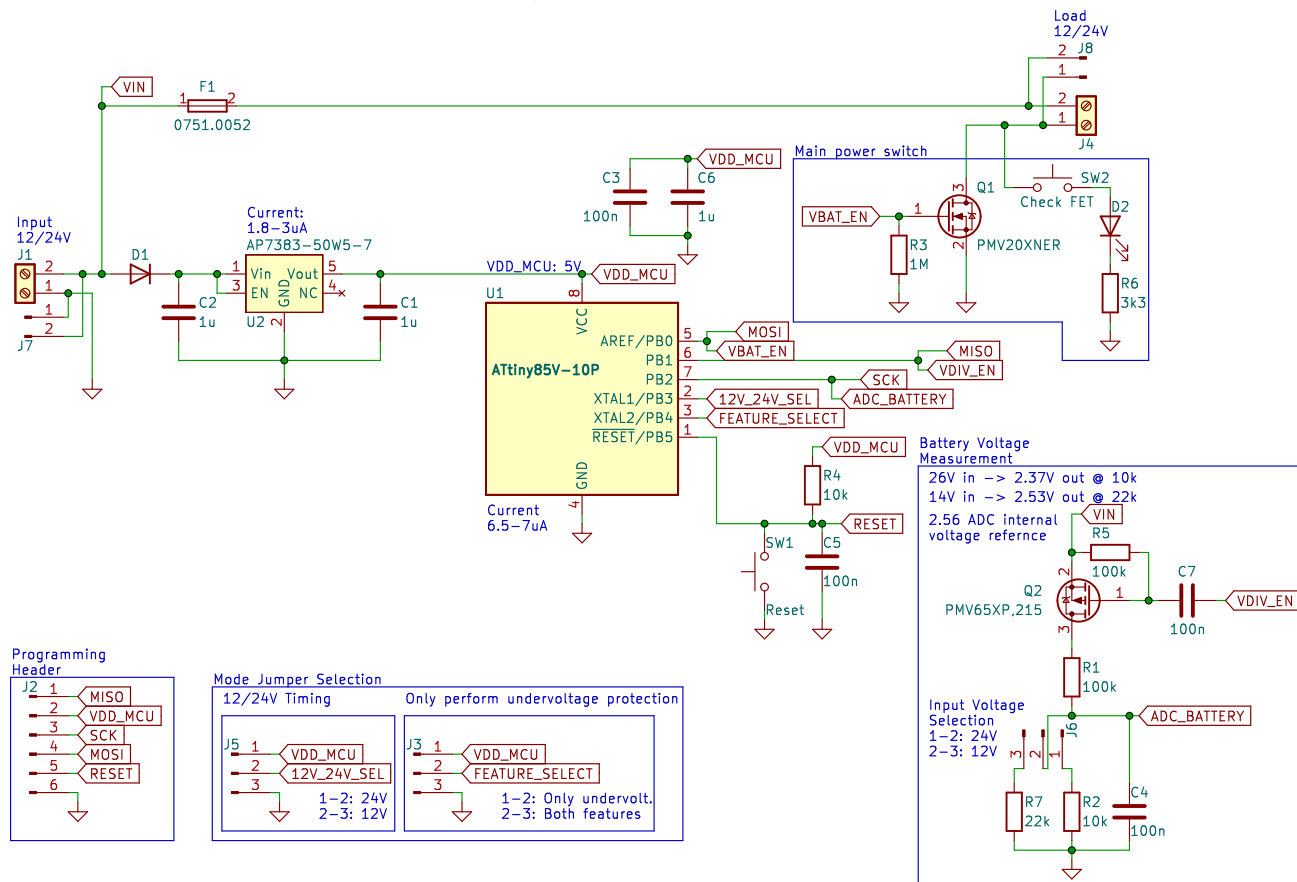


ATtiny Zeitschalter



Notes

- Power FET state after reset: ON
- Turn off power FET if input voltage is $<10/20V$
- 12V Timing:
- 24V Timing:

Current Consumption

- Active + ADC: ~2mA
- Sleep: 8.3–10uA
- Sleep for 8.192s, active for ~20ms

Sheet: /
File: attiny85_time_switch.kicad_sch

Title: ATtiny Zeitschalter

Size: A4	Date: 2023-05-25
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KiCad E.D.A. kicad 7.0.5

Rev: 1.0

Id: 1/1