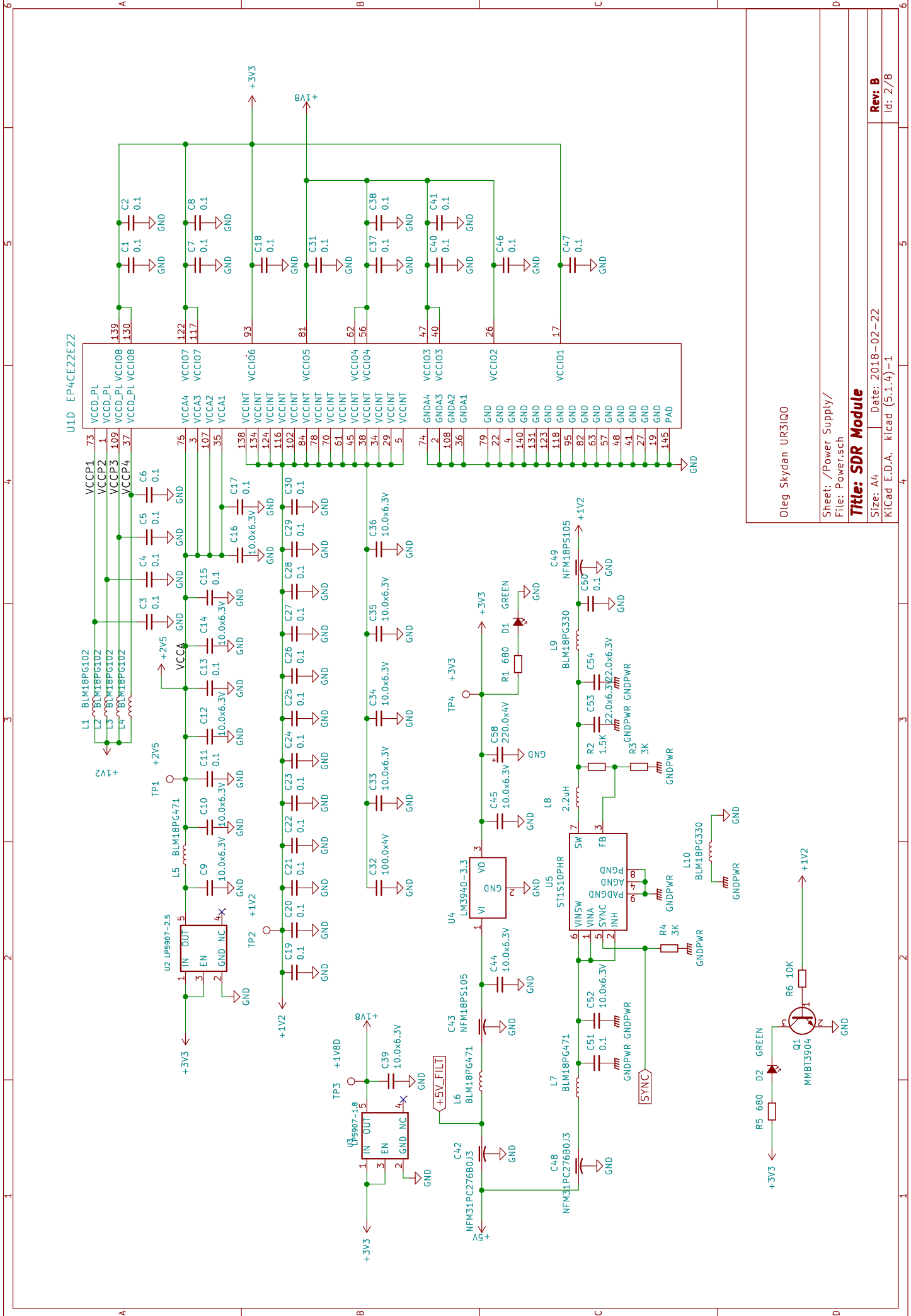


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A					
Sheet: Power Supply					
File: Power.sch					
A					
Sheet: ADC1					
File: ADC1.sch					
B					
Sheet: DAC					
File: DAC.sch					
C					
Sheet: RX VGA					
File: VGA.sch					
D					
Sheet: ADC2					
File: ADC2.sch					
B					
Sheet: Configuration & Clock					
File: Config.sch					
C					
Sheet: IO					
File: IO.sch					
D					
Sheet: /					
File: SDRModule_v2.sch					
Oleg Skydan UR3IQO					
Sheet: /					
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Title: SDR Module					
Size: A4					
Date:					
KICad E.D.A. kicad (5.1.4) - 1					
Rev: B					
Id: 1/8					
1	2	3	4	5	6



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File: Power.sch

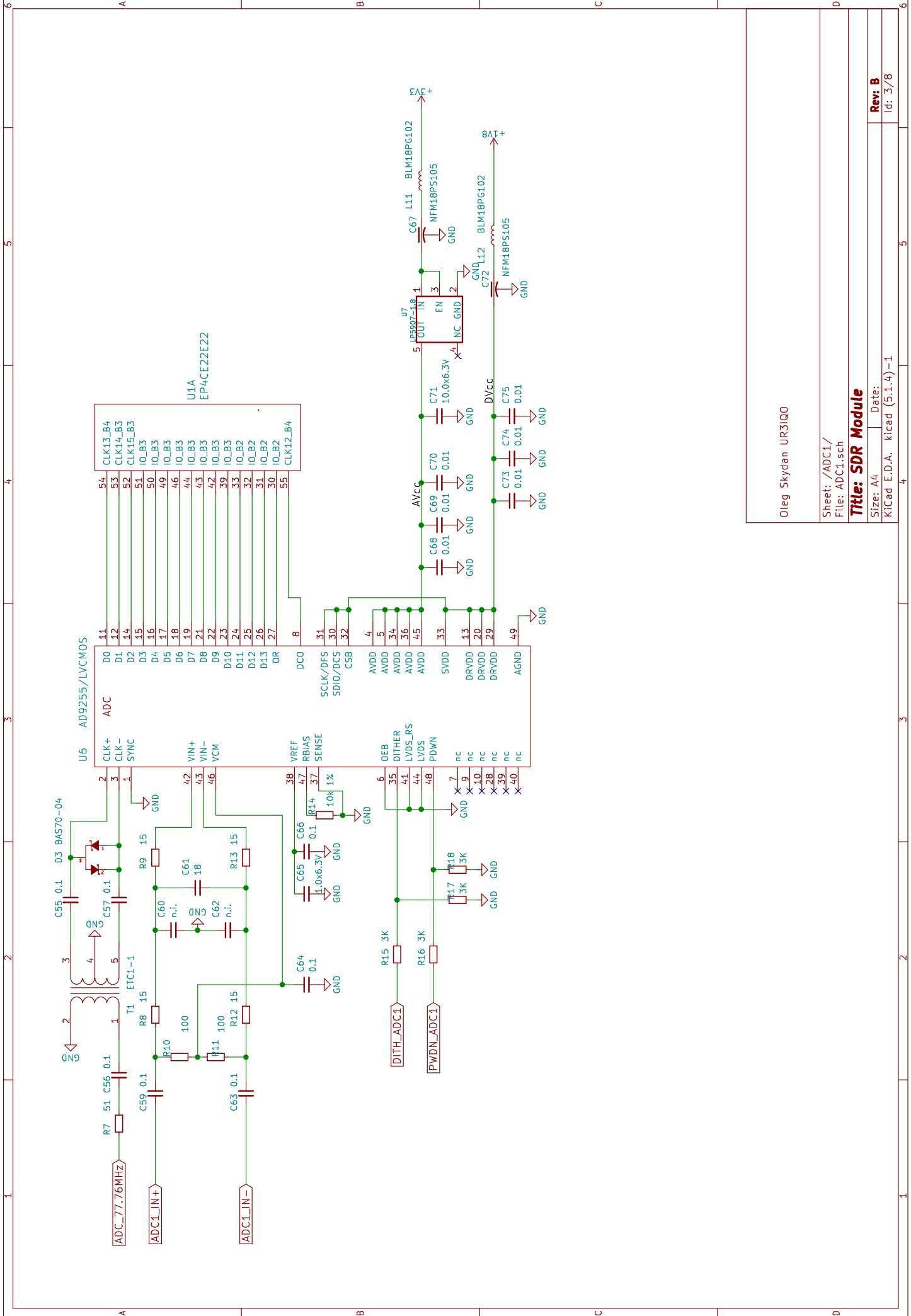
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Rev: B

Id: 2/8



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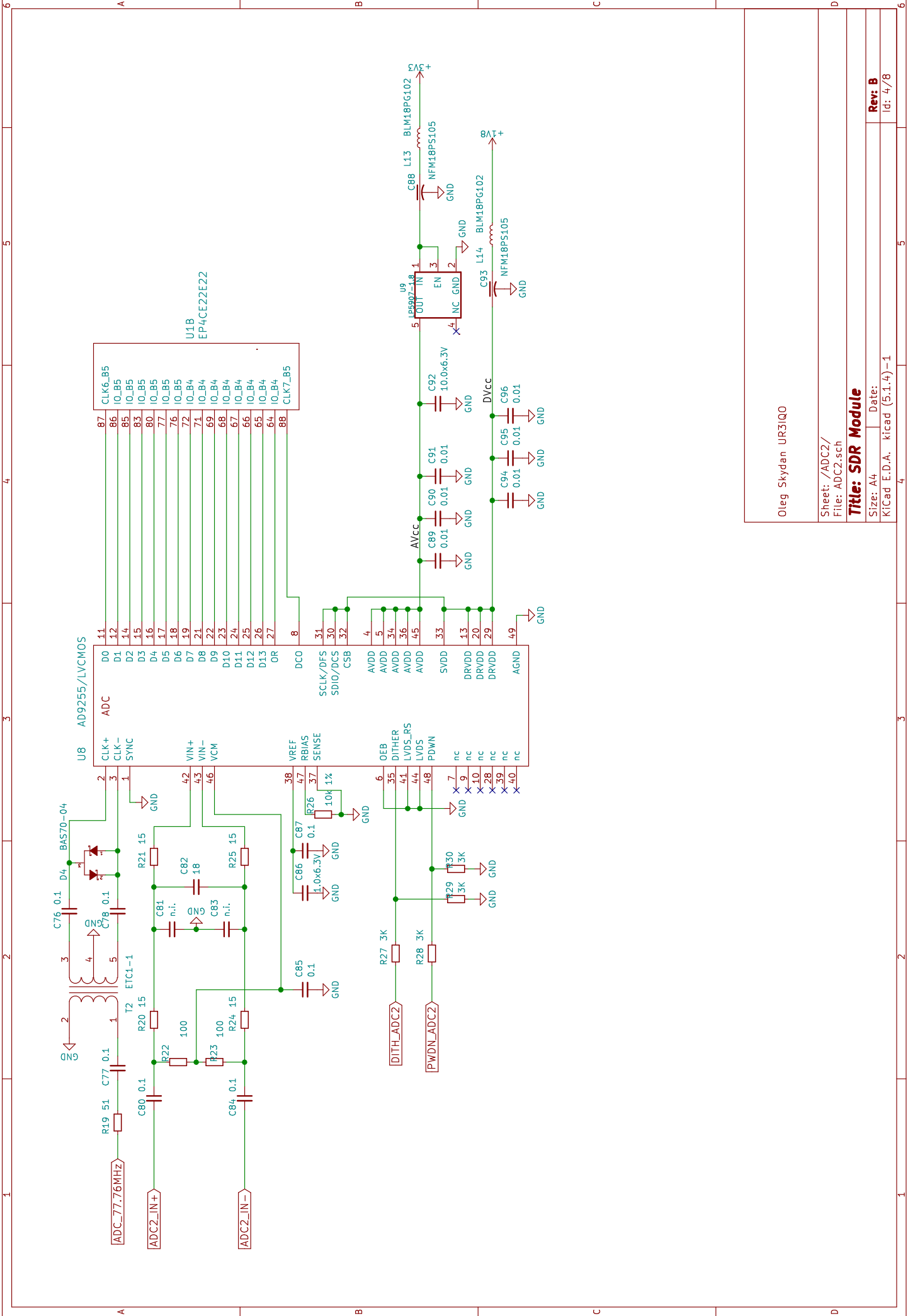
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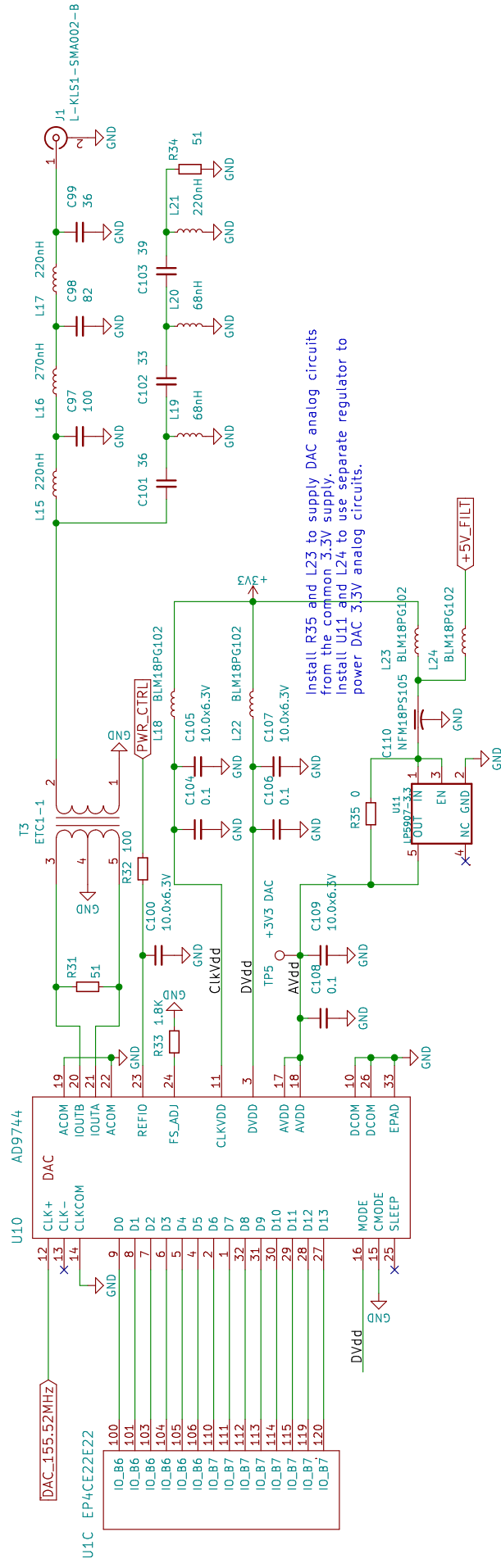
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**Rev: B**

Id: 3/8





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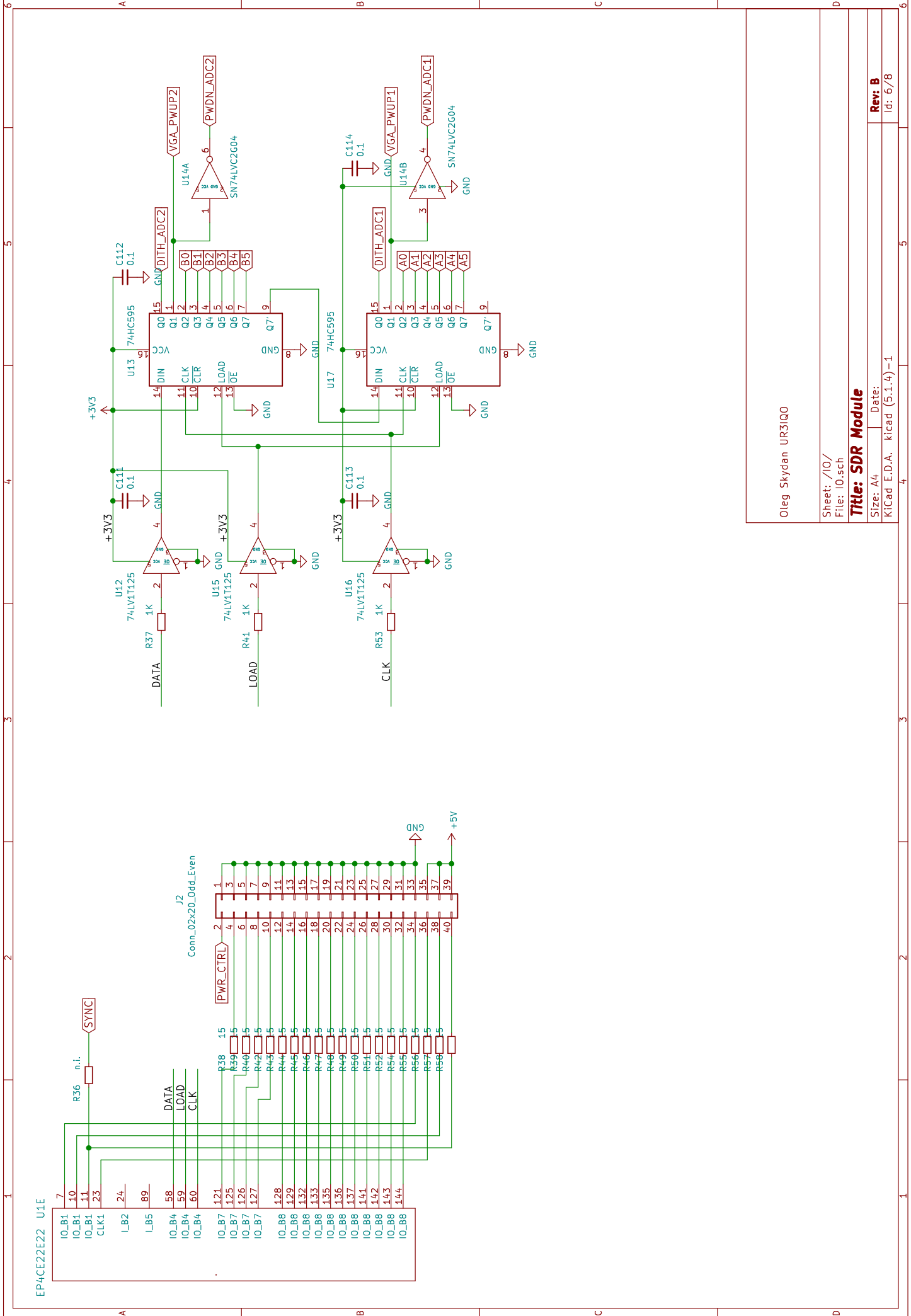
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<p>             KiCad E.D.A.    kicad (5.1.4) - 1           </p>
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Rev: B

Id: 5/8



Oleg Skydan UR3IQO

Sheet: /IO/  
File: IO.sch

**Title: SDR Module**

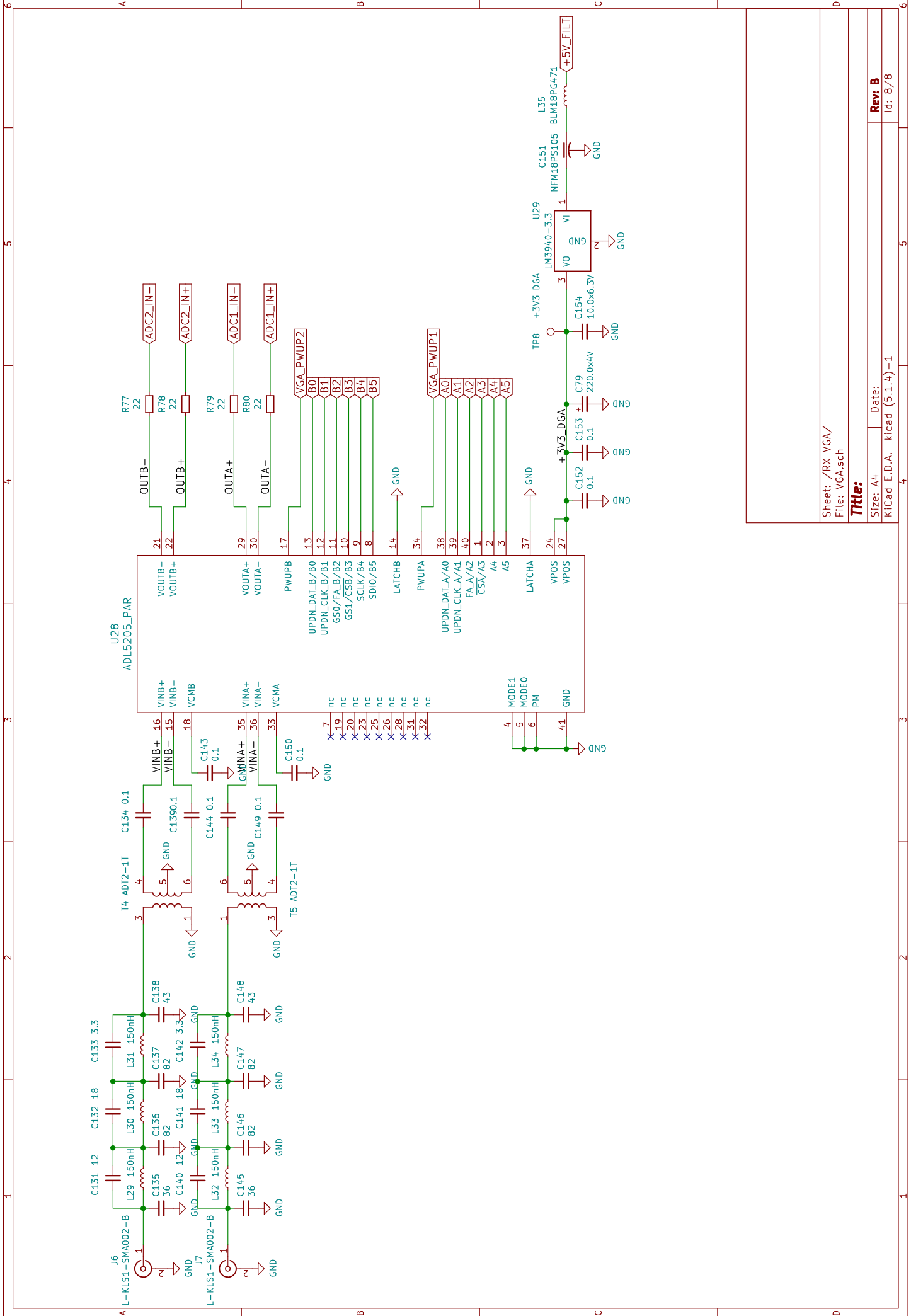
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KiCad E.D.A. kicad (5.1.4) - 1

**Rev: B**

Id: 6/8





Sheet: /RX VGA/  
File: VGA.sch

**Title:**

Size: A4 | Date:

KiCad E.D.A. kicad (5.1.4) - 1

**Rev: B**

Id: 8/8