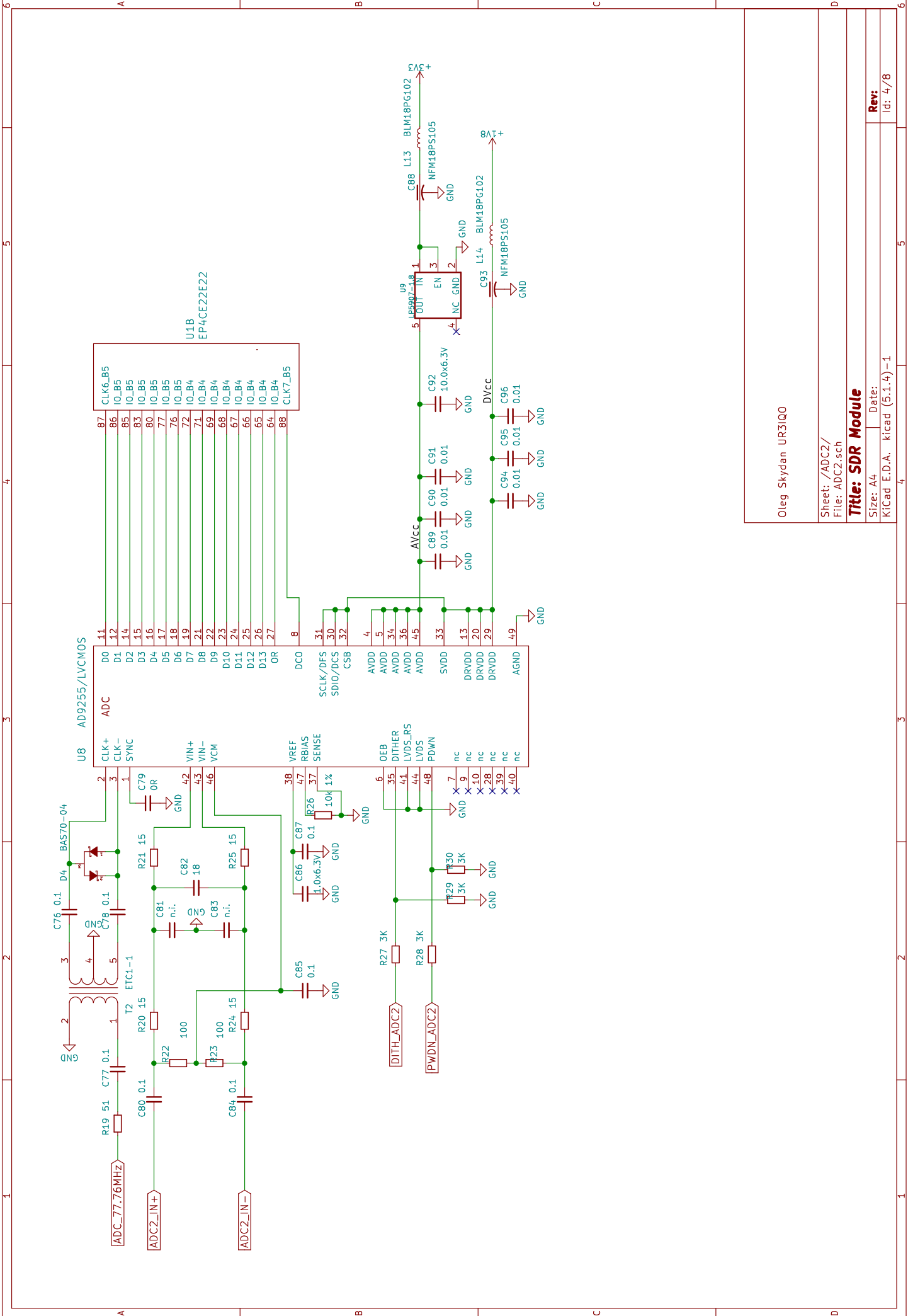


1	2	3	4	5	6
A					
Sheet: Power Supply					
File: Power.sch					
A					
Sheet: ADC1					
File: ADC1.sch					
B					
Sheet: DAC					
File: DAC.sch					
C					
Sheet: RX VGA					
File: VGA.sch					
D					
Sheet: Configuration & Clock					
File: Config.sch					
A					
Sheet: ADC2					
File: ADC2.sch					
B					
Sheet: IO					
File: IO.sch					
C					
Sheet: SDRModule_v2.sch					
File: SDRModule_v2.sch					
D					
Sheet: /					
File: SDRModule_v2.sch					
Title: SDR Module					
Size: A4					
Date:					
Kicad E.D.A. kicad (5.1.4) - 1					
Id: 1/8					
Rev:					
Oleg Skydan UR3IQO					
1					
2					
3					
4					
5					
6					







Oleg Skydan UR3IQO

Sheet: /ADC2/  
File: ADC2.sch

**Title: SDR Module**

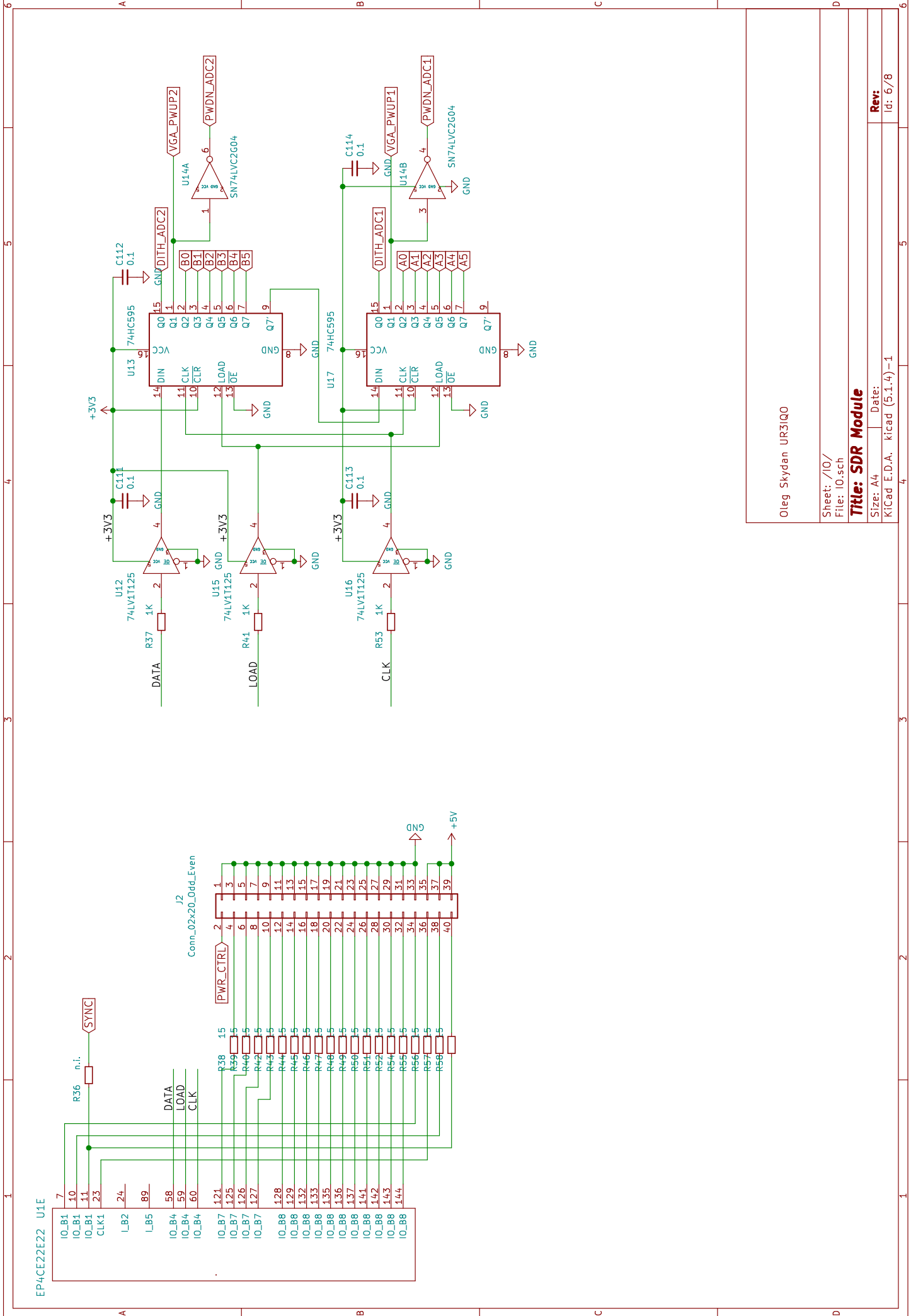
Size: A4 | Date:

KiCad E.D.A. kicad (5.1.4) - 1

**Rev:**

Id: 4/8





Oleg Skydan UR3IQO

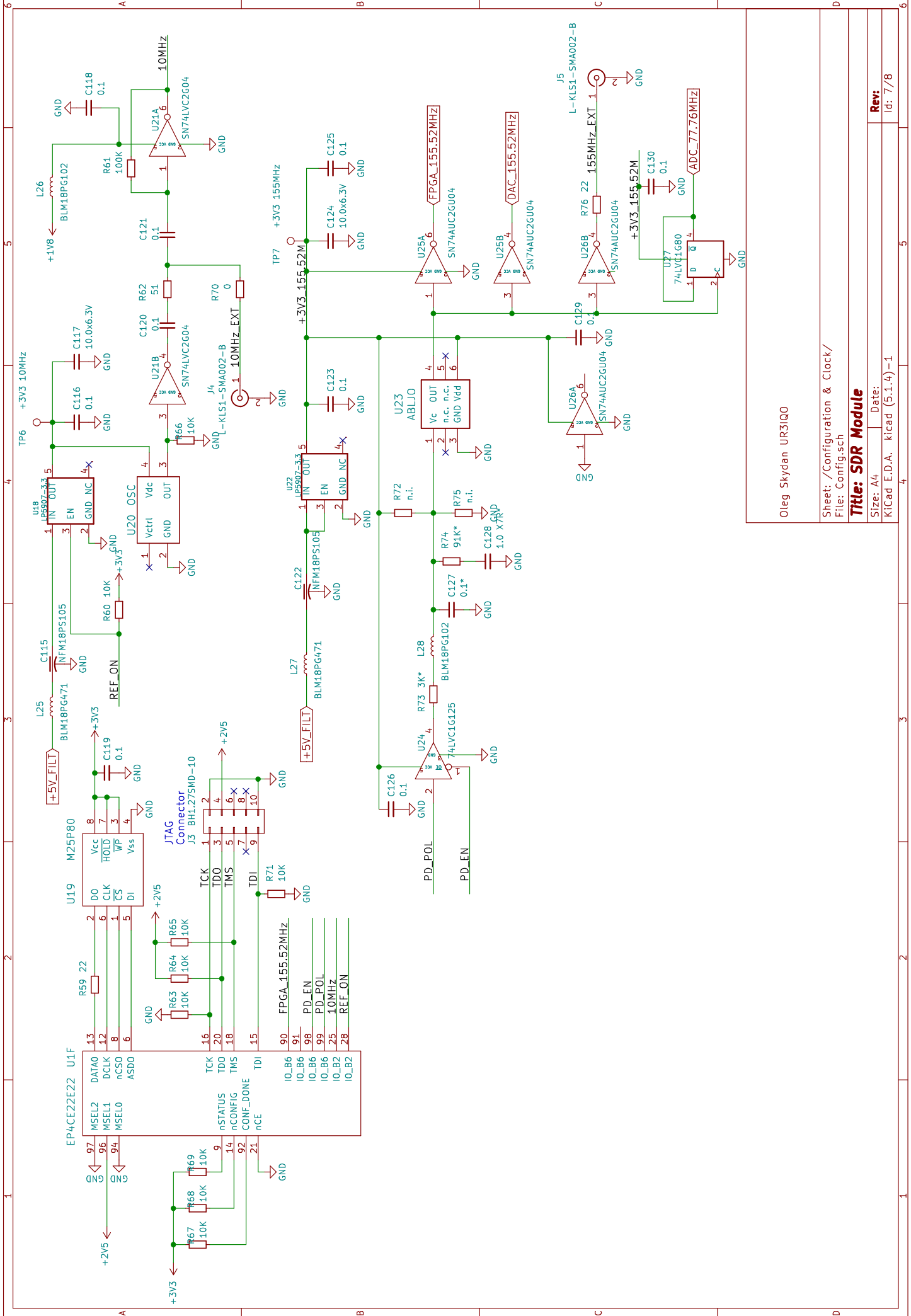
Sheet: /IO/  
File: IO.sch

**Title: SDR Module**

Size: A4 Date:

KiCad E.D.A. kicad (5.1.4) - 1

**Rev:**  
Id: 6/8



Oleg Skydan UR3IQO

Sheet: /Configuration & Clock/  
File: Config.sch

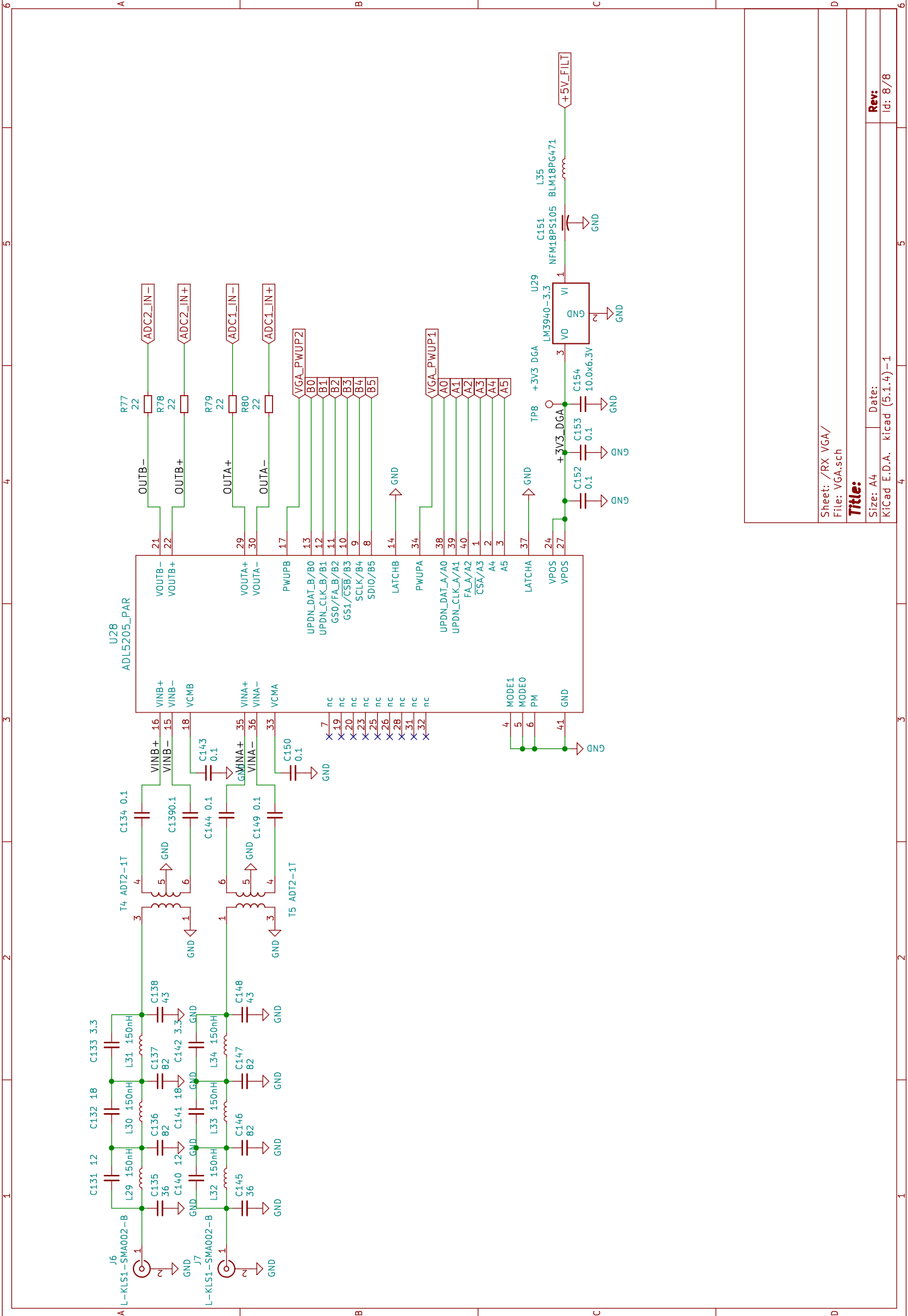
**Title: SDR Module**

Size: A4     Date:

KiCad E.D.A. kicad (5.1.4)-1

**Rev:**

Id: 7/78



Sheet: /RX VGA/  
File: VGA.sch

**Title:**

Size: A4 | Date:

KiCad E.D.A. kicad (5.1.4) - 1

**Rev:**

Id: 8/8