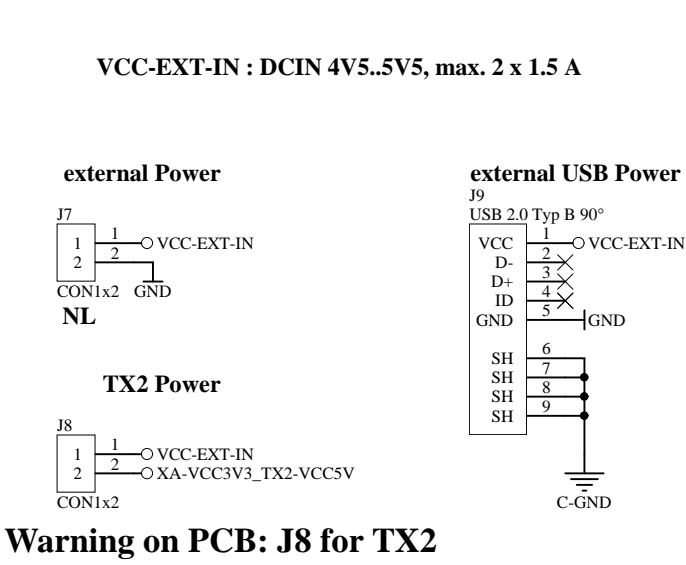
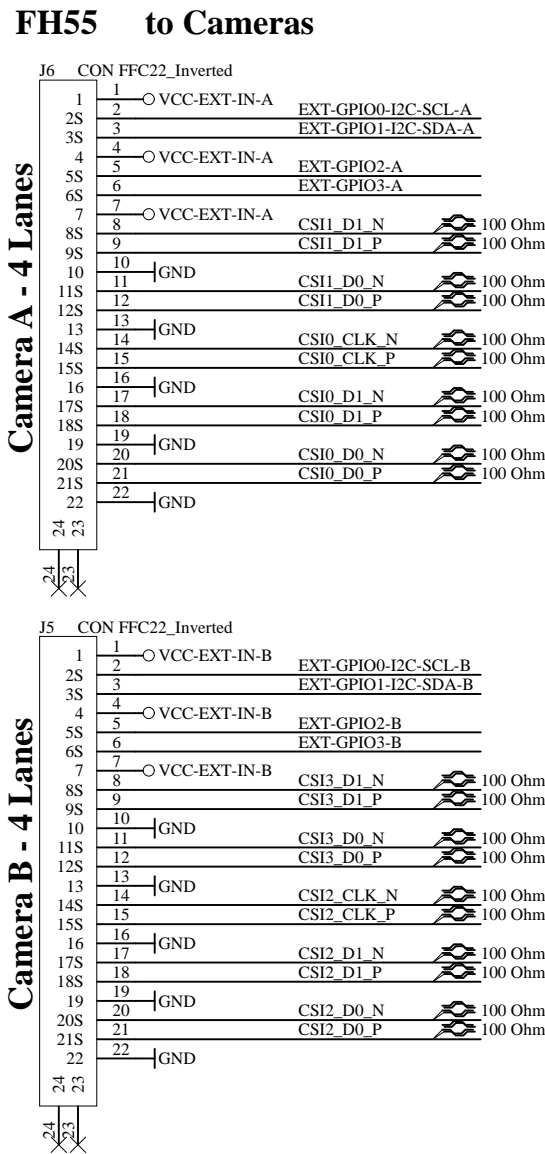
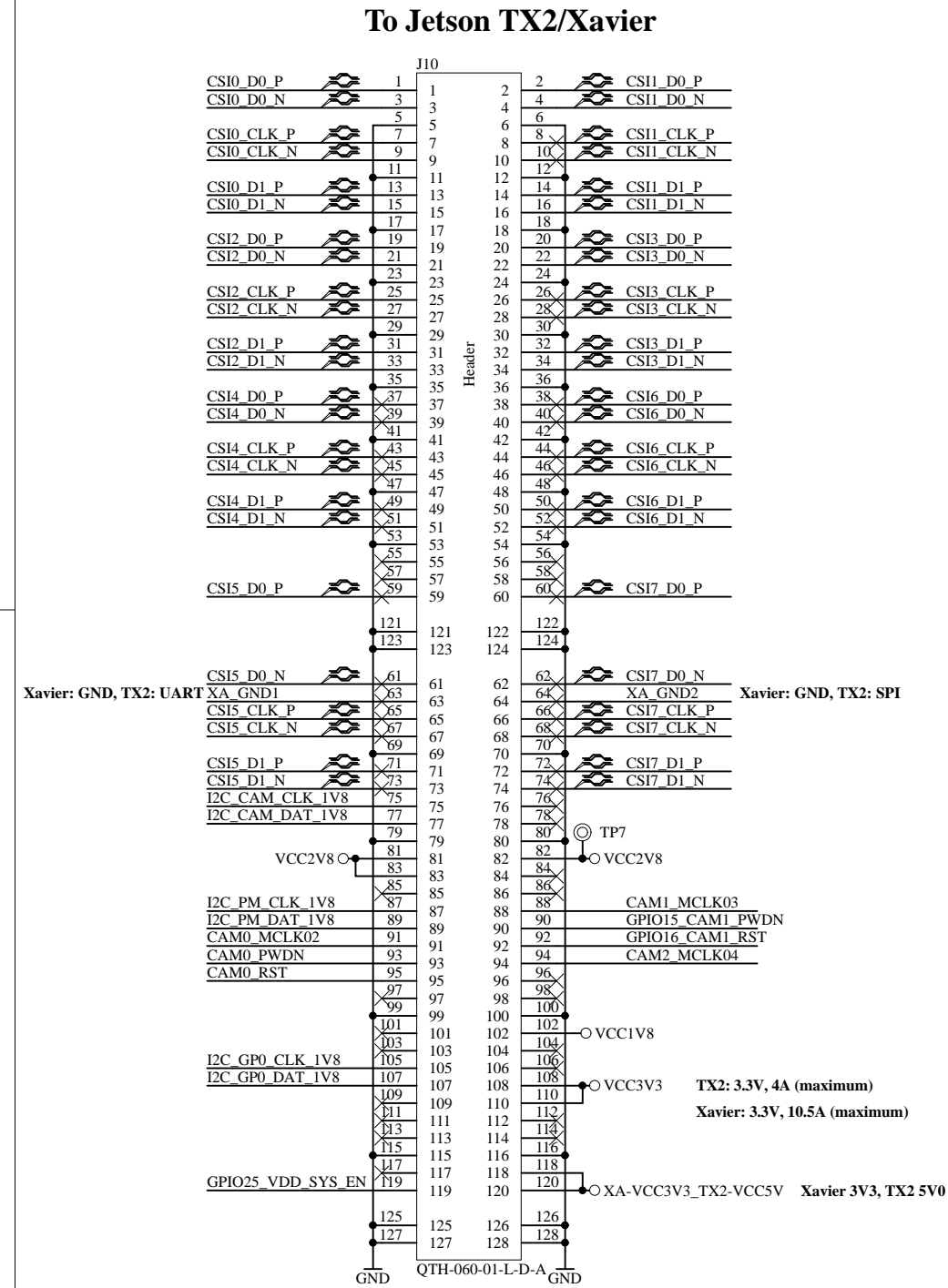


A

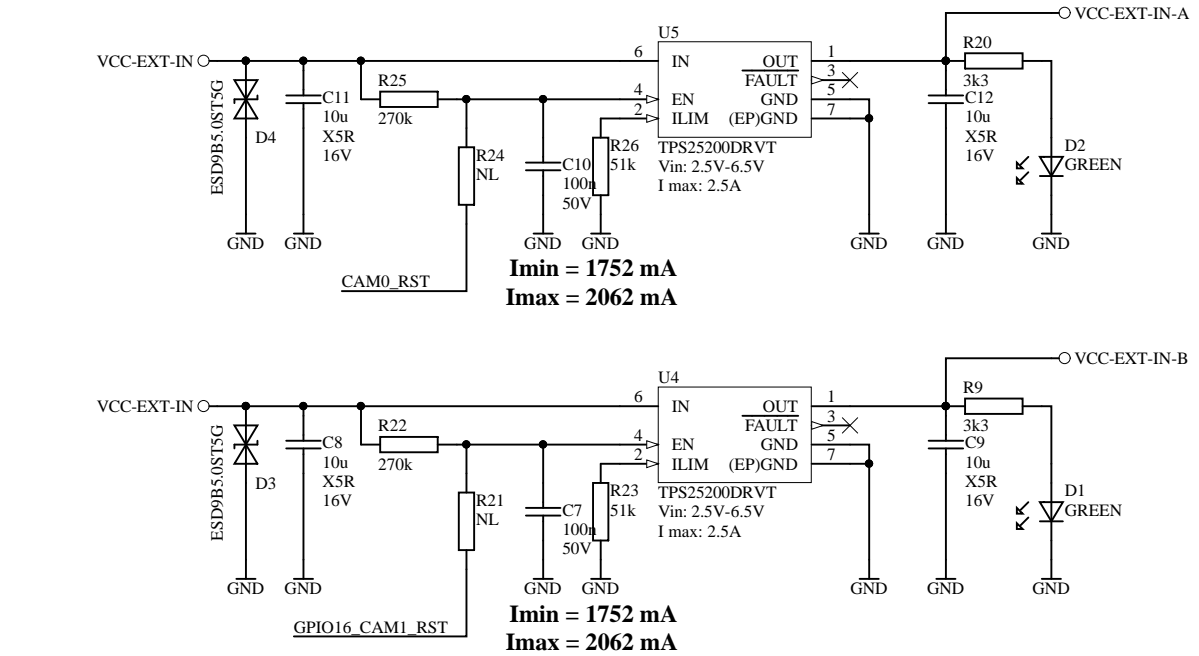
B

C

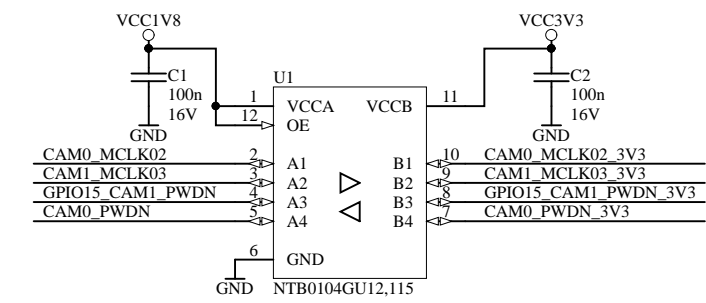
D



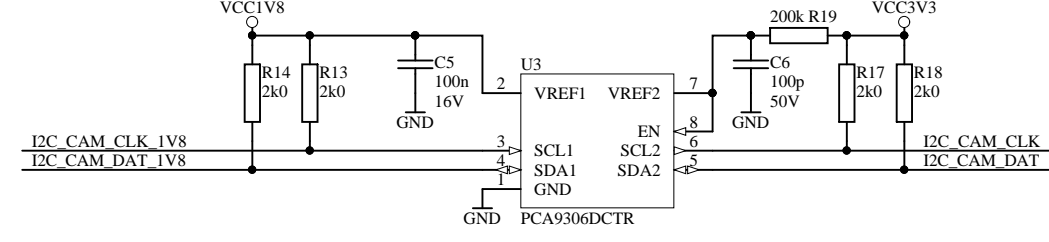
Warning on PCB: J8 for TX2



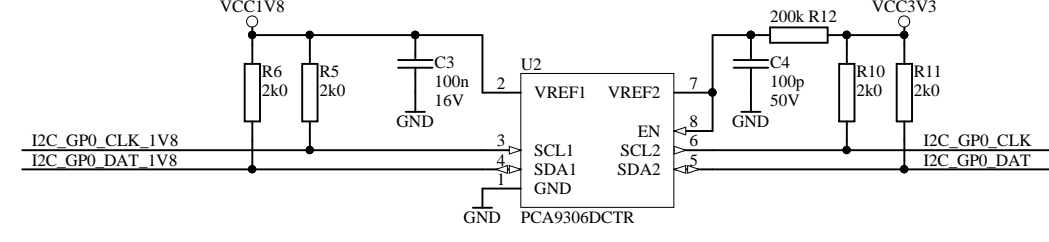
GPIO Levelshifter 1V8 <-> 3V3



I2C Levelshifter CAM



I2C Levelshifter GP0

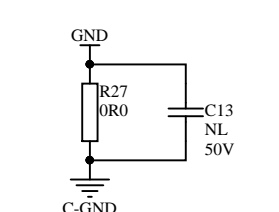


Based on:
TX2 Dev Kit Specification 20190408
Xavier Dev Kit Specification 20180809

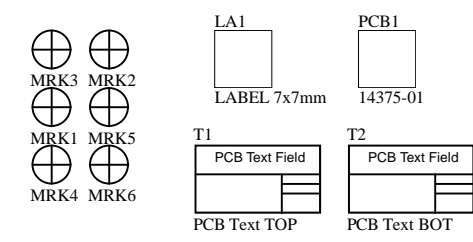
Unused GPIO 1V8




GND <-> Chassis-GND



Holes on Nvidia Carrier Boards are connected to GND



Allied Vision Technologies GmbH			Taschenweg 2a	07646 Stadtröda	 Allied Vision		
Title: CSI2-TX2-XAVIER_Rev01			Confidential				
PCB:	PCBA:	3					
Author: CWA	Checked by: OFU	2					
Date: 26.11.2019	Page: 1 of 1	1					
File: CSI2-TX2-XAVIER_Rev01.SchDoc		No.	Changes	Rev.:	Date:	Name:	