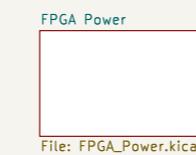


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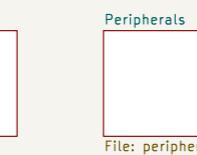
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File: peripherals.kicad_sch



open source
hardware

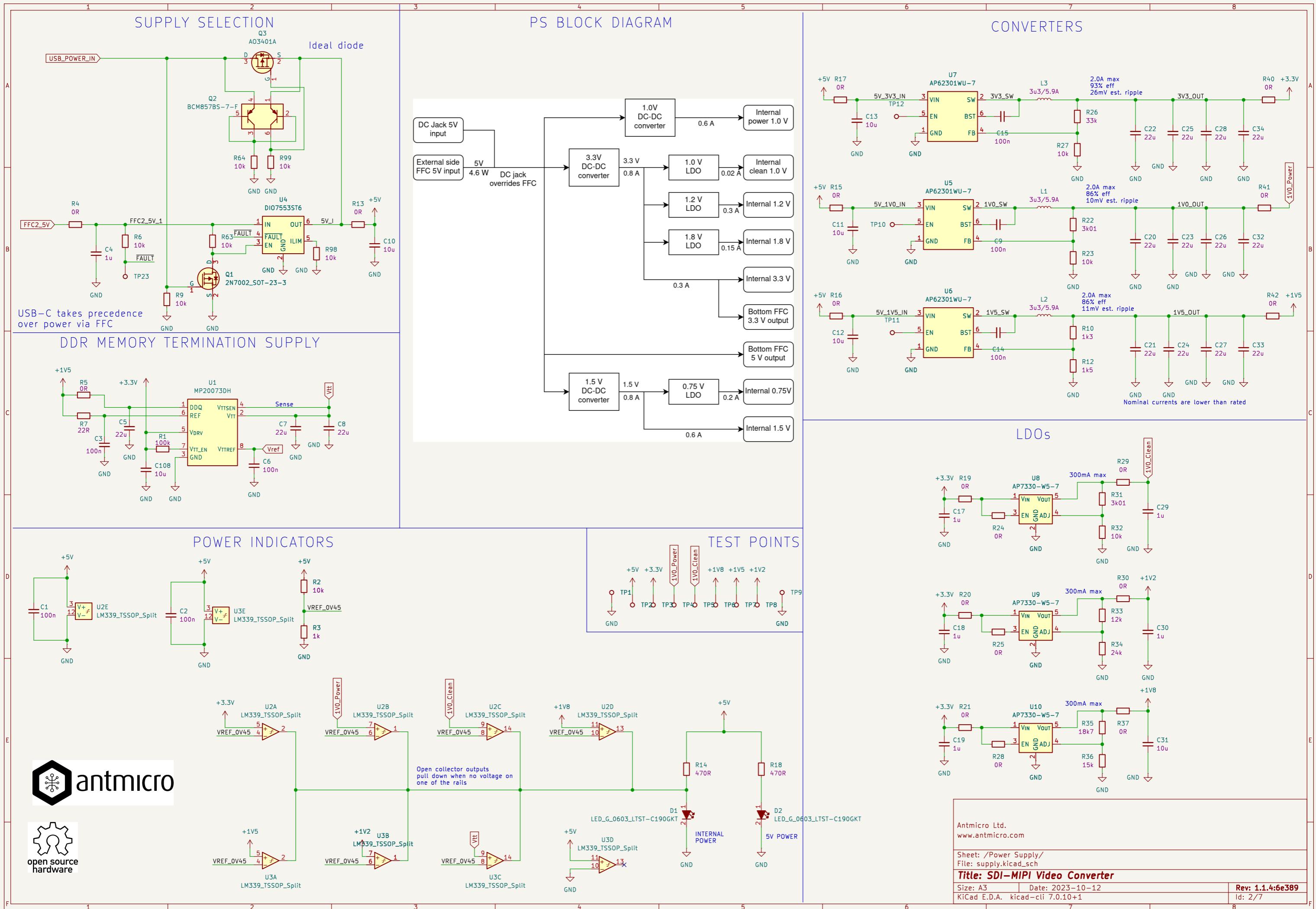
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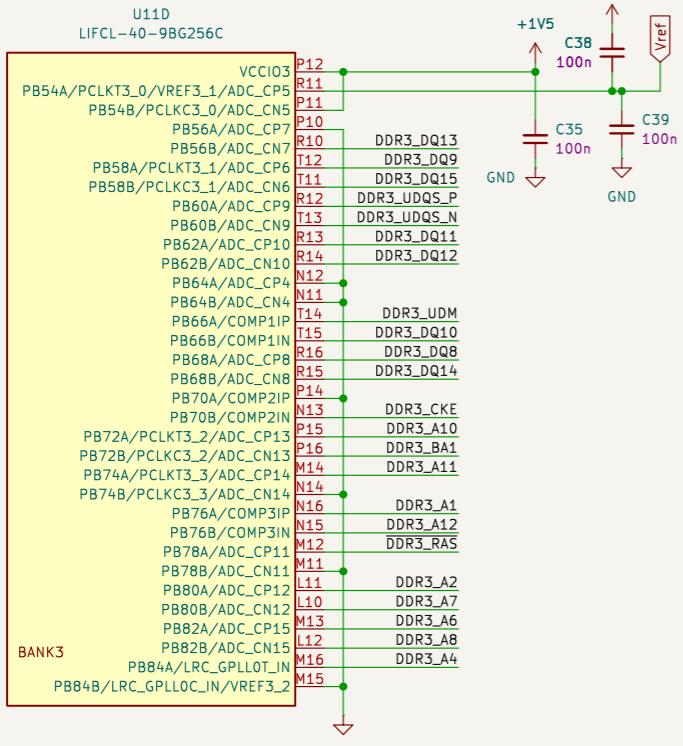
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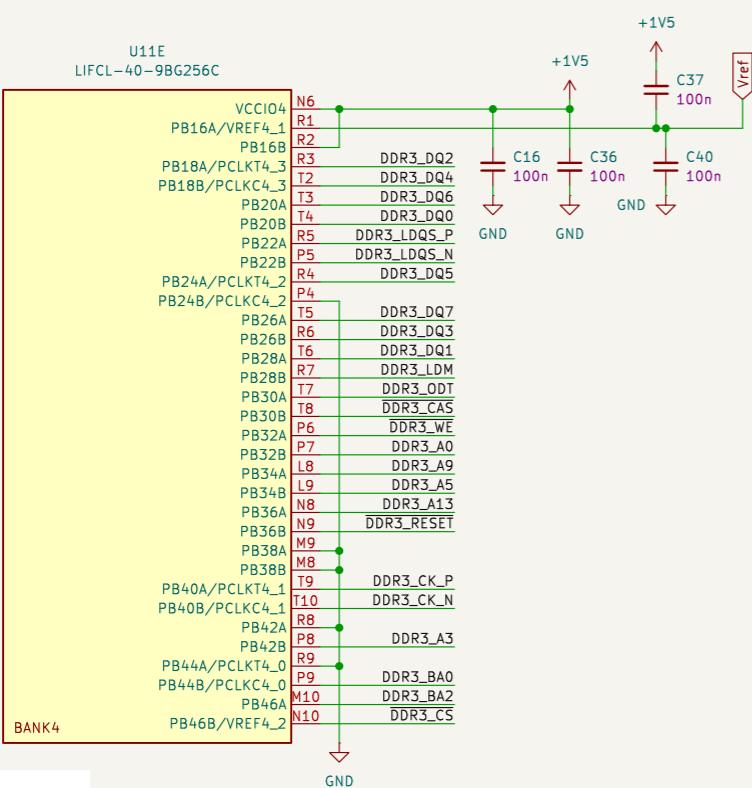
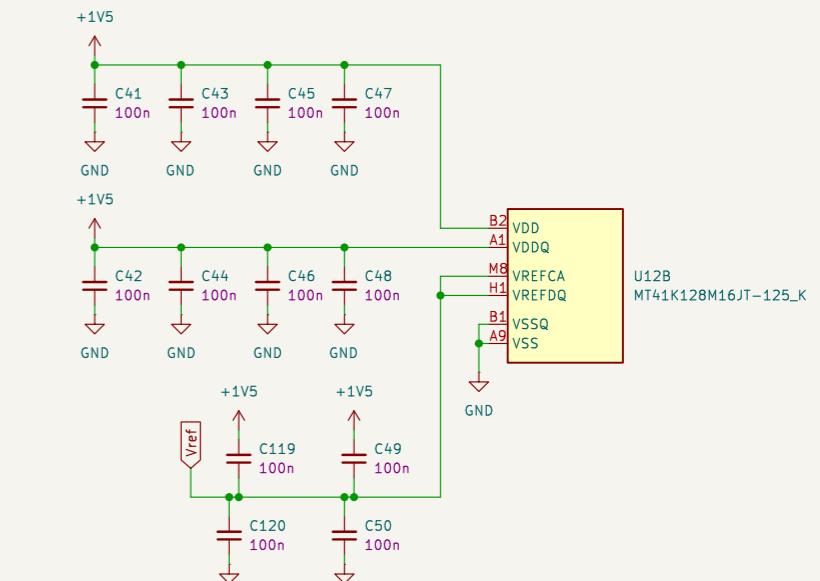
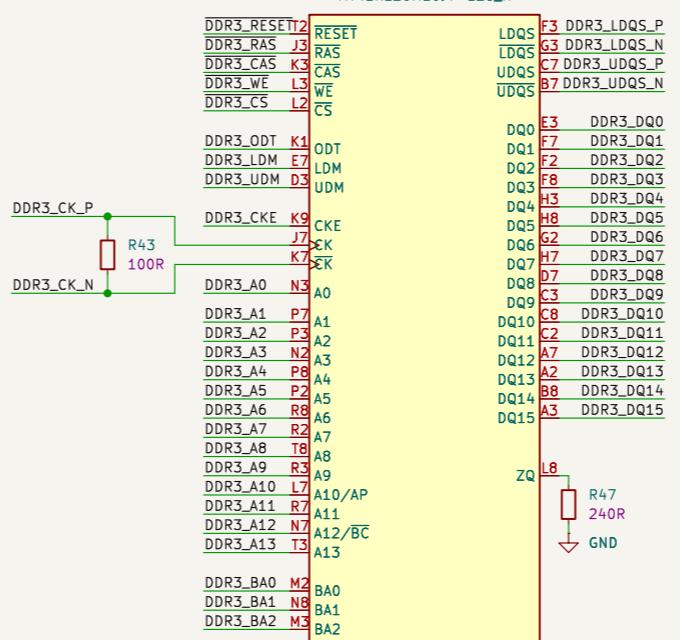


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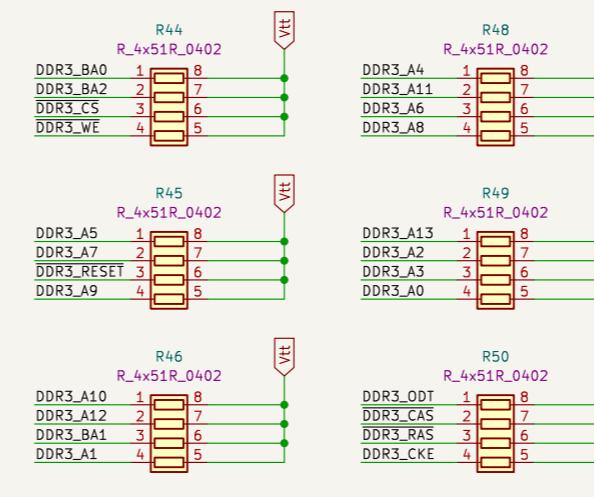
FPGA



RAM



TERMINATION



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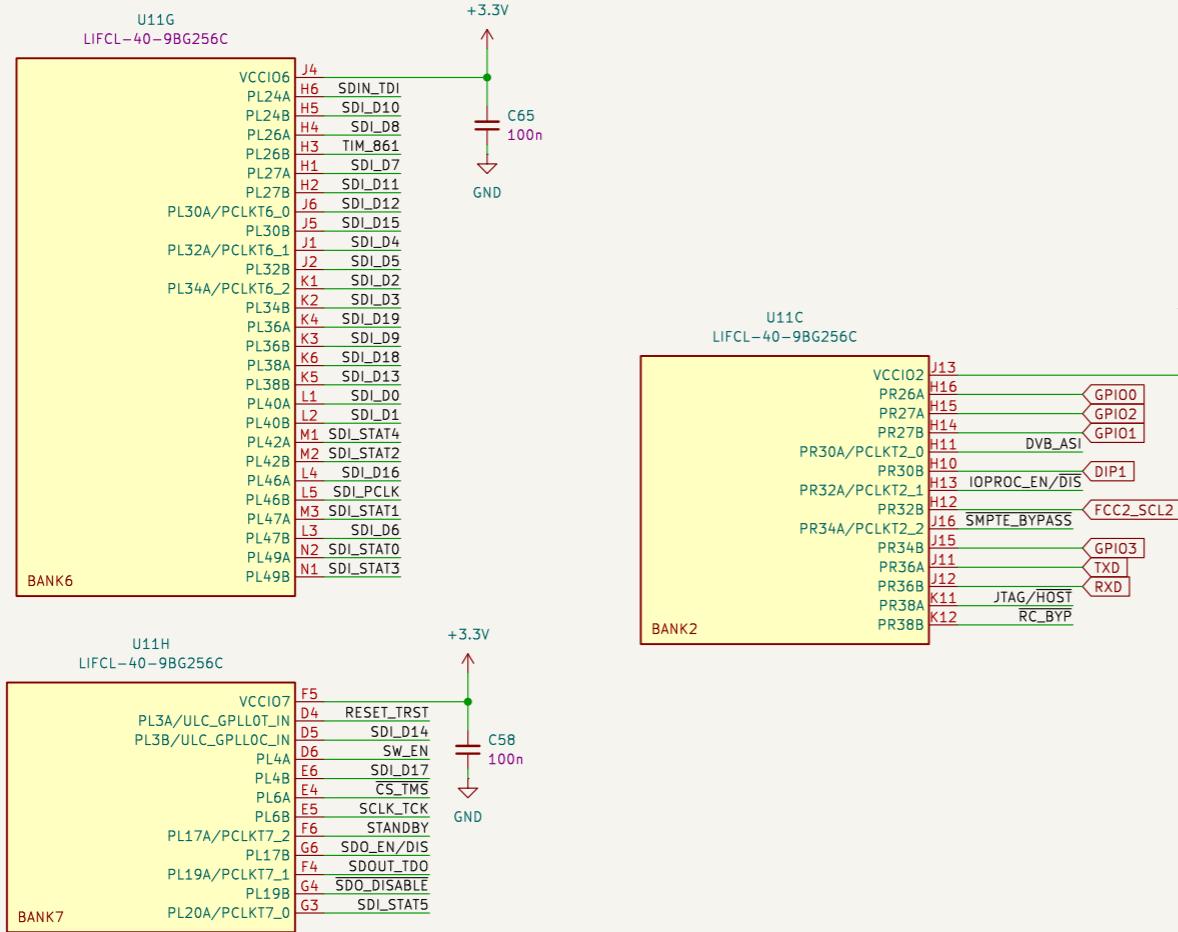
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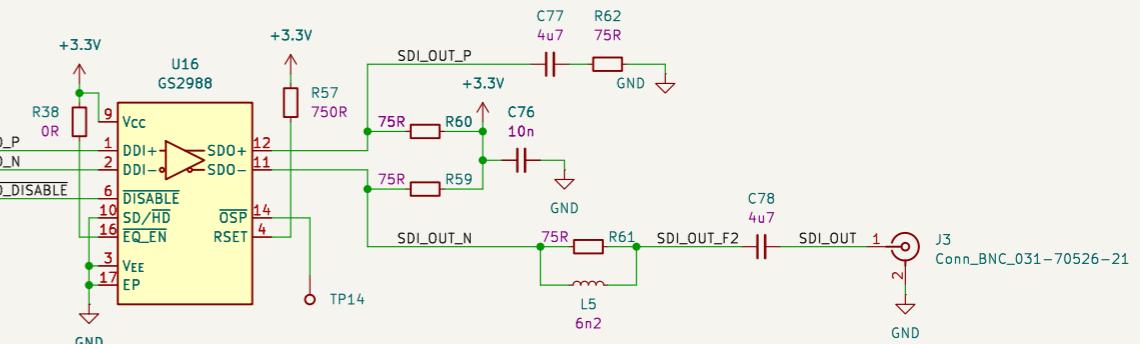
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Rev: 1.1.4:6e389
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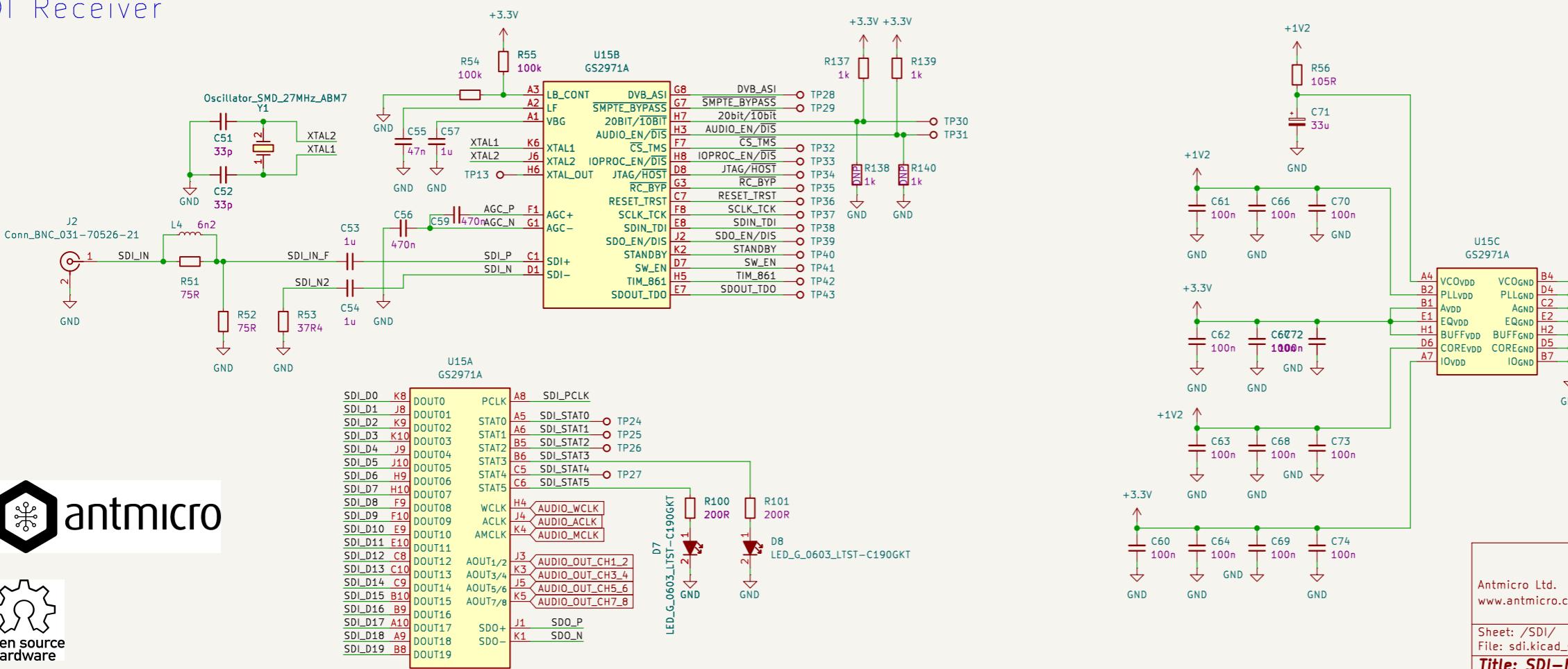
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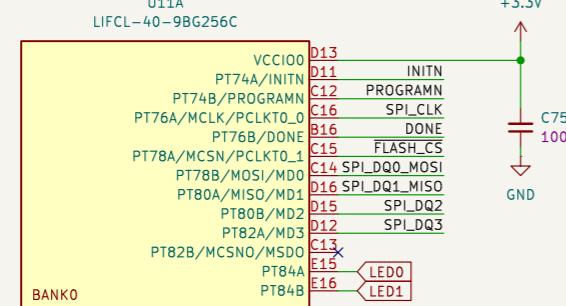
Cable driver



SDI Receiver

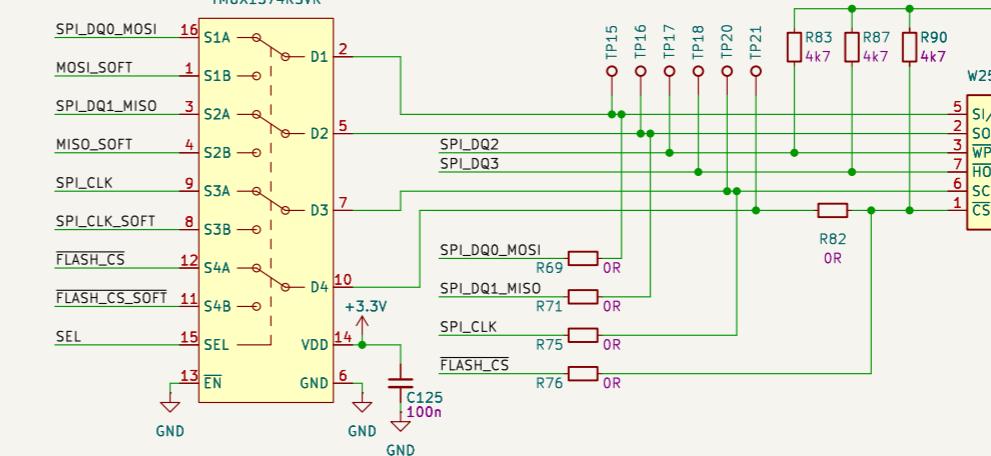


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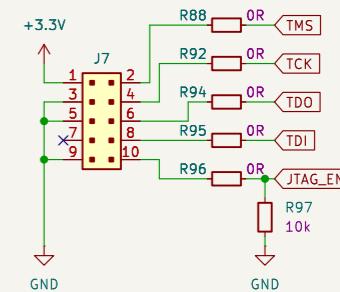
FPGA CONFIGURATION

CONFIGURATION FLASH

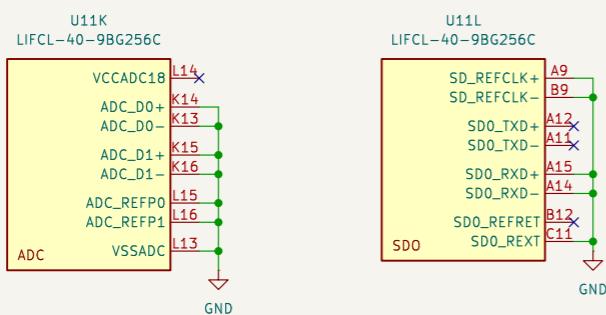


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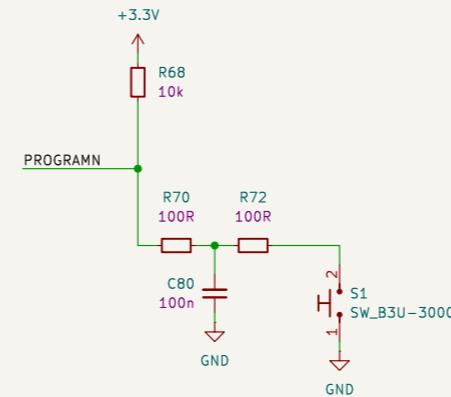
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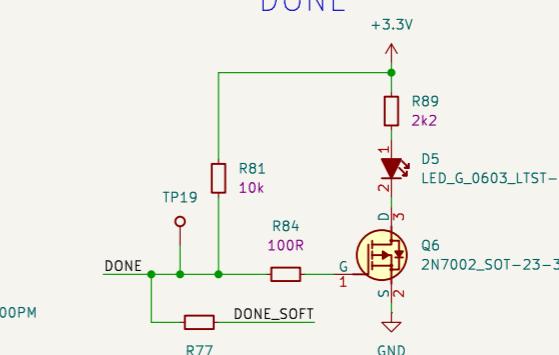
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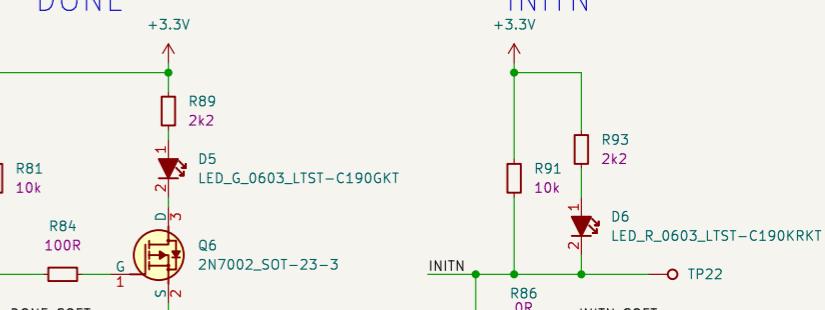
PROGRAMM



DONE

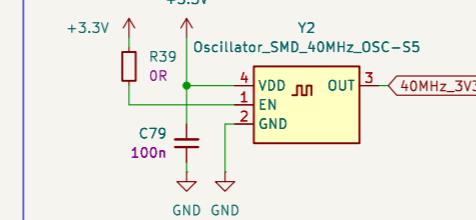


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OSCILLATOR

OPTIONAL, NORMALLY DNP



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