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 $\label{local-communication} \mbox{ Home (https://www.brainkart.com) } \mbox{ $|\ |$ Digital Communication (https://www.brainkart.com/subject/Digital-Communication_222/) $|\ Convolutional codes$}$ 

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Chapter: Digital Communication: Error Control Coding

### **Convolutional codes**

The operation of a convolutional encoder can be explained in several but equivalent ways such as, by a) state diagram representation. b) Tree diagram representation. c)

# Рішуча відповідь на спазм\*



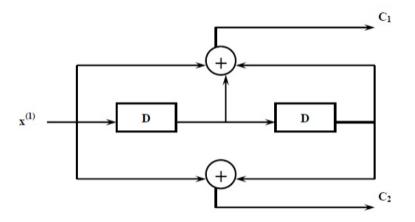
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practical communication systems for error correction. \*The encoded bits depend on the current k input bits and a few past input bits. \* The main decoding strategy for convolutional codes is based on the widely used Viterbi algorithm. \*Convolutional codes are commonly described using two parameters: the code rate and the constraint length. The code rate, k/n, is expressed as a ratio of the number of bits into the convolutional encoder (k) to the number of channel symbols output by the convolutional encoder (n) in a given encoder cycle. \*The constraint length parameter, K, denotes the "length" of the convolutional encoder, i.e. how many k-bit stages are available to feed the combinatorial logic that produces the output symbols. Closely related to K is the parameter m which can be thought of as the memory length of the encoder. A

addition (EXCLUSIVE-OR operation) of the input information bits and the contents of the shift registers which are a few previous information bits.



**Fig 3.1** . A convolutional encoder with k=1, n=2 and r=1/2

The operation of a convolutional encoder can be explained in several but equivalent ways such as, by

- a) state diagram representation.
- b) Tree diagram representation.
- c) trellis diagram representation.

# a) State Diagram Representation:

A convolutional encoder may be defined as a finite statemachine.

by input bits, is depicted in the state diagram. Fig. 3.2 shows the state diagram of the encoder in Fig. 3.1. A new input bit causes a transition from one state to another

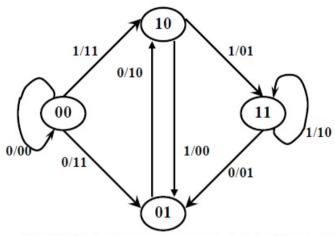
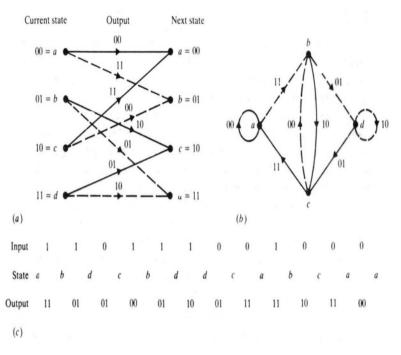


Fig 3.2 State diagram representation for the encoder

# b) Tree Diagram Representation:

The tree diagram representation shows all possible information and encoded sequences for the convolutional encoder. Fig. 3.3 shows the tree diagram for the encoder in Fig. 3.1. The encoded bits are labeled on the branches of the tree. Given an nput sequence, the encoded sequence can be directly read from the tree.

#### **STATE DIAGRAM:**



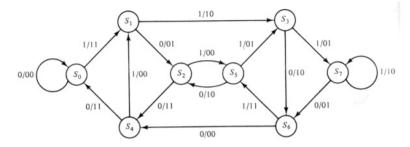
Inspecting state diagram: Structural properties of convolutional codes:

·Each new block of k input bits causes a transition into new state.

·Hence there are 2k branches leaving each state.

· Assuming encoder zero initial state, encoded word for any input

of L bits one thus he obtained For instance below for well 1 1 0



- Encoder state diagram for (n,k,L)=(2,1,2) code
- Note that the number of states is 2L+1=8.

### **Distance for some convolutional codes:**

n	k	R,	L	$d_f$	$R_c d_f/2$
4	1	1/4	3	13	1.63
3	1	1/3	3	10	1.68
2	1	1/2	3	6	1.50
			6	10	2.50
			9	12	3.00
3	2	2/3	3	7	2.33
4	3	3/4	3	8	3.00

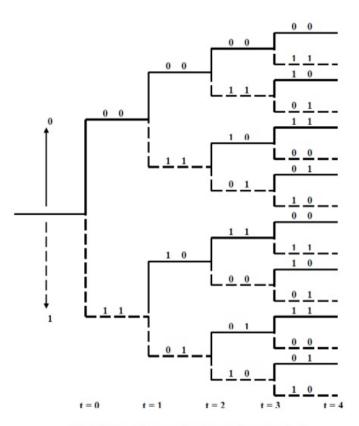


Fig.3.3 A tree diagram for the encoder in Fig. 3.1

## c) Trellis Diagram Representation:

The trellis diagram of a convolutional code is obtainedfrom its state diagram. All state transitions at each time step are explicitly shown in the diagram to retain the time dimension, as is present in the corresponding tree diagram. Usually, supporting descriptions on state transitions, corresponding input and output bits etc. are

decoder, especially when the famous "Viterbi Algorithm (VA)" is followed. Fig. 3.4 shows the trellis diagram for the encoder in Figure 3.1.

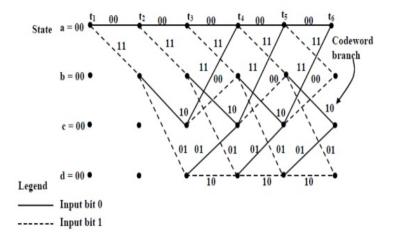


Fig.3.4. Trellis diagram for the encoder in Fig. 3.1

#### **Hamming Code Example:**

$$\mathbf{G} := egin{pmatrix} 1 & 0 & 0 & 0 \ 0 & 1 & 0 & 0 \ 0 & 0 & 1 & 0 \ 0 & 0 & 0 & 1 \ 0 & 1 & 1 & 1 \ 1 & 0 & 1 & 1 \ 1 & 1 & 0 & 1 \end{pmatrix}$$

H(7,4)

Generator matrix G: first 4-by-4 identical matrix

- ·Message information vector p
- ·Transmission vector x
- ·Received vector r and error vector e
- · Parity check matrix H

$$\mathbf{Gp} = \begin{pmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 1 & 1 \\ 1 & 0 & 1 & 1 \\ 1 & 1 & 0 & 1 \end{pmatrix} \begin{pmatrix} 1 \\ 0 \\ 1 \\ 1 \end{pmatrix} = \begin{pmatrix} 1 \\ 0 \\ 1 \\ 1 \\ 0 \end{pmatrix} = \mathbf{x}$$

$$\mathbf{H} := \begin{pmatrix} 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \end{pmatrix}$$

#### **Error Correction:**

If there is no error, syndrome vector z=zeros

$$\mathbf{Hr} = \mathbf{H}(\mathbf{x} + \mathbf{e}_i) = \mathbf{Hx} + \mathbf{He}_i = \mathbf{0} + \mathbf{He}_i = \mathbf{He}_i$$

If there is one error at location 2

$$\mathbf{Hr} = \begin{pmatrix} 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 1 \\ 0 \\ 1 \\ 1 \\ 0 \\ 1 \\ 0 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \\ 0 \\ 0 \end{pmatrix} = \mathbf{z}$$

$$\mathbf{r} = \mathbf{x} + \mathbf{e}_2 = \begin{pmatrix} 1 \\ 0 \\ 1 \\ 1 \\ 0 \\ 1 \\ 0 \end{pmatrix} + \begin{pmatrix} 0 \\ 1 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{pmatrix} = \begin{pmatrix} 1 \\ 1 \\ 1 \\ 1 \\ 0 \\ 1 \\ 0 \end{pmatrix}$$

$$\mathbf{Hr} = \begin{pmatrix} 0 & 1 & 1 & 1 & 1 & 0 & 0 \\ 1 & 0 & 1 & 1 & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} 1 \\ 1 \\ 1 \\ 1 \\ 0 \\ 1 \\ 0 \end{pmatrix} = \begin{pmatrix} 1 \\ 0 \\ 1 \\ 0 \end{pmatrix} = \mathbf{z}$$

- 1. Consider a single error correction (7,4) linear code and the corresponding decoding table.
- 2. Find the (7,4) linear systematic block code word corresponding to 1101. Assume a suitable generator matrix.

Let

1 0 0 0 : 1 0 1

code vector G=[Ik: P]

$$P_{4\times 3} = \begin{bmatrix} 1 & 0 & 1 \\ 1 & 1 & 1 \\ 1 & 1 & 0 \\ 0 & 1 & 1 \end{bmatrix}$$

Check matrix C=MP

C1 = m1 + m2 + m3

C2 = m2 + m3 + m4

C3 = m1 + m2 + m4

C = [010]

Complete code word can be calculated  $X=\{M:C\}=\{1\ 1\ 0\ 0\ 1\ 0\}$ 

The parity matrix H=[pT : I] = [I : pT] =

$$\begin{bmatrix} 1 & 1 & 1 & 0 & : & 1 & 0 & 0 \\ 0 & 1 & 1 & 1 & : & 0 & 1 & 0 \\ 1 & 1 & 0 & 1 & : & 0 & 0 & 1 \end{bmatrix}$$

Minimum weight W(X)=3

3. Determine the generator polynomial g(X) FOR A (7,4) cyclic code and fine the code vector for the following data vector 1010, 1111 and 1000

n=7 k=4

To obtain the generator polynomial (p7+1) = (p+1)(p3+p2+1)(p3+p+1) Let G(p) = (p3+p+1)

To obtain the generator matrix in systematic form

$$G = \begin{bmatrix} 1 & 0 & 0 & 0 & : & 1 & 0 & 1 \\ 0 & 1 & 0 & 0 & : & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & : & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & : & 0 & 1 & 1 \end{bmatrix}$$

To determine the code vector

1. code vector for M=1010

X=MG

$$X = \begin{bmatrix} 1 & 0 & 1 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 0 & : & 1 & 0 & 1 \\ 0 & 1 & 0 & 0 & : & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & : & 1 & 1 & 1 \\ 0 & 0 & 0 & 1 & : & 0 & 1 & 1 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 1 & 0 & 0 & 1 & 1 \end{bmatrix}$$

2. code vector for M=1111

3. code vector for M=1000

$$X = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 & 0 & : & 1 & 0 & 1 \\ 0 & 1 & 0 & 0 & : & 1 & 1 & 1 \\ 0 & 0 & 1 & 0 & : & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & : & 0 & 1 & 1 \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 & 0 & 1 & 0 & 1 \end{bmatrix}$$

4. Assume a (2,1) convolutional coder with constraint length 6.Draw the tree diagram, state diagram and trellis diagram for the assumed coder

Design block code for a message block of size eight that can correct for single errors Briefly discuss on various error control codes and explain in detail with one example for convolution code.

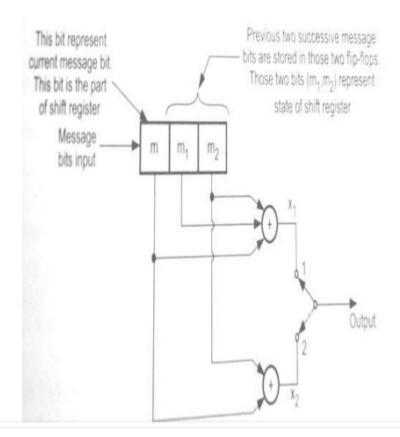
N=2, K=1 AND K=6 (CONSTRAIN LENGHT)

M=K/n=6/2=3, since constrain length k=n\*M

2 storage alement in shift register

One set k=1 of shift register having 3 storage element the convolutional code structure is easy to draw from its parameters. First draw m boxes representing the m memory register. Then draw n modulo-2 adders to represent the n output bits. Now connect the memory registers to the adders using the generator polynomial

$$g^{(i)}(D) = g_0^{(i)} + g_1^{(i)}D + g_2^{(i)}D^2 + \cdots + g_M^{(i)}D^M$$



*k*=1 is usually used!!

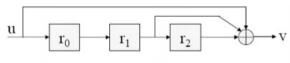
n = number of encoder output bits corresponding to the k0020 information bits

$$r = k/n = \text{code rate}$$

K =constraint length, encoder memory

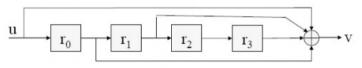
Each encoded bit is a function of the present input bits and their past ones.

#### **Generator Sequence**



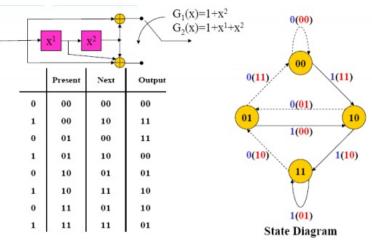
$$g_0^{(1)} = 1$$
,  $g_1^{(1)} = 0$ ,  $g_2^{(1)} = 1$ , and  $g_3^{(1)} = 1$ .

Generator Sequence: g<sup>(1)</sup>=(1 0 1 1)

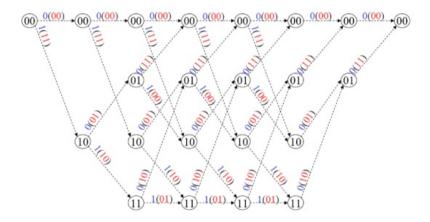


$$g_0^{(2)} = 1$$
,  $g_1^{(2)} = 1$ ,  $g_2^{(2)} = 1$ ,  $g_3^{(2)} = 0$ , and  $g_4^{(2)} = 1$ 

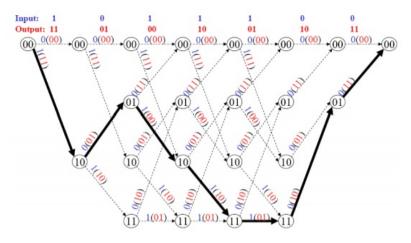
Generator Sequence:  $g^{(2)}=(1\ 1\ 1\ 0\ 1)$ 



Trellis Diagram Representation



**Encoding Process** 



Viterbi Decoding Algorithm Maximum Likelihood (ML) decoding rule

received sequence r detected sequence d  $\min(d,r)$  !!

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