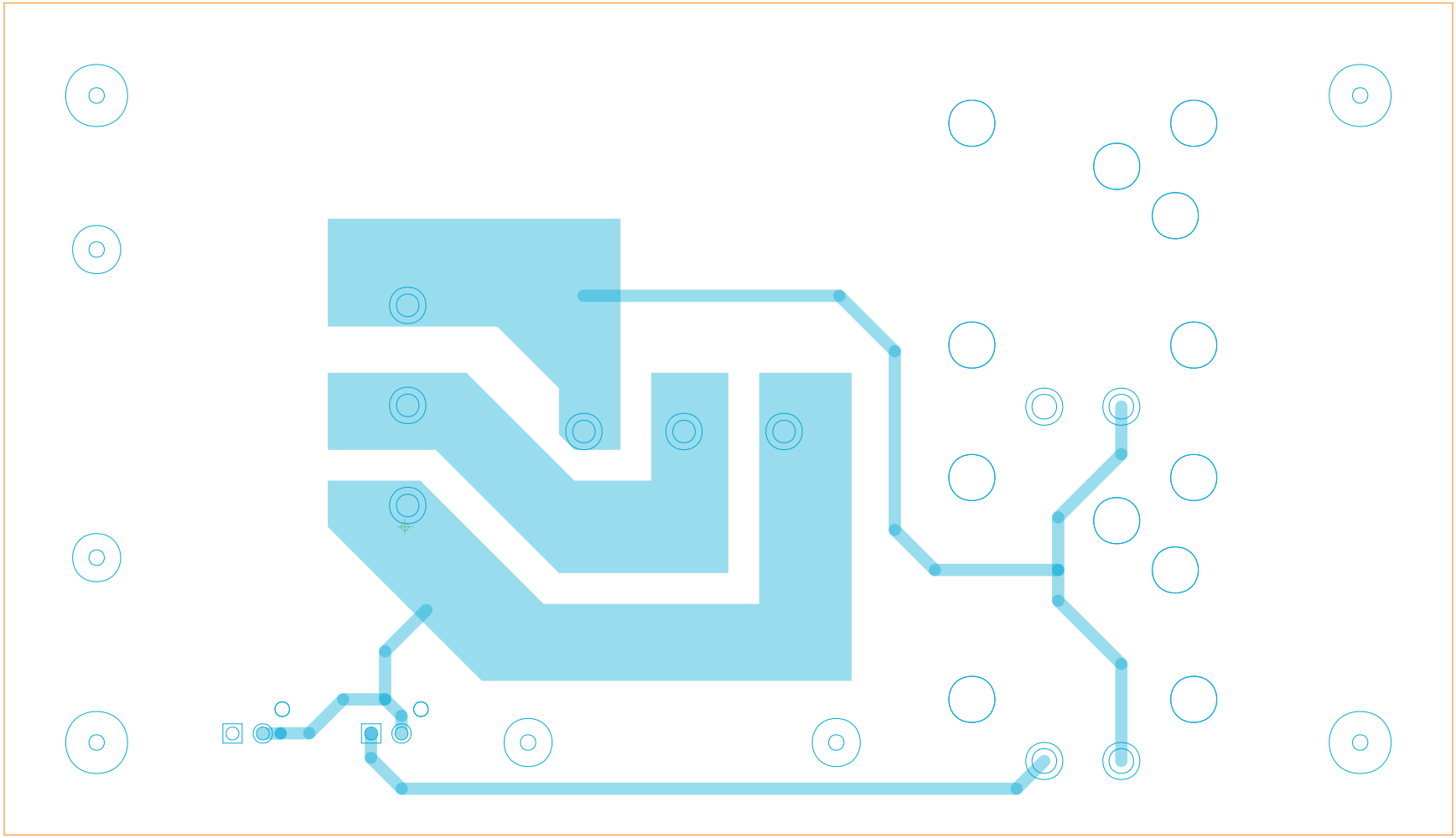
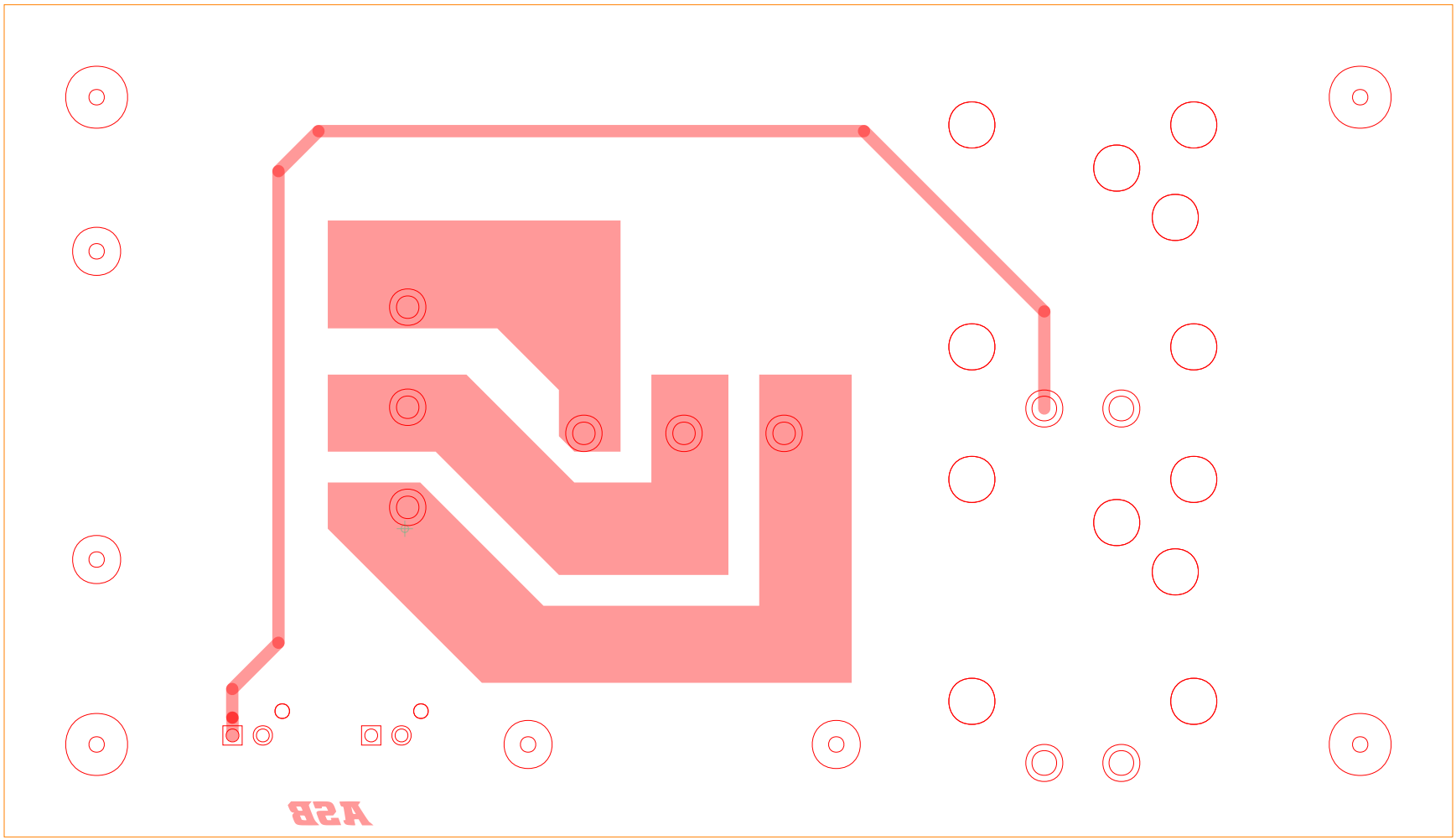
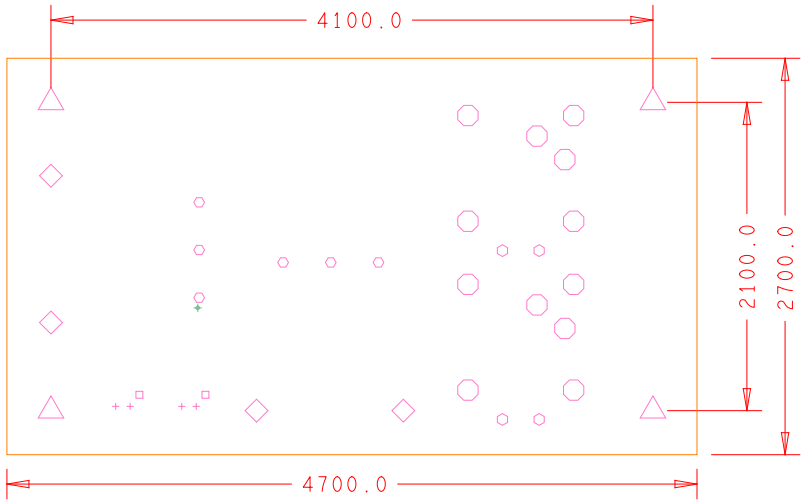


ART FILM - TOP



ART FILM - BOTTOM



FABRICATION NOTES (UNLESS OTHERWISE SPECIFIED):

1. TOP SIDE SHOWN.
2. BUILD IN ACCORDANCE WITH ALL IPC STANDARDS RELEVANT TO PRINTED CIRCUIT BOARD FABRICATION.
3. DIELECTRIC MATERIAL SHALL MEET THE REQUIREMENTS TYPE 370HR PER STACKUP DETAIL OR EQUIVALENT AND SATISFY ANY IMPEDANCE REQUIREMENTS.
4. DRILL HOLE, SLOT, AND CUTOUT DIMENSIONS ARE SHOWN AFTER PLATING.
5. SILKSCREEN BOTH SIDES USING EPOXY OR ACRYLIC BASED WHITE NON-CONDUCTIVE SILKSCREEN. SILKSCREEN TO BE REMOVED FROM ANY CONDUCTIVE SURFACE.
6. ETCH VENDOR ID, UL DESIGNATION, AND DATECODE ON THE BACKSIDE.
7. SOLDER MASK BOTH SIDES GREEN. NO SOLDERMASK ON COMPONENT PINS.
8. IMPEDANCE REQUIREMENTS ARE AS FOLLOWS, +/- 10%
   IF PRESENT, ALL OUTER 6.56 MIL WIDE TRACES 50 OHM SINGLE ENDED.
   IF PRESENT, ALL INNER 5.58 MIL WIDE TRACES 50 OHM SINGLE ENDED.
   TRACE WIDTHS SHALL NOT BE MODIFIED WITHOUT APPROVAL.
9. NO CHANGES WILL BE ALLOWED TO DESIGN WITHOUT SPECIFIC APPROVAL.
10. ALL MATERIALS ARE TO BE ROHS AND WEEE COMPLIANT.
11. LAYER STACKUP INFORMATION INCLUDED IN THIS DRAWING SHOWS OUTER LAYER THICKNESS AFTER PLATING AND SUPERSEDES ANY OTHER DESIGN DOCUMENTATION.
12. ELECTRICALLY DISCONNECTED COPPER THIEVING IS ACCEPTABLE TO HELP MAINTAIN COPPER PLATING DISTRIBUTION IN LOW PRESSURE AREAS TO AVOID DELAMINATION.
13. <DO NOT> PROVIDE BOARDS IN ARRAY FORMAT. ARRAYS SHOULD HAVE MINIMUM OF 250 MILS ON ALL SIDES.

STACKUP TABLE				
Unit = Mils				
#	NAME	TYPE	MATERIAL	THICKNESS
		SURFACE	AIR	0
1	TOP	CONDUCTOR	COPPER	1.7
		DIELECTRIC	FR-4	58
2	BOTTOM	CONDUCTOR	COPPER	1.7
		SURFACE	AIR	0
			TOTAL THICKNESS	61.4

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	FINISHED_SIZE	TOLERANCE_DRILL	PLATED	QTY
+	41.7	+4.0/-4.0	PLATED	4
□	47.2	+4.0/-4.0	PLATED	2
○	72.8	+5.0/-5.0	PLATED	6
⬡	79.9	+5.1/-5.1	PLATED	4
⬡	149.6	+8.0/-8.0	PLATED	12
◇	156.0	+8.0/-8.0	NON-PLATED	4
△	201.0	+8.0/-8.0	NON-PLATED	4

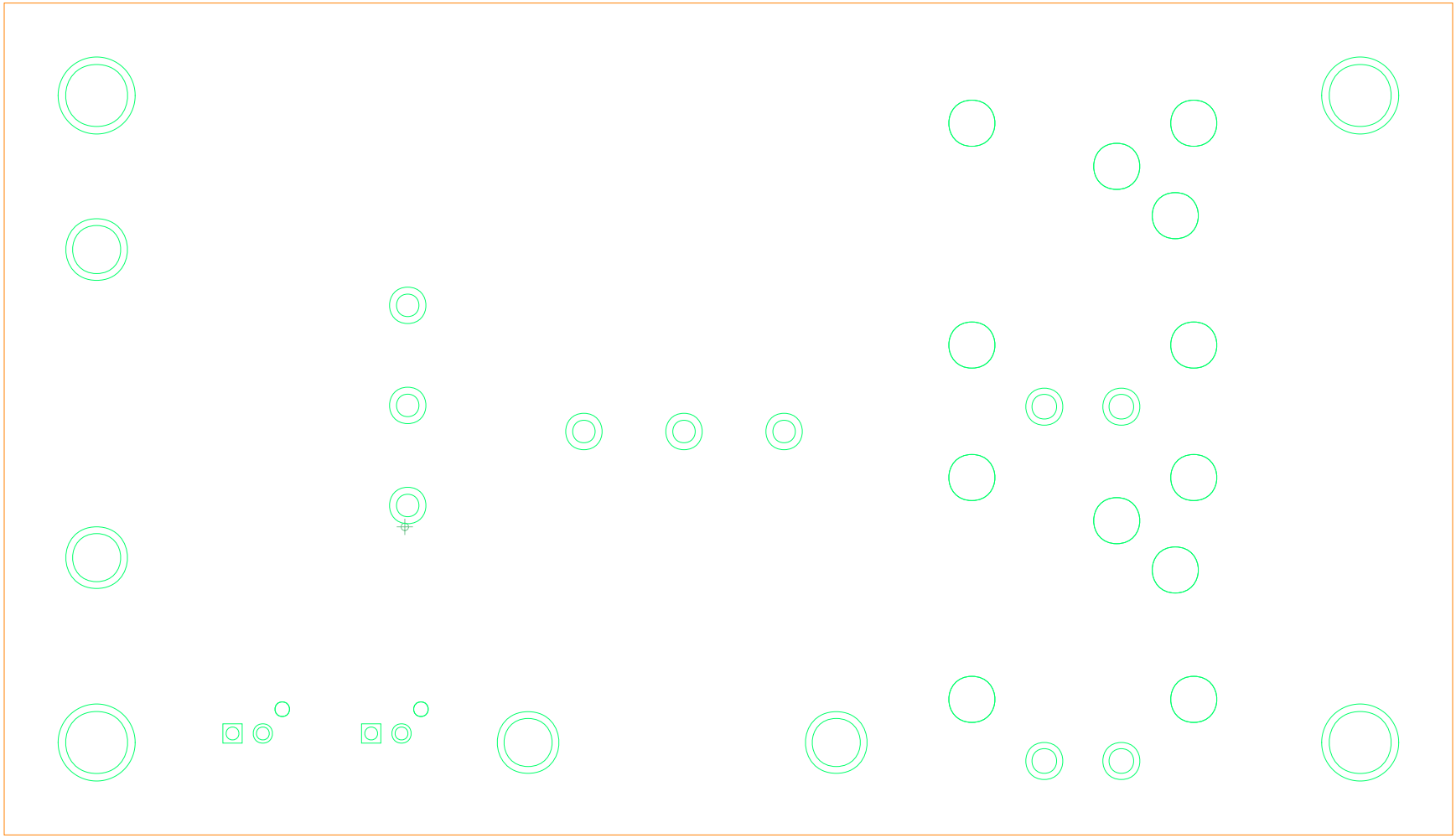
UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN MILS	DRAWN: ARIC BEAVER	06/30/20
TOLERANCES ON:	CHECKED: ARIC BEAVER	06/30/20
	ENGRG: ARIC BEAVER	06/30/20
	ISSUED: ARIC BEAVER	06/30/20

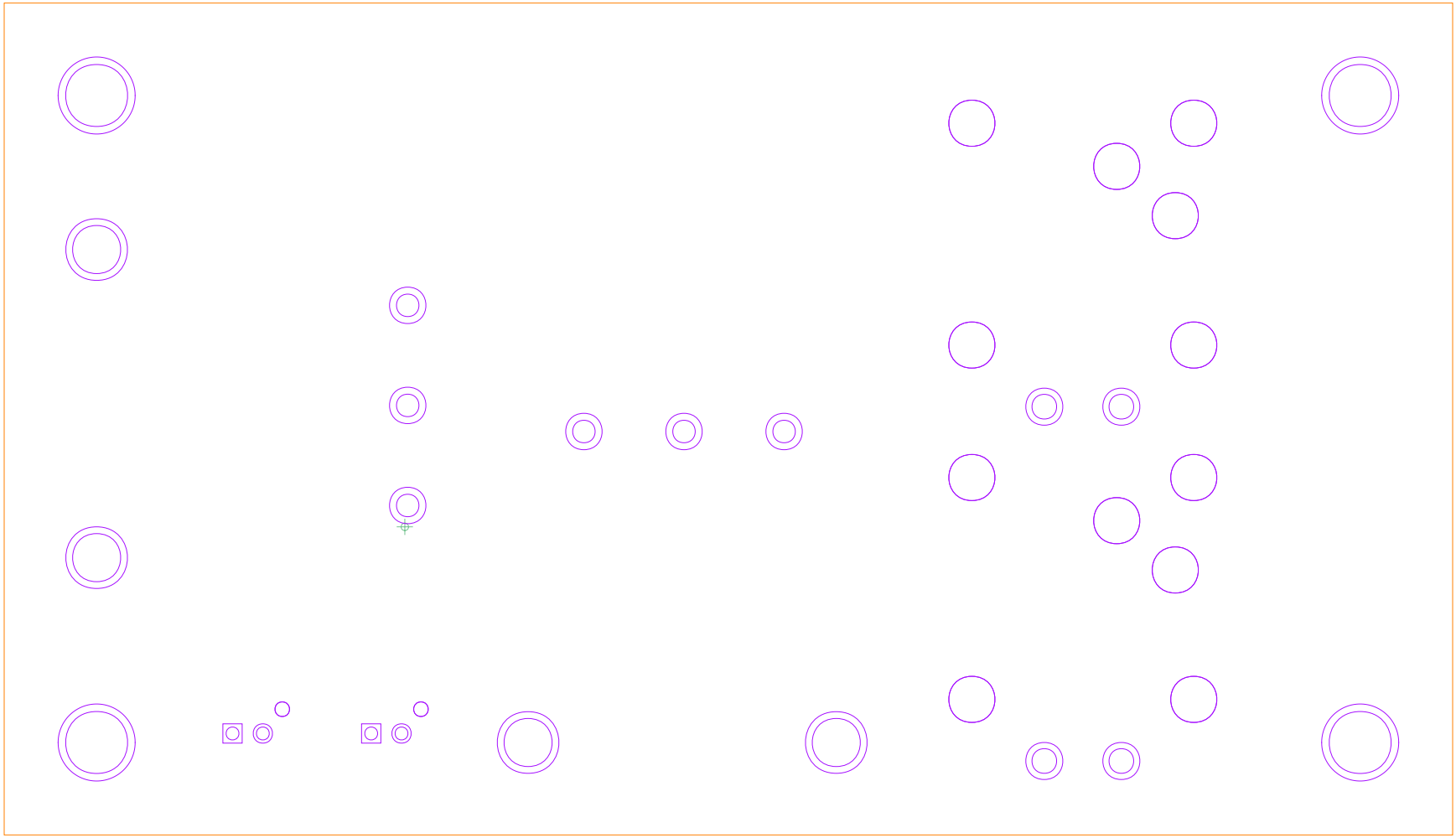
ALWAYS HARDCORE DESIGN

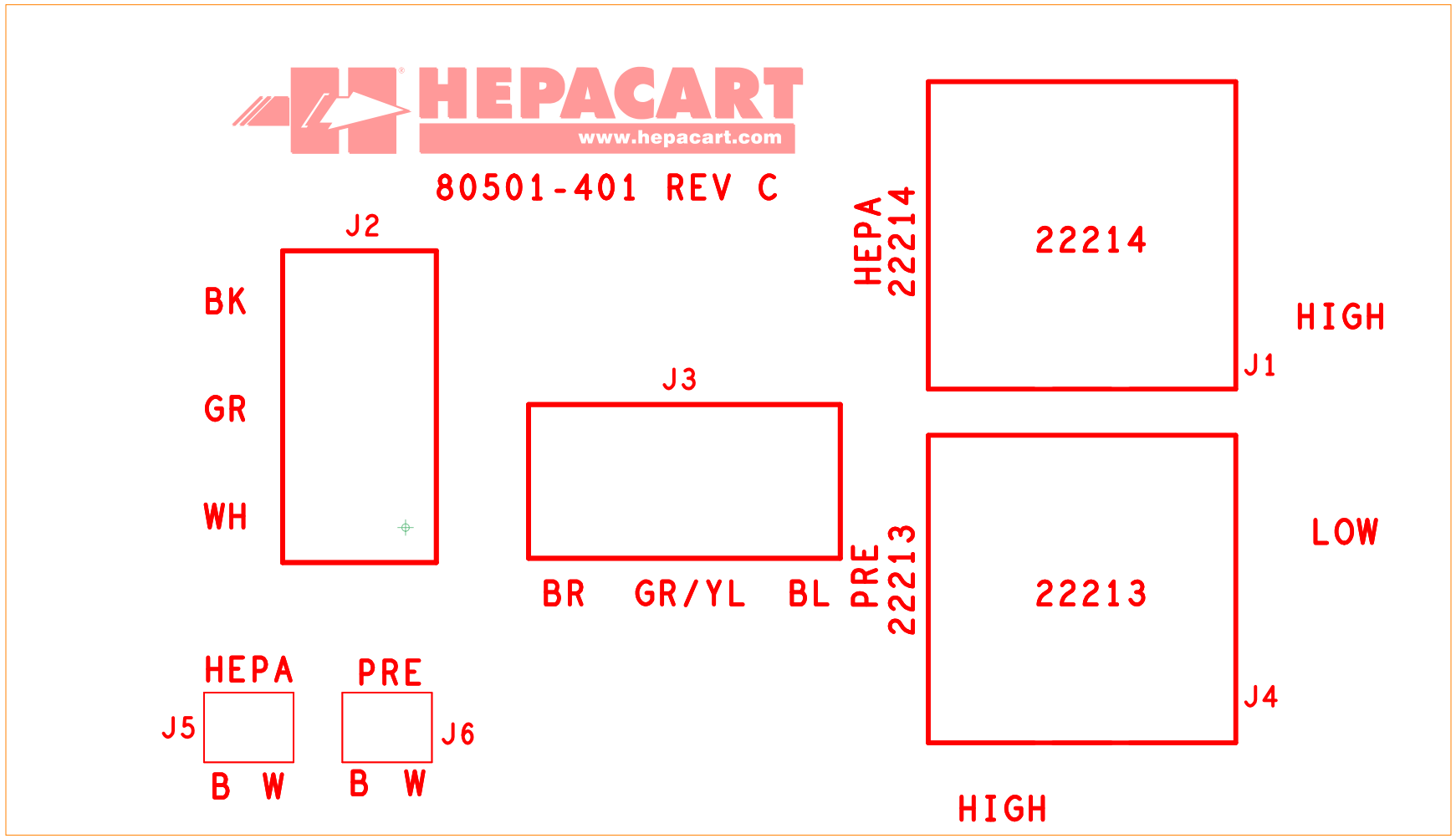
DP SWITCH BARRIER BLOCK INTERFACE

SIZE	REVISION	DWG NO
A	REV C	AHC-20-001

SCALE	DRAWING	SHEET
1/1	FABRICATION	1 OF 3






HEPACART
www.hepacart.com
80501-401 REV C
J2
BK
GR
WH
J3
BR
GR/YL
BL
HEPA
22214
22214
HIGH
J1
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22213
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J5
J6
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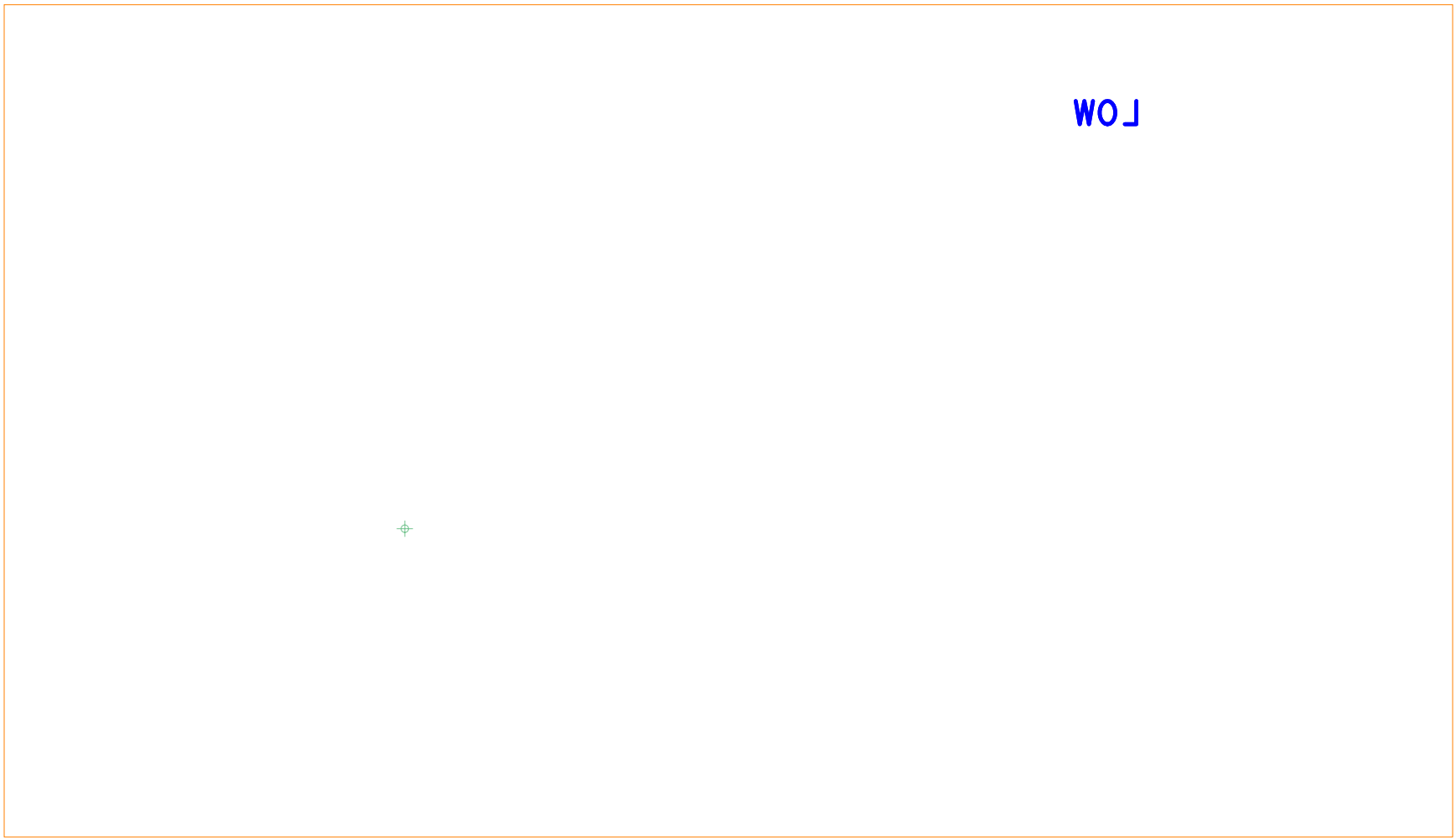
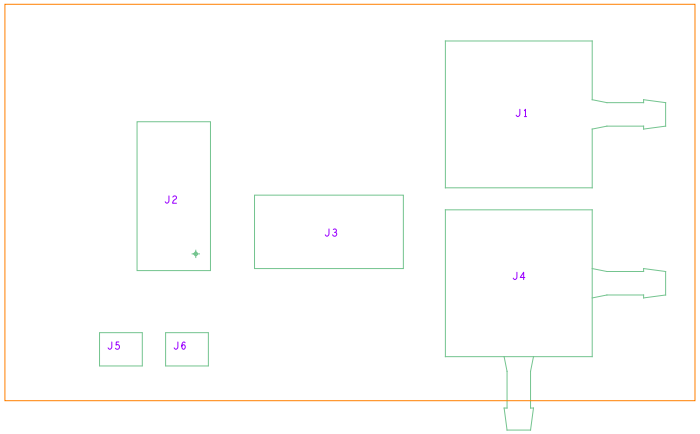

LOW



ASSEMBLY TOP NOTES (UNLESS OTHERWISE SPECIFIED):

1. ASSEMBLE AND INSPECT IN ACCORDANCE WITH ALL IPC STANDARDS RELEVANT TO PRINTED CIRCUIT BOARD ASSEMBLY.
2. ALL MATERIALS ARE TO BE ROHS AND WEEE COMPLIANT.
3. ~~IF BARE BOARDS ARE SUPPLIED THEY WILL BE AN <ARRAY OR INDIVIDUAL PEICES>.~~
4. ONLY PARTS CONTAINED IN BILL OF MATERIALS SHALL USED FOR TURN-KEY ASSEMBLY UNLESS EQUIVALENT PART IS APPROVE.
5. REMOVE ANY KAPTON TAPE COVERING COMPONENTS.
6. "DNI" REFERENCE IN BILL OF MATERIALS MEANS DO NOT INSTALL ON PCB.
7. RETURN ANY UNASSEMBLED OR EXTRA COMPONENTS WITH ASSEMBLED PCBS.
8. REFLOW HEAT PROFILE ARE REQUIRED TO PROPERLY ATTACH COMPONENTS.
9. HIDDEN PINS ON BGA OR CSP PACKAGES ARE REQUIRED TO BE X-RAY'ED FOR PROPER SOLDER ATTACH.
10. REMOVE ARRAY MOUSE BITES ON EDGE OF FINISHED PCB WITH PLUNGE ROUTER.
11. SOLDERMASK BARREL RELIEF TO AVOID ASSEMBLY ISSUES ON LARGE SURFACE MOUNT PADS IS ACCEPTABLE.
13. ASSEMBLY VARIATIONS EXIST IN THIS DESIGN AND A BILL OF MATERIALS FOR EACH VARIANT IS PROVIDED.

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN MILS	DRAWN: ARIC BEAVER	06/30/20
TOLERANCES ON:	CHECKED: ARIC BEAVER	06/30/20
	ENGRG: ARIC BEAVER	06/30/20
	ISSUED: ARIC BEAVER	06/30/20

ALWAYS HARDCORE DESIGN

DP SWITCH BARRIER BLOCK INTERFACE

SIZE	REVISION	DWG NO
A	REV C	AHC-20-001

SCALE	DRAWING	SHEET
1/1	ASSEMBLY TOP	2 OF 3

ASSEMBLY BOTTOM NOTES (UNLESS OTHERWISE SPECIFIED):
1. USE ASSEMBLY TOP NOTES.

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN MILS	DRAWN: ARIC BEAVER	06/30/20
TOLERANCES ON:	CHECKED: ARIC BEAVER	06/30/20
	ENGRG: ARIC BEAVER	06/30/20
	ISSUED: ARIC BEAVER	06/30/20

ALWAYS HARDCORE DESIGN

DP SWITCH BARRIER BLOCK INTERFACE

SIZE	REVISION	DWG NO
A	REV C	AHC-20-001

SCALE	DRAWING	SHEET
1\1	ASSEMBLY BOTTOM	3 OF 3