	直接作答.若空間不夠,請在背面作答.(注意:背面作答要標明題號)
	1.(10%)Please write down the full names of the following abbreviations. Please d
	not make any explanations.
	(1) FCFS: first-come - first-service
	(2) FIFO: First - in - First-out
	(3) RR (in scheduling): Round Robin
9	(4) NUMA (for memory access): Non-Uniform memory access
5	(5) CLI (for interface): Command line
	(6) GUI (for interface): Graphics users interface
	(7) API (for interface): Application program interface
	(8) RPC: Remote Provedure call
	(9) PCB: Process control block
	(10) IPC: Interprocess communication
	2. (30%)Please explain the following terminology briefly.
	(1)Kernel:
	DS的主架構,一種不斷執行的 program,
	(2)firmware
	在按 ROM, EPROM 的 軟骨豐 (bootstrap program
	The Choistrap program
	(3)system call
ذ)	· 用柱 user mode AS 带至 (rommand)
/	(4)trap
	軟體中斷(欧路以豐)
	(6)matchmaker (in RPC)
	查表
	(6) heap:
	在執行期間用來做動態記憶體的分配
	(7)socket:
	畫通 (communication) by 鎮點 (end point)
	POSIX thread, 用性 user mode, 在 kernel mode
	常相在 (MIX OS D

(9)Load balancing:

(10)mode bit:

(4%)Consider (i)FCFS, (ii)shortest job first, (iii)RR, (iv) priority, (v)multilevel feedback queue.

)Which of the above scheduling algorithm could result in starvation?

(2)( (1) )Which of the above scheduling algorithm is(are) better of interactive processes?

4. (4%)(1) What is a privileged instruction?

描有較高執行順位的程式

(2)(a \c) (multiple choices) Which of the following instructions should be privileged? (a)set value of timer; (b)clear memory; (c)turn off interrupts, (d)issue a trap instruction

5. (4%)(1)Explain what is a (process) context switch

打接PCB,當process被打掉時,其狀態(state)會被儲存 生後七寸美分的的(ess 後, 2) What state information do you need to save/restore about threads when 电起 performing a context switch? (Note: only the thread, Not the process.)

6. (4%)Including the initial parent process, how may processes are created by the program show below:

#include <stdio.h> #include <unistd.h> int main() { /\*for a child process \*/ fork: /\*fork another child process\*/ fork: /\*and fork another\*/ fork: return 0:

2x2x2 = STIFT processes

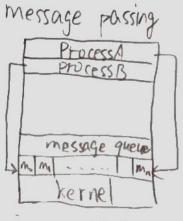
(8%)Describe what is a shared memory model and what is a message passing model.

A)

shared memory



華由將雙於的資訊 傳入 shared memory 中來達成 IPC



Process A 將資訊 傳人 mersage queue 丝後 Process B 连A

8. (4%)(1)Why so some operating systems store the operating system in firmware,

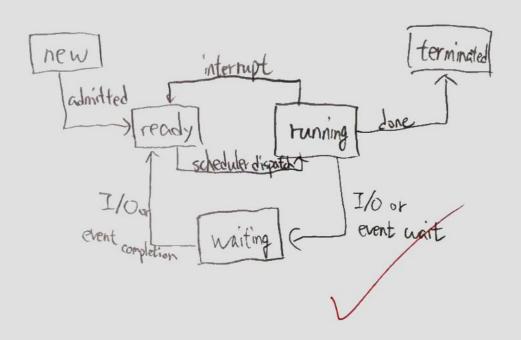
F9

While others store it on disk? 4开比范嵌入对系统中可能设有 disk, 所以藉由 firmware 车存效 OS

(2)How could a system be designed to allow a choice of operating systems from which to boot? What would the bootstrap program need to do?

可以藉由 啟動管理 (boot management) 末選擇 要進入的 OS, 通常啟動管理 管被储存於 Jisk中, 以便 bootstrap program去找尋及使用 9. (8%)Define what is a policy and what is a mechanism and show an example for each of them. policy:做什麽 mechanism: 意度估文 以排程旨例 policy:排程的方式(RR,SJF, FCFS) me chanism FCFS AS 演算法

10. (12%)(1)Draw a process state (transition) diagram. The states are new, ready(runnable), running, terminated, and waiting(blocked). Note that you should identify the events that trigger each state transition.

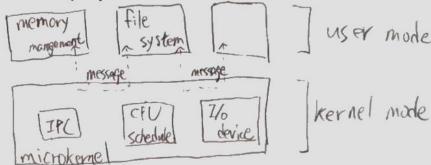


- (2) For each of the following process state transitions, say whether the transition is legal and how the transition occurs or why it cannot.
- (a) Change from the state BLOCKED to thread state RUNNING.

要回到 ready, 並由排程決定執行的 process illegal 才育至幸村行 (running (b) Change from the state RUNNING to state BLOCKED. legal 由Blocked 回到 ready 準備 執行 (running)

下個 process

11. (8%) Draw a block diagram showing the structure of a microkernel. Note that you must show that how the kernel invokes a user level process which would serve as a system service or as a policy.



12. (8%)(1)List two reasons why *overuse* of threads is bad (i.e., using too many threads for different tasks). Be explicit in your answers.



(2) List two reasons why threads are useful/important.



13. (8%)You're hired by COOL Computers Inc. to improve the performance of their system. They point out that their applications only use 10 of the CPU's 32 registers; so to improve the performance of the applications, they suggest that you change the OS's context switch routine so it only saves the 10 registers used by the applications. Assume that you can correctly change the context switch routine. Is this a good or bad idea? Why?



14. (20%)Assume the following processes are to be scheduled using a *preemptive*, *round-robin* scheduling algorithm. Each process is assigned a priority (value). A higher value indicates a higher priority. In addition, assume that the system has an idle process. The idle process, identified as P<sub>tdle</sub>, has priority value 0 and it is scheduled when there is no available process to run. The length of a time quantum is 8 units. If a process is preempted by a higher priority process, the preempted process is placed at the end of the queue.

thread priority		burst	arrival		
Pl	40	20	0		
P2	30	25	25		
P3	30	25	30		
P4	35 15		60		
P3 P4 P5 P6	5	10	100		
P6	10	10	105		

- (1)Show the scheduling order of the processes using a Gantt chart.
- (2) What is turnaround time for each process?
- (3) What is the waiting time for each process?
- (4) What is the CPU utilization rate?

(Hint: you can make your own assumptions if you think the problem here is not clearly described.)

51	151	TP1	idk	el P2	- P:	. P2	TP	3 B	74	P4	B	P3	P3	idle	idle	P5
1	8	16	20	25	33	4(	49	57	60	68	757	દ ક	4 89	5 93	B (i	00 105

(1

	tyraroundtime	waiting time
91	20	0
Pz	3\ <b>x</b>	24 X
Ps	35	35_
124	KX	OX
PS	ID X	10
74	10 Y	ON

