

Mid Term II

E927500
Circuit Analysis
May 27, 2015

A. (5×6=30 points) Answer each of the following statements.

- Comparison between transformer as "isolation device" and "matching device".
- Comparison between "120-V connection" and "240-V connection" in household system.
- Comparison between "phase current" and "line current" in three-phase system.
- Comparison between "single-phase" and "three-phase".
- Explain "To mitigate the inductive aspect of the load, a capacitor is added in parallel with the load. The power factor has improved."
- Explain "If same power loss is tolerated in single-phase and three-phase systems, the three-phase system should be more economic."

B. (10+5= 15 points) Turns ratio in Ideal Transformer

Derive (a) $\frac{V_2}{V_1} = \frac{N_2}{N_1} = n$ and (b) $\frac{I_2}{I_1} = \frac{N_1}{N_2} = \frac{1}{n}$ for the ideal transformer (as Fig. 1(a)) according to a linear transformer as shown in Fig. 1(b), where n is the turns ratio.

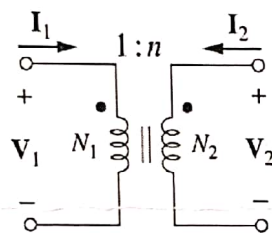


Fig. 1(a)

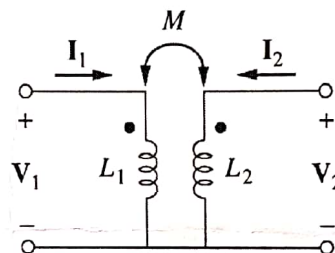


Fig. 1(b)

C. (5+5+10+5=25 points) Equivalent Circuit for Ideal Transformer & Impedance Matching + Maximum Power Transfer with Ideal Transformer

- Please derive and draw an equivalent circuit for Fig. 2(a) obtained by reflecting the secondary circuit to the primary side.

As the circuit of Fig. 2(b), an ideal transformer can be used to match the load, $Z_L = 200 \Omega$, to the amplifier to achieve maximum power transfer. The Thevenin equivalent of the amplifier is: $V_{Th} = 30$ V and $Z_{Th} = 3000 + j4000 \Omega$.

- Calculate the required turns ratio for maximum power transfer.
- Determine the load voltages and the power transfers with the ideal transformer based on $P = I^2 R$ and the equivalent circuit by reflecting the primary circuit to the secondary one.
- Calculate the complex power supplied by the source.

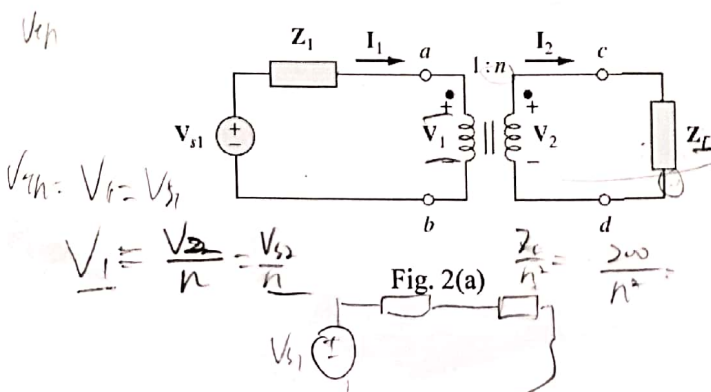


Fig. 2(a)

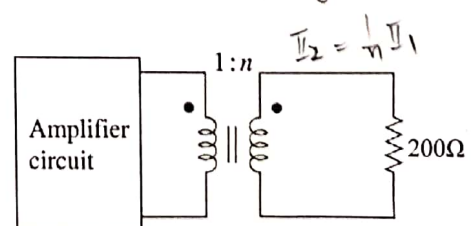


Fig. 2(b)

D. (8+12+5=25 points) Balanced Three-Phase System with Power Factor Correction

Figure 3 shows a three-phase circuit. The capacitors can be added to improve the power factor of the load. We need to determine the value of the capacitance, C , required to obtain a power factor of 0.9 lagging if three capacitors are connected as a Δ in parallel.

- Find the per-phase equivalent circuit which can be used to analyze this circuit.
- Derive the following formula which can provide for calculating the reactance, X_1 , needed to correct the power factor of a load. R and X are the real and imaginary parts of the load impedance before the power factor is corrected and pfc is the corrected power factor. After this equation is used to calculate X_1 , the capacitance, C , can be calculated from X_1 .

$$X_1 = \frac{R^2 + X^2}{R \tan(\cos^{-1} pfc) - X}$$

- Calculate the capacitance of each capacitor according the formula in (b).

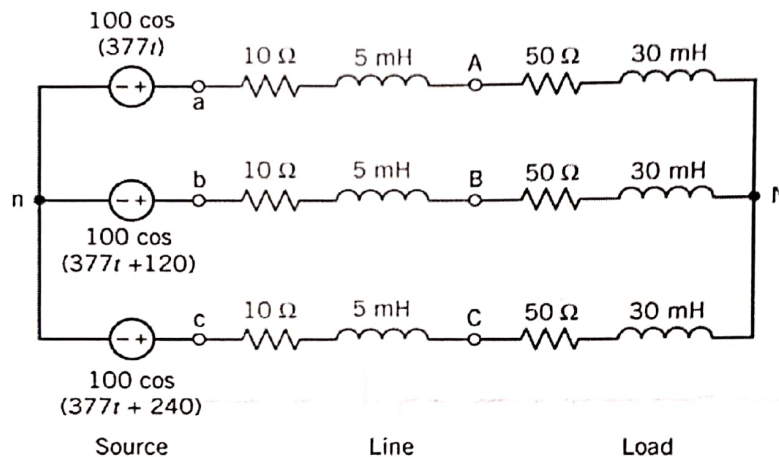


Fig. 3

E. (10+5=15 points) PSpice

- Fig. 4(a) is an electric circuit of a transformer, and Fig. 4(b) is a PSpice simulation figure. Determine whether the design and the parameter settings in Fig. 4(b) are correct. If not, please explain the reason for these mistakes and plot the right PSpice simulation figure. If we want to use AC sweep to analysis the electric circuit, how should we set the parameters of the start frequency?

- If we use two individual inductances to design a transformer, please plot the PSpice simulation figure and explain how to design the parameters in detail.

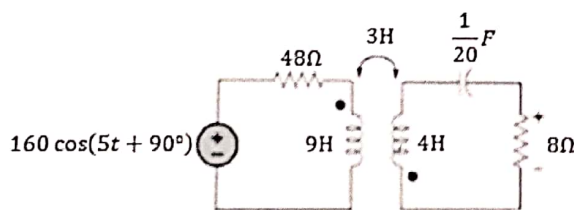


Fig. 4(a)

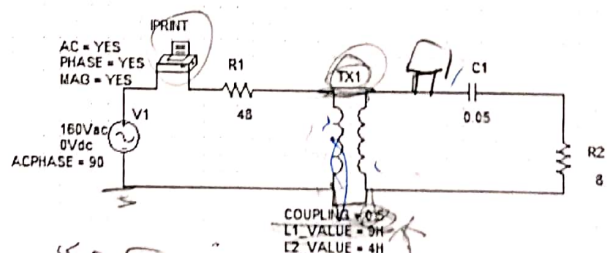


Fig. 4(b)