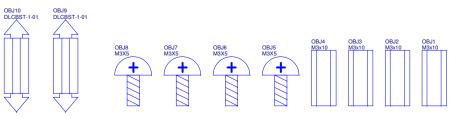
Revision	Date	Author	Description			
1	13/05/2017	G.Boschini	First Draft			
2	26/08/2017	G.Boschini	- Updated symbol from library - Removed micro SD card interface - Removed Aritk 020 USB Debug - Removed Aritk 020 USB Debug - Removed CMSL Jrag connector - Removed CMSL Jrag connector - Removed Accelerometer and Gyroscope sensor - Removed Bartery Backup from Artik - Changed Uniplay and Camera connector - Changed Uniplay and Camera connector - Changed WiFiell Module - Changed WiFiell Module - Changed LED driver functionality (from CM3 or Artik) - Moved on IZCO Camera, Display and EEPROM HAT - Moded NamoSIM support - Added NamoSIM support - Added extended IO Artik connector - Added Deve Disconnection functionality managed by Artik - Corrected SWD Artik pinout - Replaced USB USB and Ethernet controller with LAN9514			
3	20/11/2017	G.Boschini	- Updated symbol from library - Removed DSW1 - Removed DSW1 - Removed DSW1 - Removed Tryogramm connector (J23) - Removed Tryogramm connector (J23) - Repleaced eMMC with Sandisk - Repleaced eMMC with Sandisk - Added Power On/Off LED - Added URAT connection between Artis and CML via FTDI chip - Added Sy protection for power-up from HAT - Added Py protection for power-up from HAT - Added Power switch for power-down SV device (USB HOST, HDMI) - Added PLUIP on HBML LB, CB signal - Changed PUID LB, CB RB Resistor - Changed Wolfage Input Rail up to 30V			
4	02/01/2018	G.Boschini	- Correct PCB Led SilkScreen			
4	17/01/2018	G.Boschini	Removed UART Bluetooth connection  Changed IZC bus for RTC, Artik 020, CM3, IO Expander and isolated RTC and Artik bus from IO Expander and CM3 when CM3 is not powered  Modified SV HAT Power Supply  Enable M, pisable_mCTE  Removed WIF1 clock enable  Added switch for selection of WiF1 antennas and removed signal from CM3  Added switch for selection of WiF1 antennas and removed signal from CM3  Removed RGB Led  Added SWO connection from CM3 to Artik  Added SWO connection from CM3 to Artik  Adved HDMN Enable and PSM WiF1 to IO Expander  Replaced power input connector with black version			







	Project		RUBUS	Page name		
	Board		RUBUS		00 - REVISION	
TECHNOLOGY	Designer		G.Boschini	Approved	Not approved	
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Mail: info@fae.technology Tel: +39 035738130	Interna	l Code	<database></database>	Code	-	
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ll presente progetto é i delato della legge sul dirito d'autore, che viel	ology S.p.A.					

