# Ben Lancaster

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# **PERSONAL PROFILE**

I am passionate about <u>Embedded Firmware/Software</u> and <u>FPGAs</u> with great experience from an <u>RF Firmware Engineering placement</u>. I am interested in <u>Linux kernel</u> and <u>driver</u> development and I am active in the <u>open-source</u> community with contributions to Gravity-lang (compiler). I am always looking for an interesting project to dive into. **Key strengths:** 

- Self-motivated
- Problem-solving
- FPGA Placement experience
- C & C++
- Embedded Firmware/Software
- Linux kernel + driver development

#### **EMPLOYMENT**

## Firmware Engineer, Placement

## **Spirent Communications**

June 2016 - August 2017

- Embedded programming on Xilinx MicroBlaze FPGAs and PIC16/24 microcontrollers.
- Linux USB and PCle kernel driver development.
- Implemented on-chip power levelling and calibration for GNSS RF signal generators.
- Controlling on-board <u>fans</u>, <u>LEDs</u>, <u>EEPROM</u>, and other peripherals with <u>I2C</u> and <u>SMBus</u>.
- Configuring, building, and maintaining <u>Embedded Linux</u> distributions using <u>Yocto</u>.

# **EDUCATION**

## MSc, Embedded Systems Engineering

**University of Leeds** 

2018 - 2019

 Courses include: <u>Digital Signal Processing for Communications</u>, <u>FPGA Design for System-on-Chip</u>, <u>Embedded Microprocessor System Design</u>, <u>Medical Electronics and E-Health</u>, <u>Digital Media Engineering</u>.

## BSc (Hons), Computer Science (Current)

**University of Plymouth** 

2014 - 2018

- Final Project: FPGA-based 16-bit RISC soft-microprocessor (with IO & interrupts) and Compiler.
- Courses include: <u>Digital Electronics</u>, <u>Embedded Systems and Compilers</u>, <u>Machine Vision</u>, <u>Computation</u> Theory.

#### **OPEN-SOURCE PROJECTS & CONTRIBUTIONS**

- **16-bit RISC soft-microprocessor** bendl/vcc An <u>FPGA</u>-based RISC soft-microprocessor written in Verilog.
- Self-hosted Compiler and Standard Library bendl/libccl A C-like programming language and optimising compiler supporting, 8086, x86. Includes self-written standard library. C/C++, LLVM, Assembly.
- **Gravity-lang** marcobambini/gravity Contributor to an <u>open-source</u> compiler and virtual-machine. Contributions include fixing Windows runtime.
- NRBF Neural Network A dynamic NRBF neural network written in Python to predict UK energy usage.

#### **ADDITONAL EXPERIENCE AND AWARDS**

Dean's List 2015 & 2016 member. List of students who achieved academic excellence in their studies.

#### **TECHNOLOGIES**

- C, C++, Python, Linux (user + kernel), Bash, Linux kernel and driver development
- Xilinx FPGAs, ISE, Vivado, Impact, Visual Studio, CMake, CUDA

## **REFERENCES**

Available on request.