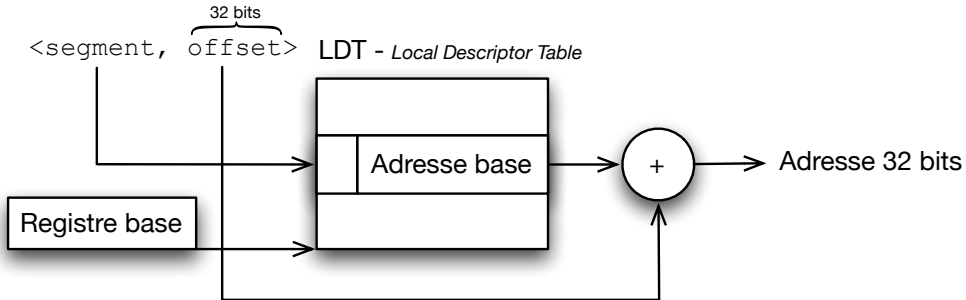


Première étape



Seconde étape

Adresse paginée sur 32 bits

$\langle \text{page}, \text{offset} \rangle$

The second step involves a 32-bit paginated address, which is split into a 20-bit page and a 12-bit offset, denoted as $\langle \text{page}, \text{offset} \rangle$.