

```
graph TD; subgraph Interface; MAC[MAC] --- DMA[DMA]; MAC --- PCS[PCS]; PCS --- Serdes[Serdes]; MDIO[MDIO]; end; Serdes --- Switch[Switch]; MDIO --- Switch; Switch --- PHY1[PHY]; Switch --- PHY2[PHY];
```

MAC

DMA

PCS

Serdes

MDIO

Switch

PHY

PHY