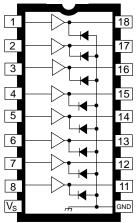
# 2981 THRU 2984

# 8-CHANNEL SOURCE DRIVERS

# UDN2981A thru UDN2984A



Dwg. No. A-10, 243

Note that the UDN2980A series (dual in-line package) and UDN2980LW series (small-outline IC package) are electrically identical and share a common terminal number assignment.

### **ABSOLUTE MAXIMUM RATINGS**

at 25°C Free-Air Temperature

Output Voltage Range, V<sub>CE</sub> (UDN2981A, UDN2982A, UDN2982LW, and A2982SLW) . . 5 V to 50 V (UDN2983A, UDN2984A, UDN2984LW, and A2984SLW). 35 V to 80 V Input Voltage, VIN (UDN2981A and UDN2983A) . . . . . . . . 15 V (UDN2982A, UDN2984A, UDN2982LW, UDN2984LW, A2982SLW, and A2984SLW) . . . . . . . 20 V Output Current, I<sub>OUT</sub> . . . . . . . . . . . . -500 mA Package Power Dissipation,  $P_D$  . . . . . . . . . See Graph Operating Temperature Range, T<sub>A</sub> . . . . . . -20°C to +85°C Storage Temperature Range,  $T_S$  . . . . . . . . . . -55°C to +150°C Recommended for high-side switching applications that benefit from separate logic and load grounds, these devices encompass load supply voltages to 80 V and output currents to -500 mA. These 8-channel source drivers are useful for interfacing between low-level logic and high-current loads. Typical loads include relays, solenoids, lamps, stepper and/or servo motors, print hammers, and LEDs.

All devices may be used with 5 V logic systems — TTL, Schottky TTL, DTL, and 5 V CMOS. The UDN2981A, UDN2982A, UDN2982LW, and A2982SLW are electrically interchangeable, will withstand a maximum output off voltage of 50 V, and operate to a minimum of 5 V; the UDN2983A, UDN2984A, UDN2984LW, and A2984SLW drivers are electrically interchangeable, will withstand an output voltage of 80 V, and operate to a minimum of 35 V. All devices in this series integrate input current limiting resistors and output transient suppression diodes, and are activated by an active high input.

The suffix 'A' (all devices) indicates an 18-lead plastic dual in-line package with copper lead frame for optimum power dissipation. Under normal operating conditions, these devices will sustain 120 mA continuously for each of the eight outputs at an ambient temperature of +50°C and a supply of 15 V.

The suffix 'LW' (UDN2982LW and UDN2984LW only) indicates an 18-lead surface-mountable wide-body SOIC package; the A2982SLW and A2984SLW are provided in a 20-lead wide-body SOIC package with improved thermal characteristics.

The UDN2982A, UDN2982LW, A2982SLW, UDN2984A, UDN2984LW, and A2984SLW drivers are also available for operation over an extended temperature range to -40°C. To order, change the prefix 'UDN' to 'UDQ' or the suffix 'SLW' to 'ELW'.

### **FEATURES**

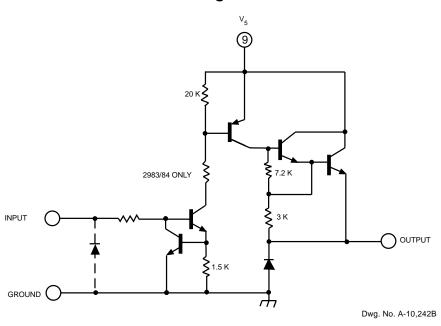
- TTL, DTL, PMOS, or CMOS Compatible Inputs
- 500 mA Output Source Current Capability
- Transient-Protected Outputs
- Output Breakdown Voltage to 80 V
- DIP or SOIC Packaging

The A2984SLW, UDN2984A, & UDN2984LW are discontinued. Shown for reference only.

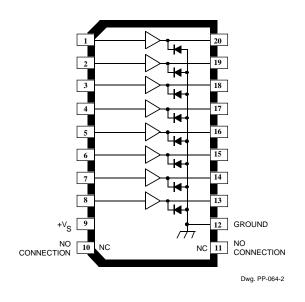
Always order by complete part number, e.g., **UDN2981A**. Note that all devices are not available in all package styles.

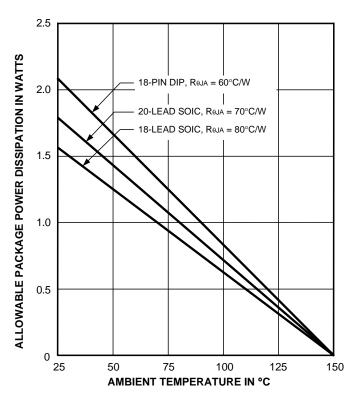


## **One of Eight Drivers**



## **A2982SLW and A2984SLW**





Dwg. GP-022-4



## ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$ (unless otherwise specified).

		Applicable			Limits			
Characteristic	Symbol	Devices	Test Conditions	Fig.	Min.	Тур.	Max.	Units
Output Leakage Current	I <sub>CEX</sub>	2981/82†	$V_{IN} = 0.4 \text{ V*}, V_S = 50 \text{ V}, T_A = +70^{\circ}\text{C}$	1	_	_	200	μΑ
		2983/84†	$V_{IN} = 0.4 \text{ V*}, V_S = 80 \text{ V}, T_A = +70^{\circ}\text{C}$	1	_	_	200	μА
Output Sustaining	V <sub>CE(SUS)</sub>	2981/82†	I <sub>OUT</sub> = -45 mA	_	35	_	_	V
Voltage		2983/84†	I <sub>OUT</sub> = -70 mA	_	45	_	_	V
Collector-Emitter			V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = -100 mA	2	_	1.6	1.8	V
Saturation Voltage	V <sub>CE(SAT)</sub>	All	V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = -225 mA	2	_	1.7	1.9	V
			V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = -350 mA	2	_	1.8	2.0	V
Input Current		2981/83A	V <sub>IN</sub> = 2.4 V	3	_	140	200	μΑ
	I <sub>IN(ON)</sub>		V <sub>IN</sub> = 3.85 V	3	_	310	450	μΑ
		2982/84†	V <sub>IN</sub> = 2.4 V	3	_	140	200	μА
			V <sub>IN</sub> = 12 V	3	_	1.25	1.93	mA
Output Source Current	I <sub>OUT</sub>	2981/83A	V <sub>IN</sub> = 2.4 V, V <sub>CE</sub> = 2.0 V	2	-350	_	_	mA
(Outputs Open)		2982/84†	V <sub>IN</sub> = 2.4 V, V <sub>CE</sub> = 2.0 V	2	-350	_	_	mA
Supply Current	I <sub>S</sub>	2981/82†	$V_{IN} = 2.4 \text{ V}^*, V_S = 50 \text{ V}$	4	_	_	10	mA
Leakage Current		2983/84†	$V_{IN} = 2.4 \text{ V*}, V_S = 80 \text{ V}$	4	_	_	10	mA
Clamp Diode	I <sub>R</sub>	2981/82†	V <sub>R</sub> = 50 V, V <sub>IN</sub> = 0.4 V*	5	_	_	50	μΑ
Forward Voltage		2983/84†	V <sub>R</sub> = 80 V, V <sub>IN</sub> = 0.4 V*	5	_	_	50	μΑ
Clamp Diode	V <sub>F</sub>	All	I <sub>F</sub> = 350 mA	6	_	1.5	2.0	V
Turn-On Delay	t <sub>ON</sub>	All	$0.5~E_{IN}$ to $0.5~E_{OUT},~R_L=100\Omega,$ $V_S=35~V$	_	_	1.0	2.0	μs
Turn-Off Delay	t <sub>OFF</sub>	All	$0.5~E_{\text{IN}}$ to $0.5~E_{\text{OUT}},~R_{\text{L}}$ = $100\Omega,$ $V_{\text{S}}$ = $35~V,~\text{See}$ Note	_	_	5.0	10	μs

NOTES: Turn-off delay is influenced by load conditions. Systems applications well below the specified output loading may require timing considerations for some designs, i.e., multiplexed displays or when used in combination with sink drivers in a totem pole configuration.

Negative current is defined as coming out of (sourcing) the specified device terminal.

UDN2982A, UDN2982LW, or A2982SLW,

UDN2983A,

UDN2984A, UDN2984LW, or A2984SLW.

The A2984SLW, UDN2984A, & UDN2984LW are discontinued. Shown for reference only.

<sup>\*</sup> All inputs simultaneously.

<sup>†</sup> Complete part number includes a prefix (A or UDN) and a suffix (A or SLW) as follows: UDN2981A,

# **TEST FIGURES** Figure 1 Figure 2 Figure 3 $I_{IN}$ -O OPEN $I_{OUT}$ Dwg. No. A-11,083 Dwg. No. A-11,084 Dwg. No. A-11,085 Figure 4 Figure 5 Figure 6 OPEN o OPEN OPEN

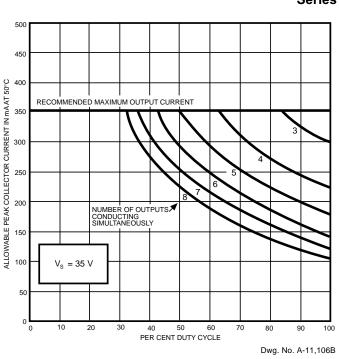
Dwg. No. A-11,086

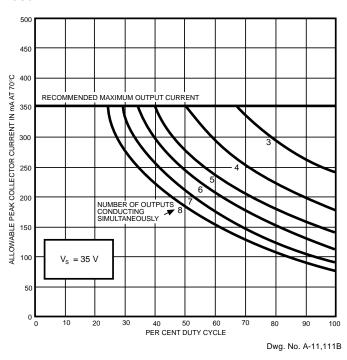
Dwg. No. A-11,087

Dwg. No. A-11,088

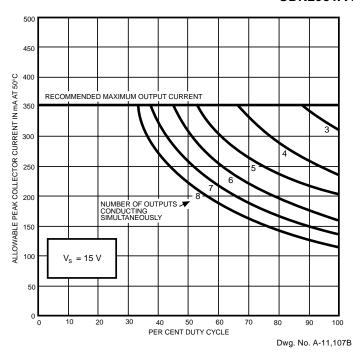
# Allowable peak collector current as a function of duty cycle

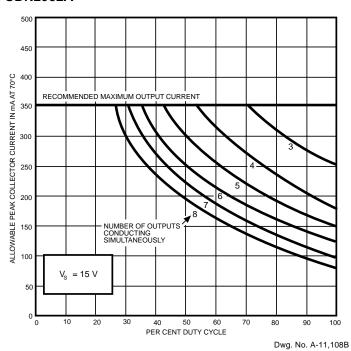
### Series UDN2980A





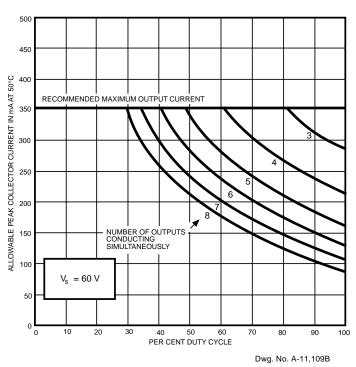
#### **UDN2981A and UDN2982A**





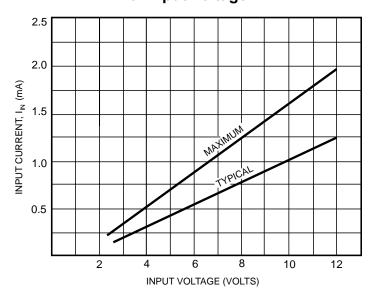
# Allowable peak collector current as a function of duty cycle

### **UDN2983A and UDN2984A**



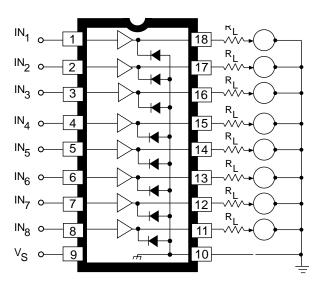
450 ALLOWABLE PEAK COLLECTOR CURRENT IN MA AT RECOMMENDED MAXIMUM OUTPUT CURRENT 350 300 250 NUMBER OF OUTPUTS CONDUCTING SIMULTANEOUSLY 150 100 V<sub>s</sub> = 60 V 50 40 50 60 PER CENT DUTY CYCLE 20 90 100 Dwg. No. A-11,110B

# Input current as a function of input voltage



Dwg. No. A-11,115B

# Typical electrosensitive printer application



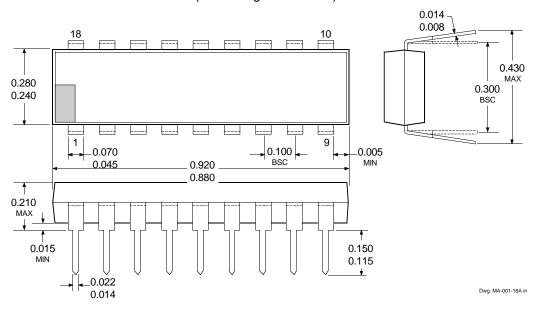
Dwg. No. A-11,113A



## UDN2981A, UDN2982A, UDN2983A, and UDN2984A

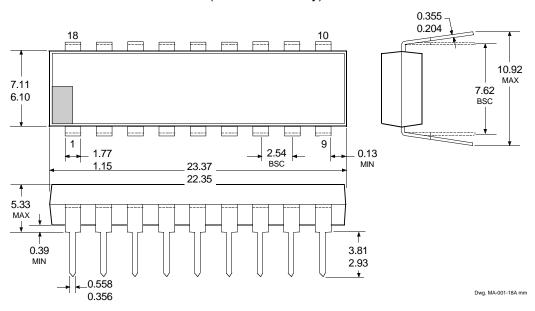
## **Dimensions in Inches**

(controlling dimensions)



# Dimensions in Millimeters

(for reference only)



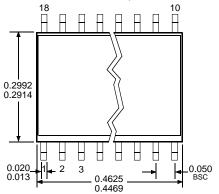
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
  - 2. Lead spacing tolerance is non-cumulative.
  - 3. Lead thickness is measured at seating plane or below.
  - 4. Supplied in standard sticks/tubes of 21 devices.

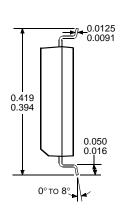
### UDN2982LW and UDN2984LW

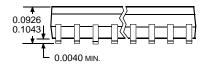
(add "TR" to part number for tape and reel)

## **Dimensions in Inches**

(for reference only)



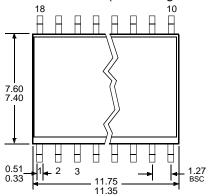


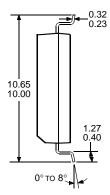


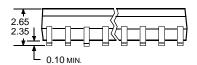
wg. MA-008-18A in

### **Dimensions in Millimeters**

(controlling dimensions)







Dwg. MA-008-18A mm

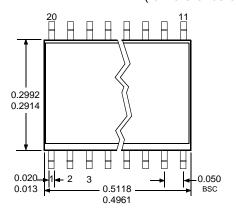
NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.

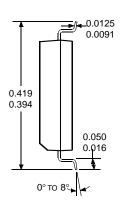
- 2. Lead spacing tolerance is non-cumetive.
- 3. Supplied in standard sticks/tubes of 41 devices or add "TR" to part number for tape and reel.

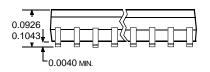


## **A2982SLW and A2984SLW**

(add "TR" to part number for tape and reel) **Dimensions in Inches**(for reference only)



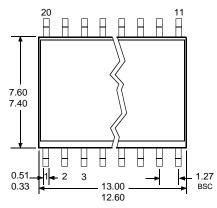


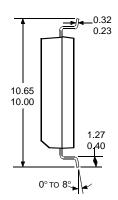


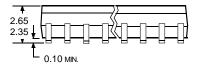
Dwg. MA-008-20 in

## **Dimensions in Millimeters**

(controlling dimensions)







Dwg. MA-008-20 mm

- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
  - 2. Lead spacing tolerance is non-cumulative.
  - 3. Supplied in standard sticks/tubes of 37 devices or add "TR" to part number for tape and reel.

# **POWER SOURCE DRIVERS**

#### IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

Oı	utput Rati	nas *						
mA	V	#	Serial Input	Latche Driver		Saturated Outputs	Internal Protection	Part Number <sup>†</sup>
-25	60	8	_	Х	_	_	_	5815
	60	10	Х	Χ	active pull-do	own –	_	5810-F and 6810
	60	12	Χ	Χ	active pull-do	own –	_	5811
	60	20	Х	Χ	active pull-do	own –	_	5812-F and 6812
	60	32	Χ	Х	active pull-do	own –	_	5818-F and 6818
	85	8	_	_	_	_	_	6118
-120	-25	8	_	_	Х	Х	_	2585
	30	8	_	_	X	Χ	_	2985
	50	8	Χ	X	X	Χ	_	5895
-350	35	8	_	_	Х	_	Х	2987
	50	8	_	_	X	_	_	2981 and 2982
	50	8	Χ	X	X	_	_	5891
	-50	8	_	_	X	_	_	2580
	80	8	_	_	X	_	_	2983
	80	8	Χ	Х	Χ	_	_	5890
	-80	8	_	_	Χ	_	_	2588
-500	6	1	_	_	=	MOSFET	X	2525 and 2535
	6	2	_	_	_	MOSFET	Χ	2535 and 2536
-4000	60	4	_	_	X	_	_	2944

<sup>\*</sup> Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

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<sup>†</sup> Complete part number includes additional characters to indicate operating temperature range and package style.

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