

VE 320 – Summer 2012 Introduction to Semiconductor Device

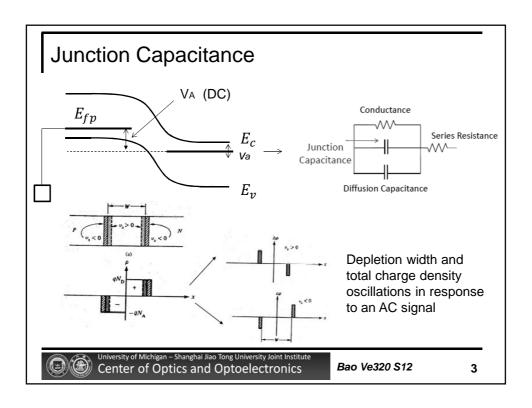
PN Junction Diode AC Response

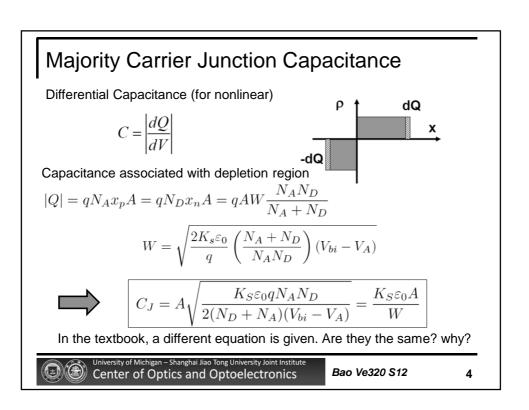
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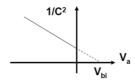




Majority Carrier Junction Capacitance

Define
$$C_{J0} = C_J|_{V_A=0}$$

$$C_J = \frac{C_{J0}}{(1 - \frac{V_A}{V_{bi}})^{1/2}}$$



Junction capacitance is proportional to applied voltage. Used as a voltage-dependent capacitor. Built-in voltage can be extracted this way.

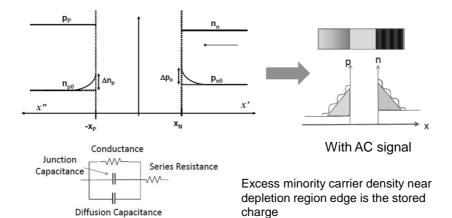


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Diffusion Capacitance

Majority carriers respond to the AC signal and create junction capacitance How about minority carriers?



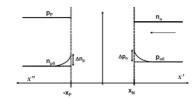
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Charge Density

$$\Delta n_{p}(x'') = \frac{n_{i}^{2}}{N_{A}} \left(e^{qV_{A}/kT} - 1 \right) e^{-x''/L_{N}}$$

$$\Delta p_{n}(x') = \frac{n_{i}^{2}}{N_{D}} \left(e^{eV_{A}/kT} - 1 \right) e^{-x'/L_{P}}$$



$$Q_p = qA \int_{x_n}^{\infty} \Delta p_n(x) dx = qA p_{n0} L_P(e^{qV_A/kT} - 1)$$

$$Q_n = qA \int_{-\infty}^{-x_p} \Delta n_p(x) dx = qA n_{p0} L_N(e^{qV_A/kT} - 1)$$

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Diffusion Capacitance

$$C_D = \frac{dQ_p}{dV} + \frac{dQ_n}{dV}$$

$$C_D = \left[Aq^2 \frac{n_{p0}L_N + p_{n0}L_P}{kT} \right] \exp\left(\frac{qV_A}{kT}\right)$$

For p+-n junction diode

$$C_D = \left(\frac{Aq^2p_{n0}L_P}{kT}\right)\exp\left(\frac{qV_A}{kT}\right)$$

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Junction and Diffusion Capacitance

$$C_J = A\sqrt{\frac{K_S \varepsilon_0 q N_A N_D}{2(N_D + N_A)(V_{bi} - V_A)}} = \frac{K_S \varepsilon_0 A}{W}$$

$$C_D = \left[Aq^2 \frac{n_{p0}L_N + p_{n0}L_P}{kT} \right] \exp\left(\frac{qV_A}{kT}\right)$$

Under what conditions are each dominant? Why?



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Quasi-statical Response

- The above derivations assume that the carriers respond quasi-statically with the a.c. signal.
- At higher frequencies, the carriers cannot move as fast as the rapidly varying signal.

Response time:

Majority Carriers: 10^{-10} s or less for silicon

Minority Carriers: on the order of minority carrier lifetime, eg. 10^{-6} s

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