

danchouzhou@gmail.com

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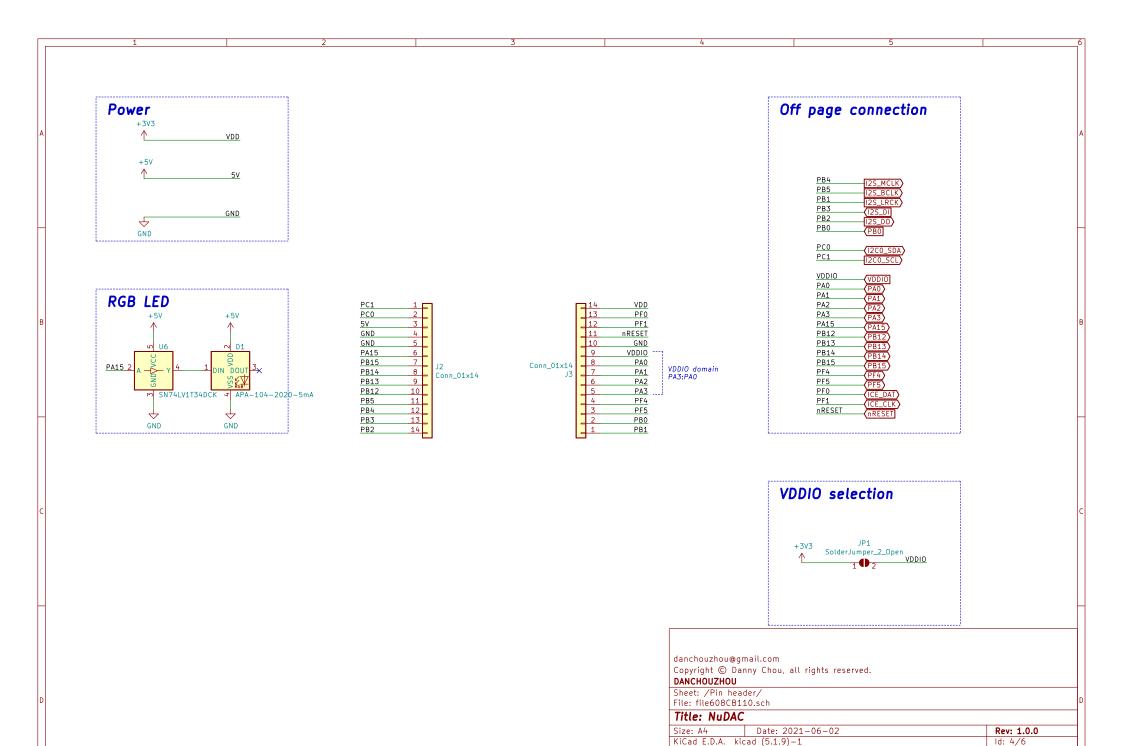
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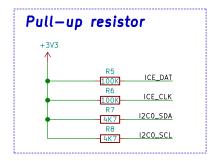
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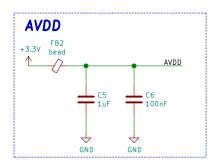
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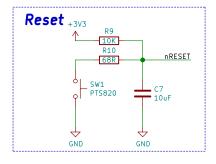
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 Date: 2021-06-02
 Rev: 1.0.0

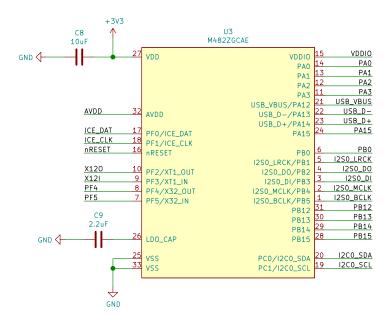
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\	'BUS
USB_VBUS USB_D - USB_D+	
1250_MCLK 1250_BCLK 1250_LRCK 1250_DI 1250_DO PBO	-[12S_MCLK) -[12S_BCLK) -[12S_LRCK) -[12S_DI] -[12S_DO) -(PB0]
12C0_SDA 12C0_SCL	-(12C0_SDA) -(12C0_SCL)
VDDIO PA0 PA1 PA2 PA3 PA15 PB12 PB13 PB14 PB15 PF4 PF5	(VDDIO) (PA1) (PA2) (PA3) (PA15) (PA15) (PB13) (PB14) (PB15) (PF4) (PF5)

TCE_DAT)

ICE_CLK

ICE_DAT

ICE_CLK

nRESET

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