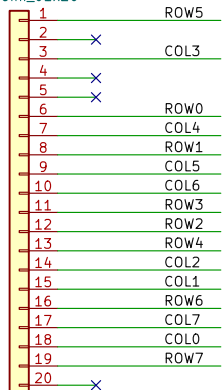
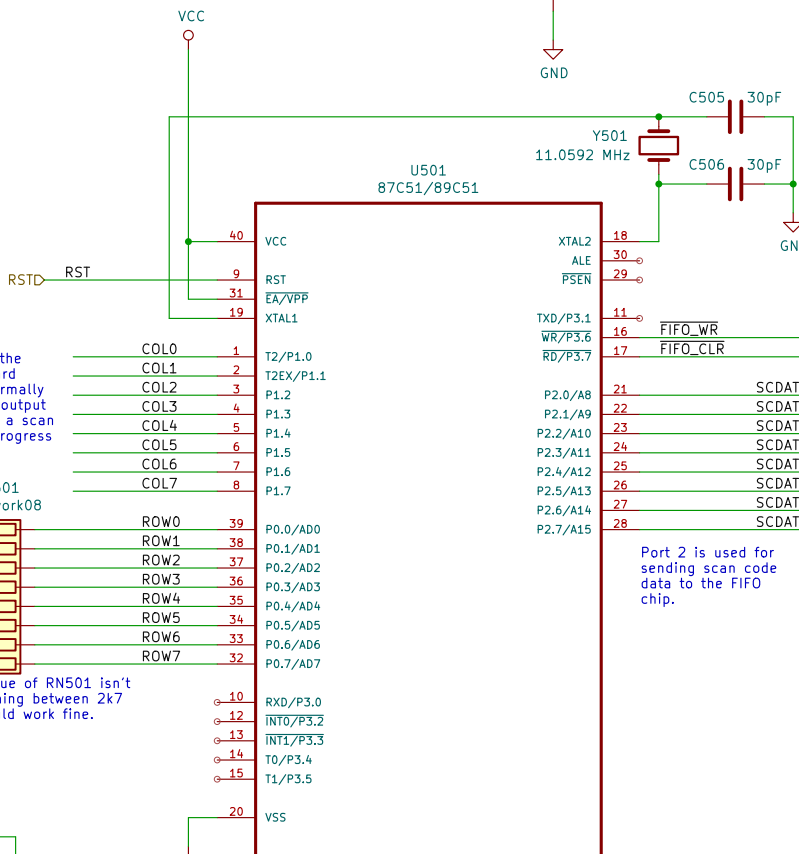
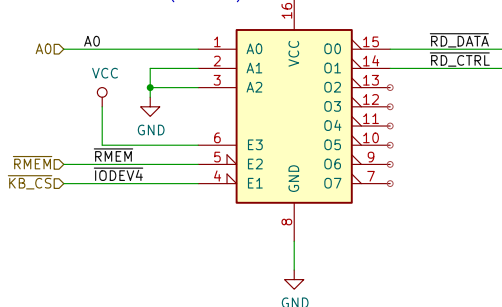


C16 keyboard connector, pin 2 is a key and should be removed

J501
Conn_01x20



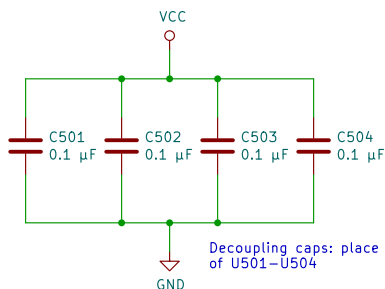
Generate chip selects for
reading data from FIFO
(RD_DATA) and reading
FIFO status bits (RD_CTRL).



Port 1 actively drives the
columns of the keyboard
matrix: outputs are normally
at VCC, but a column output
is driven to GND when a scan
of that column is in progress

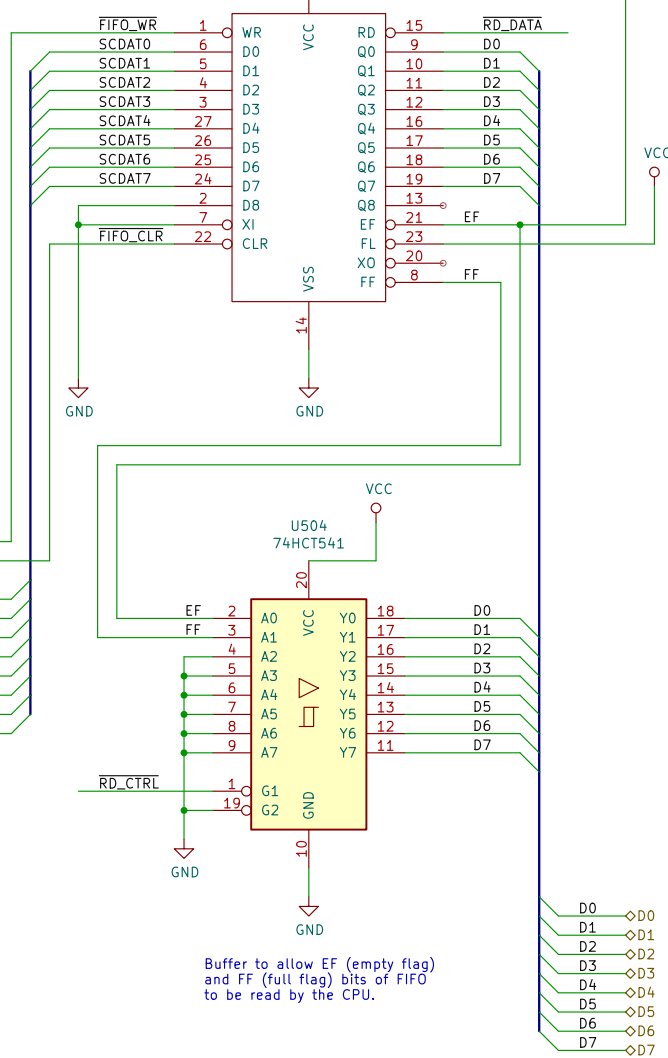
Port 0 reads the rows
(with pull up resistors
keeping the row inputs
at VCC unless a
keyswitch has made a
connection to a column
when the column scan
is in progress)

The exact value of RN501 isn't
critical: anything between 2k7
and 47k should work fine.



Decoupling caps: place near power pins
of U501-U504

Microcontroller to scan
keyboard matrix and
send scan codes to FIFO.
Will be a 89C51 or 87C51.



J503
Conn_01x02
Jumper to enable keyboard
IRQ (when FIFO is not empty)

Buffer to allow EF (empty flag)
and FF (full flag) bits of FIFO
to be read by the CPU.

daveho hacks

Sheet: /Keyboard/
File: Keyboard.kicad_sch

Title: keyboard controller

Size: USLetter Date:
KiCad E.D.A. 9.0.2

Rev:
Id: 7/9

