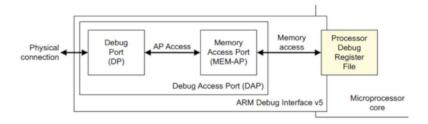
\*\*\*\*\*\*\*\*\*\*\*\*Default\*\*\*\*\*\*\*\*

## **Serial Wire Debug (SWD)**

https://community.silabs.com/s/article/serial-wire-debug-swd-x?language=en\_US



https://community.silabs.com/s/article/serial-wire-debug-successful-write-operation? language=en\_US

## Serial Wire Debug successful write operation

