

Dilip Puri

FIRMWARE DEVELOPER, INTEL CORPORATION

OBJECTIVE

To constantly improve my knowledge base being a continuous learner. I wish to add value to create better organization and to create a better world.

PERSONAL DATA

ADDRESS: Bengaluru, India
PHONE: +91 9460793983
EMAIL: dilippuripuri@gmail.com
LINKEDIN: <https://www.linkedin.com/in/dilip-puri-852ab185/>
GITHUB: <https://github.com/dilippuri>

SKILLS & INTERESTS

Areas of Interest: MACHINE LEARNING, SUPER COMPUTING, FIRMWARE DEVELOPMENT
Familiar Languages: C, C++, Python

WORK EXPERIENCE

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| AUG 2020 - PRESENT | <i>Firmware Developer (Intel Corporation)</i>
Currently, I am working as Firmware Developer at Intel. We develop critical firmware software. |
| AUG 2020 - FEB 2022 | <i>GPU Software Development Engineer (Intel Corporation)</i>
I worked as GPU Software Development Engineer in AXG Super-Compute Linux Debug team. Most of issues on which we were working on were related to OpenCL, Level-Zero and Intel-Graphics-Compiler. We were debugging failures that come from GPU validation, driver development, performance test and internal stress testing. |
| JUNE 2019 - JULY 2020
(Internship) | <i>Graduate Intern (AIPG, Intel Corporation)</i>
I did internship in Artificial Intelligence Product Group at Intel. We were working on deep learning hardware accelerator to train various machine learning models. <ul style="list-style-type: none">• Addition of new features in the profiler tool.• Json based configuration of Profiler tool.• Management API for profiler tool.• Testing using GTest framework. |
| JULY 2017 - JUNE 2018 | <i>SAP ABAP Developer (ATOS Global)</i>
I joined ATOS as an SAP ABAP developer. I got training in SAP ABAP and SAP FICO. I joined P&G team and I was responsible for developing various features using SAP ECC. Generally, features are like changes in reports, function module, tax engine etc. |

PROJECTS

JUN'19 - JUL'20 MTech Thesis Link	<i>Data sampling strategies for Multi-class imbalanced Data</i> In my MTech thesis, I worked on sampling techniques to handle multi-class imbalanced data problem for better performance and lesser complexity of machine learning algorithms using distributed programming.
JAN-MAY 2017 BTech Thesis Link	<i>Hippocampus Segmentation using CNN based deep learning architecture</i> This project was based on image segmentation in which we have segmented Hippocampus from 3D images of Brain MRI. We trained our model on different Hippocampus Images using CNN so that it can segment the hippocampus and predict whether it is shrank or not. We used Auto encoder model and achieved an accuracy around 80%.
MAY-JULY 2016 Team size - Individual Link	<i>Optical Character Recognition for AadhaarCard, PAN Card</i> In this project, we created OCR services to extract the meaningful data from a scanned image of Aadhaar-Card. The accuracy of extracted data was around 80-85%. We extracted meaningful data like Name, Date of birth, Aadhaar-Card No., Address, etc. In this project, we used OpenCV for image processing and python-tesseract library for text extraction.
AUG 2015 Team size - 3 Link	<i>PageRank to rank search results</i> In this project, we implemented a small scale version of Google's Pagerank Algorithm. The main aim was to rank interlinked web pages for given one or multiple words query and to sort the pages according to relevance with given query.
JULY-AUG 2015 Team size - 3 Link	<i>3D image reconstruction based on Stereo Vision</i> The aim was to extract 3D information of scene points from a given pair of stereo images. We calculated depth map of stereo images and relative distances of objects in the image.

EDUCATION

JULY 2020	Master of Technology in COMPUTER SCIENCE ENGINEERING, Sardar Vallabhbhai National Institute of Technology, Surat
MAY 2017	Bachelor of Technology in COMPUTER SCIENCE ENGINEERING, Indian Institute of Information Technology, Vadodara

AWARDS & ACHIEVEMENTS

- 2019 Placement co-ordinator MTech class, SVNIT, Surat.
- 2012 Top 1% in state merit, MHRD Scholarship for Higher-Education([SHE](#)) by Department of Science and Technology (DST), Delhi.
- 2012 District topper in Senior Secondary.

INTERESTS AND ACTIVITIES

Watching Documentaries and TED videos
Table Tennis and Reading Books
OpenSource Technology