

CIPSAL

4CB110

10A

250V~

8kA 30/60Hz
V8-443



<http://www.flickr.com/photos/bmoyle/529339319/>



<http://www.flickr.com/photos/mugley/2038957354/>



<http://www.flickr.com/photos/whatwhat/30382890/>



<http://www.flickr.com/photos/thundershead/385352269/>



<http://www.flickr.com/photos/wili/157235301/>



<http://www.flickr.com/photos/dominik99/1403329318/>



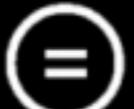
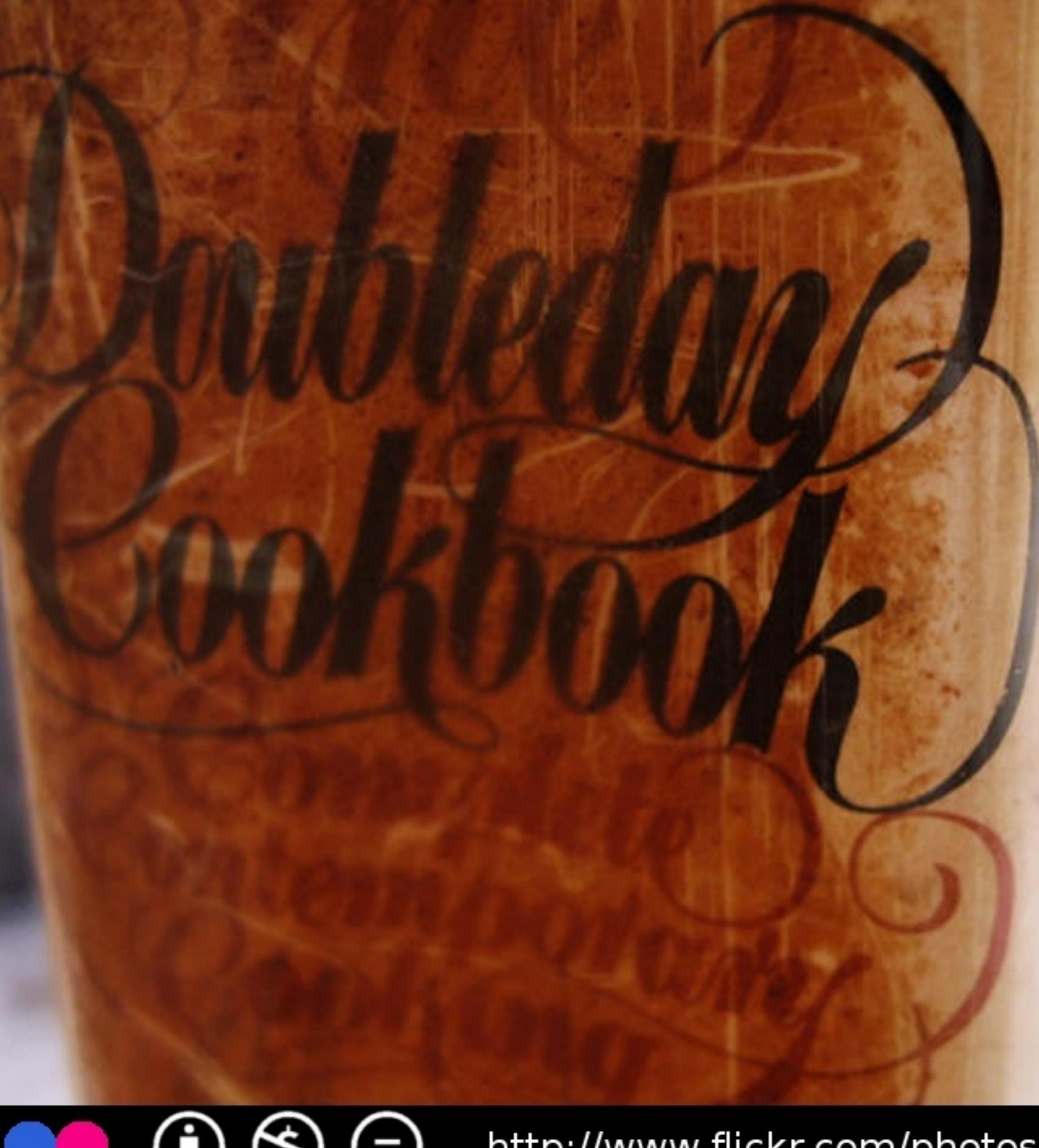
<http://www.flickr.com/photos/maxbisschop/324291309/>



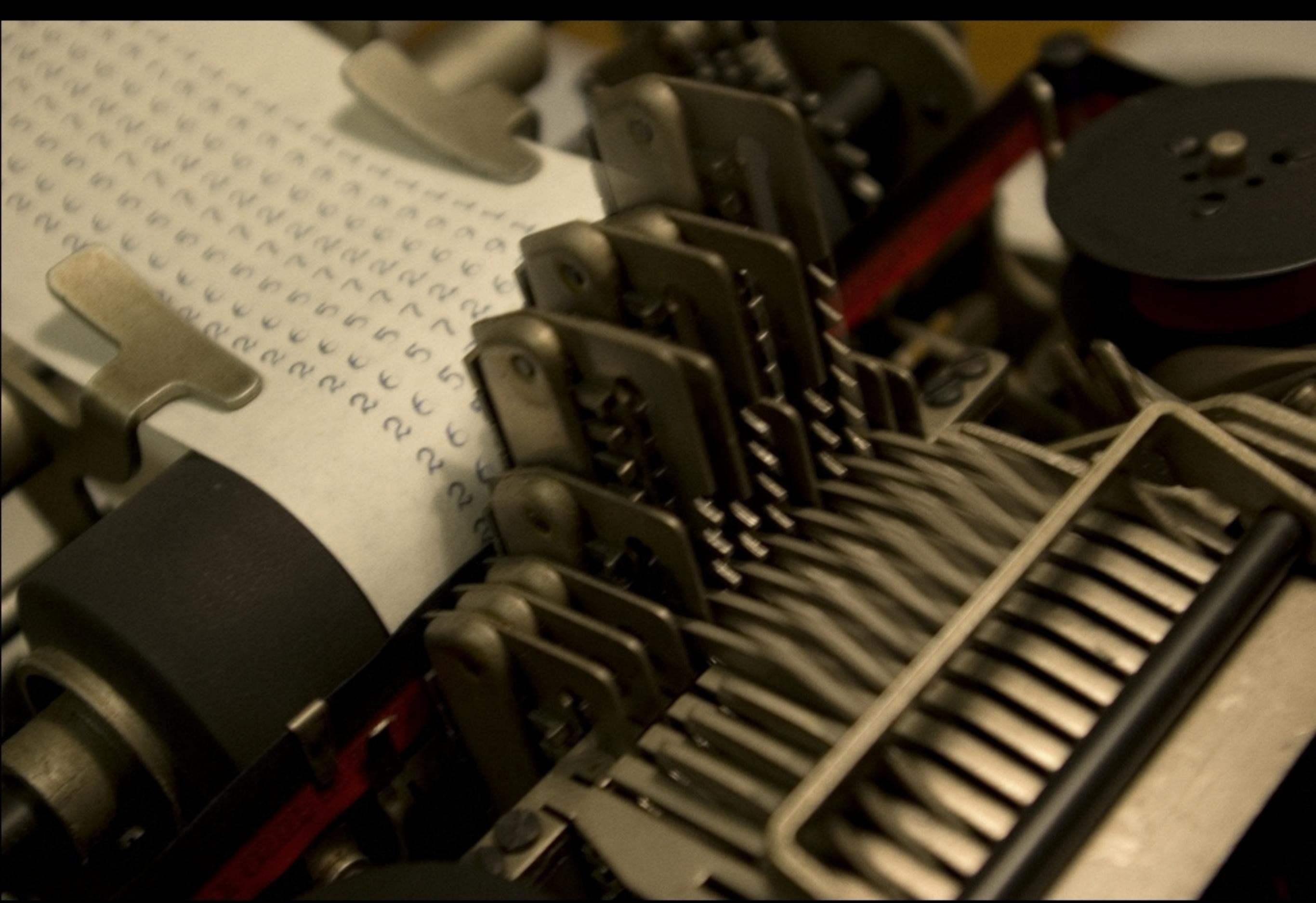
<http://www.flickr.com/photos/tonivc/2283676770/>



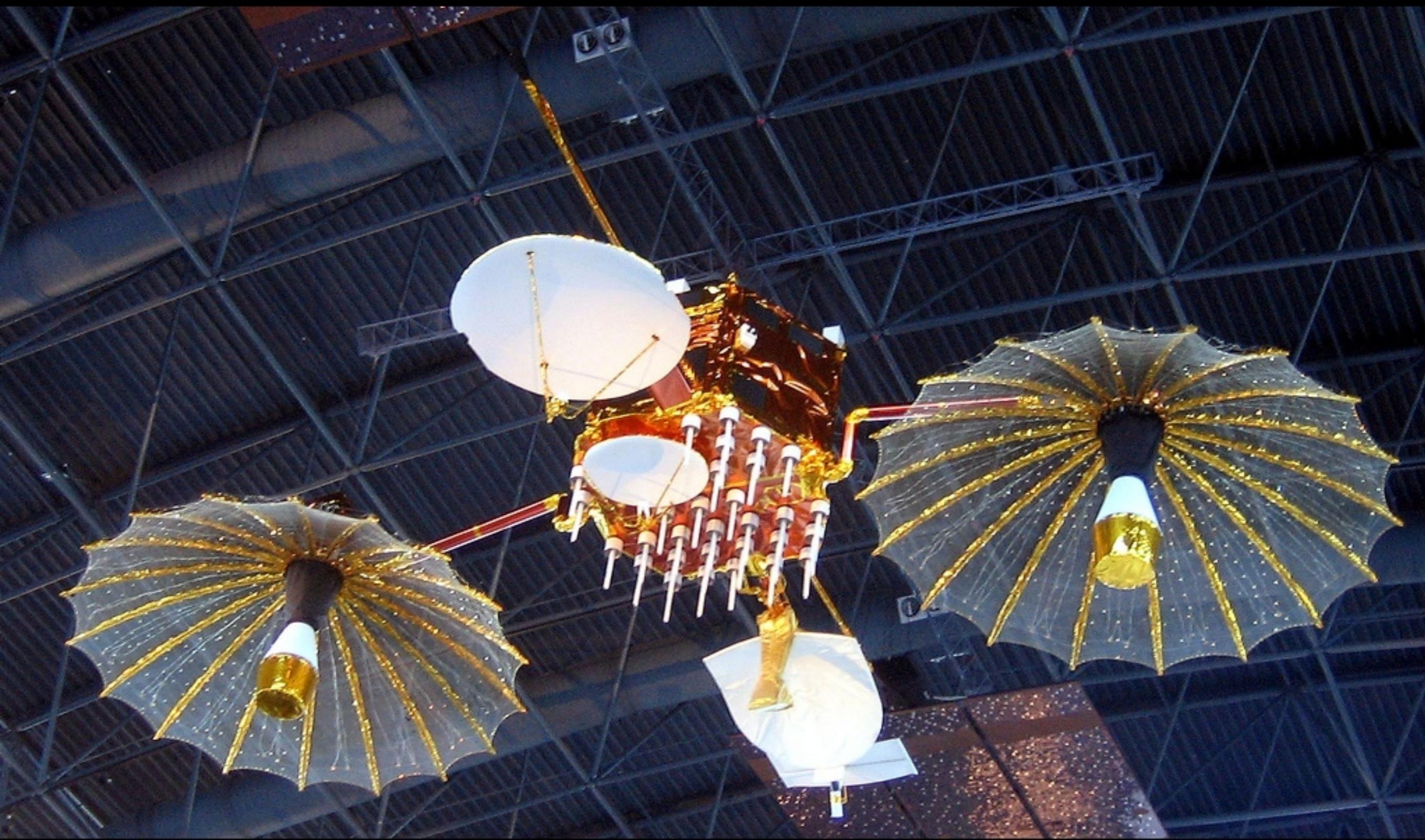
<http://www.flickr.com/photos/jeff-bauche/1209466469/>



<http://www.flickr.com/photos/p3nnylan3/271453186>

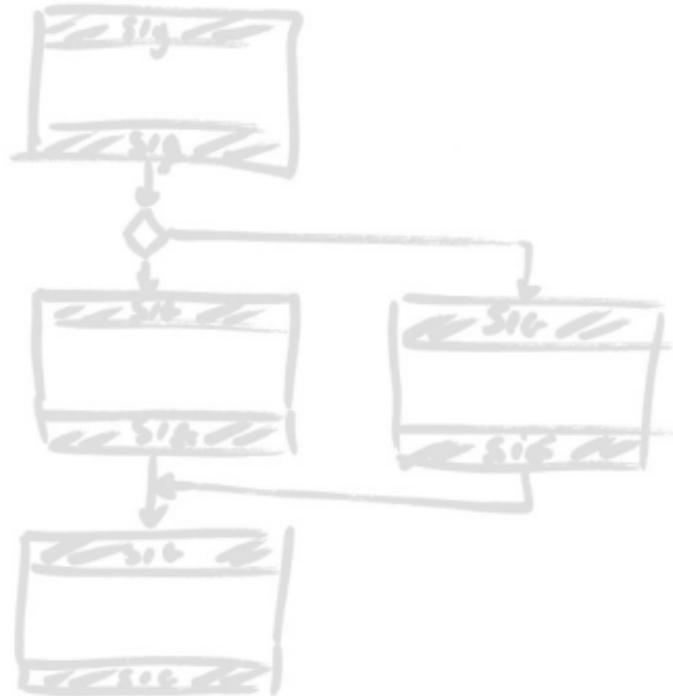


<http://www.flickr.com/photos/pradovit/2554965389/>

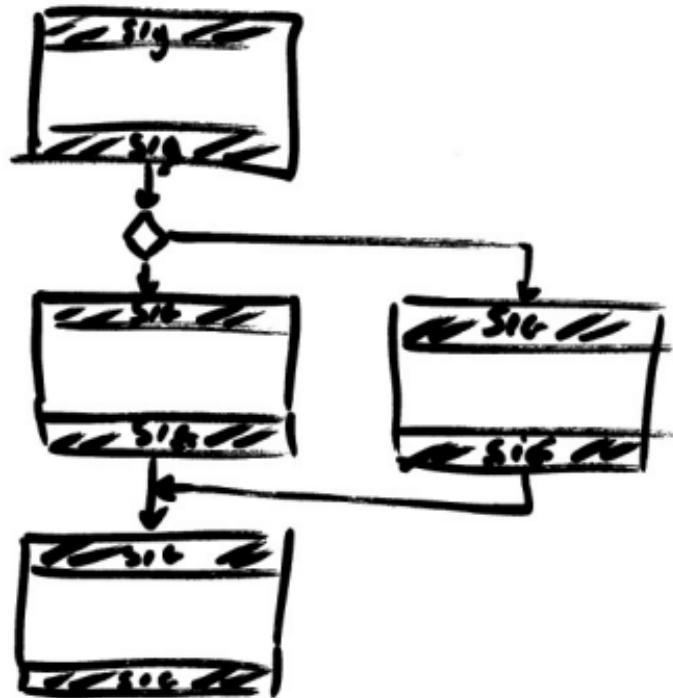


<http://www.flickr.com/photos/quirky/451275880/>

$x \rightarrow x_1, x_2$



$x \rightarrow x_1, x_2$



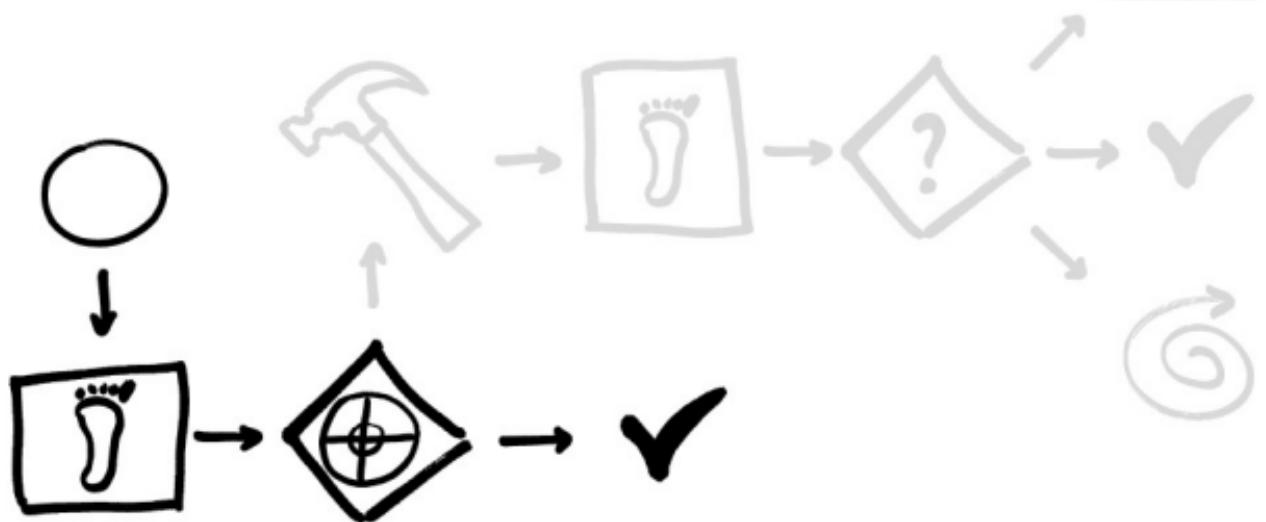


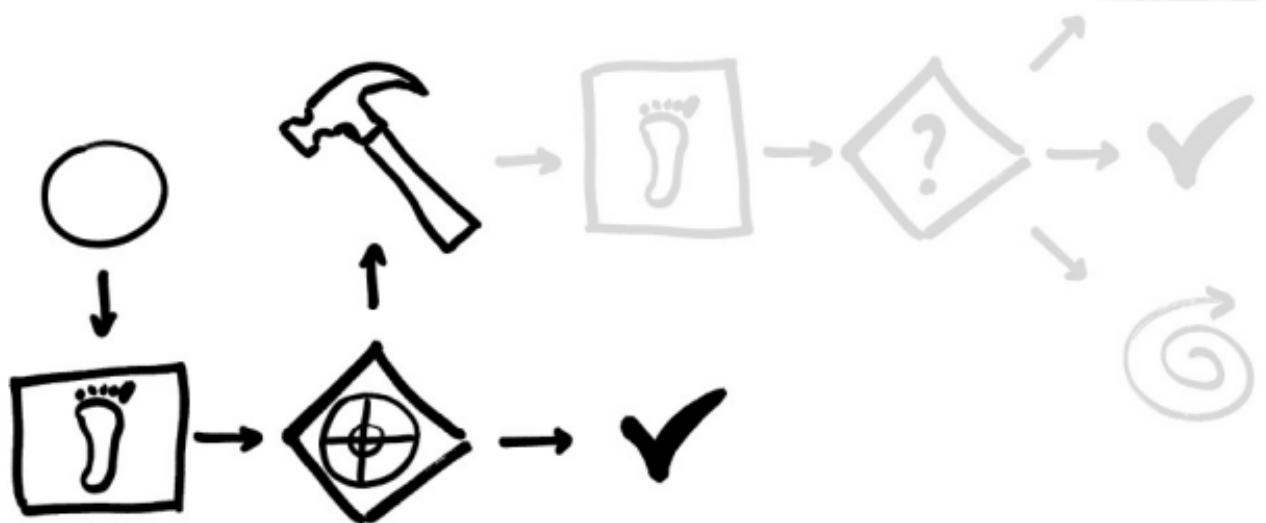


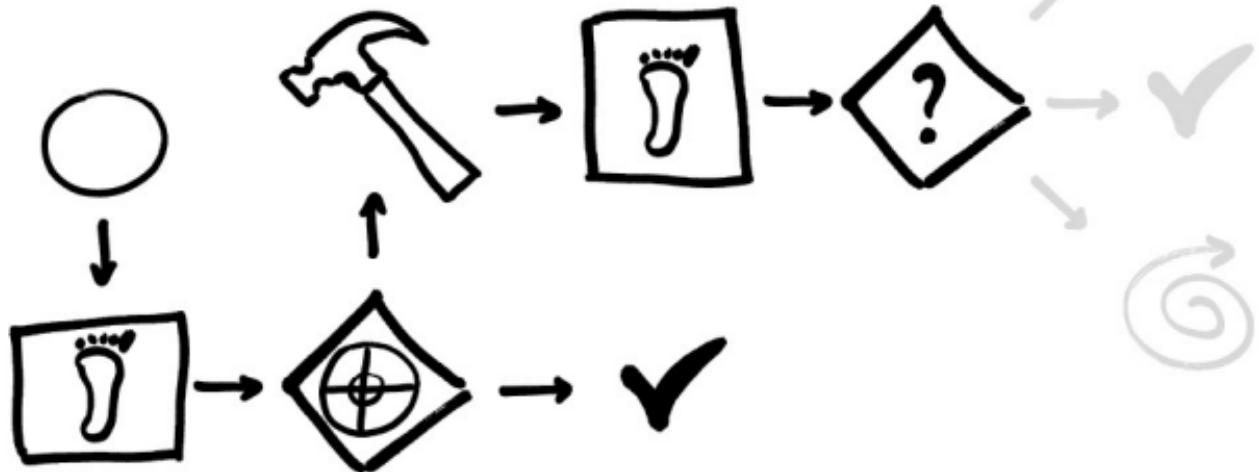


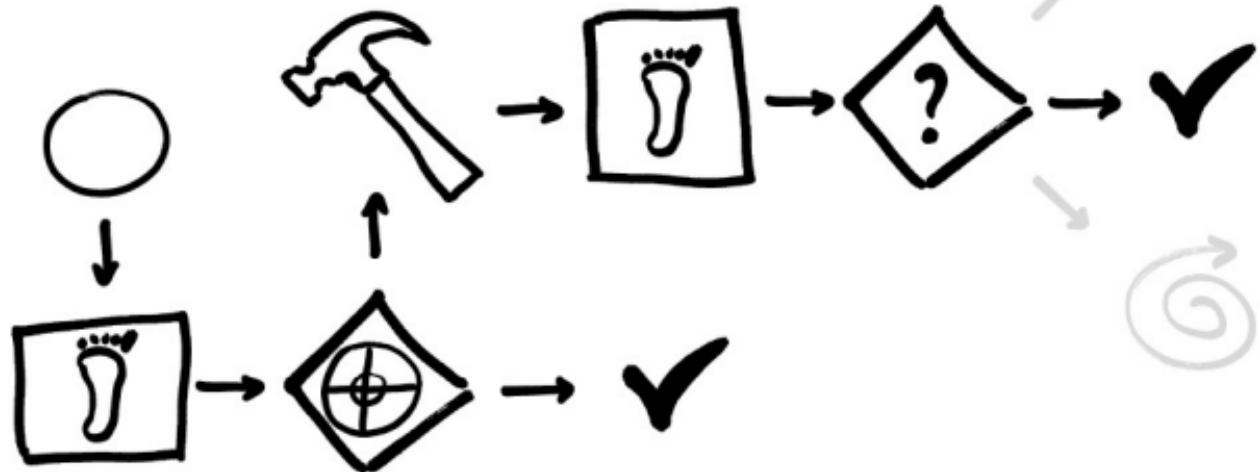


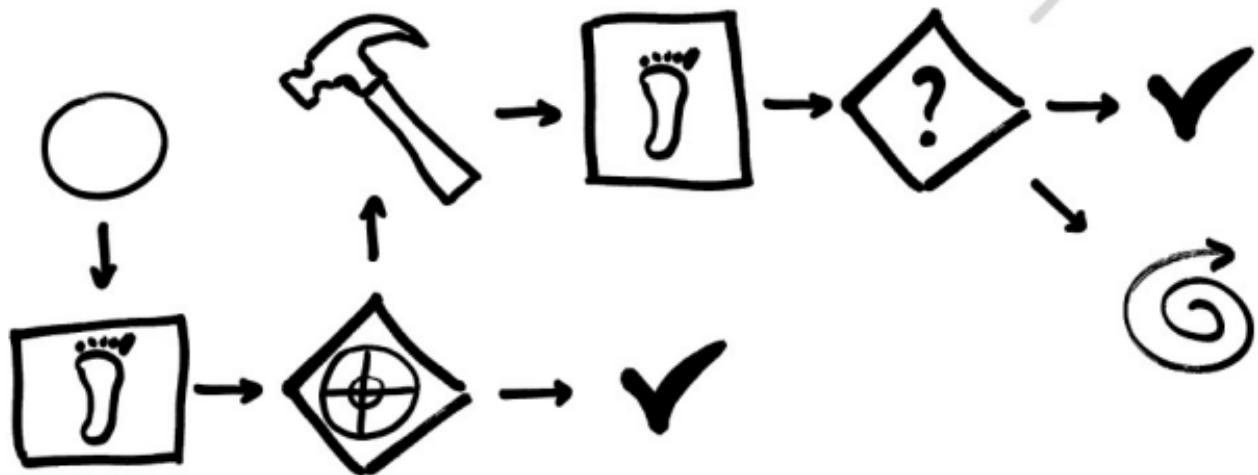


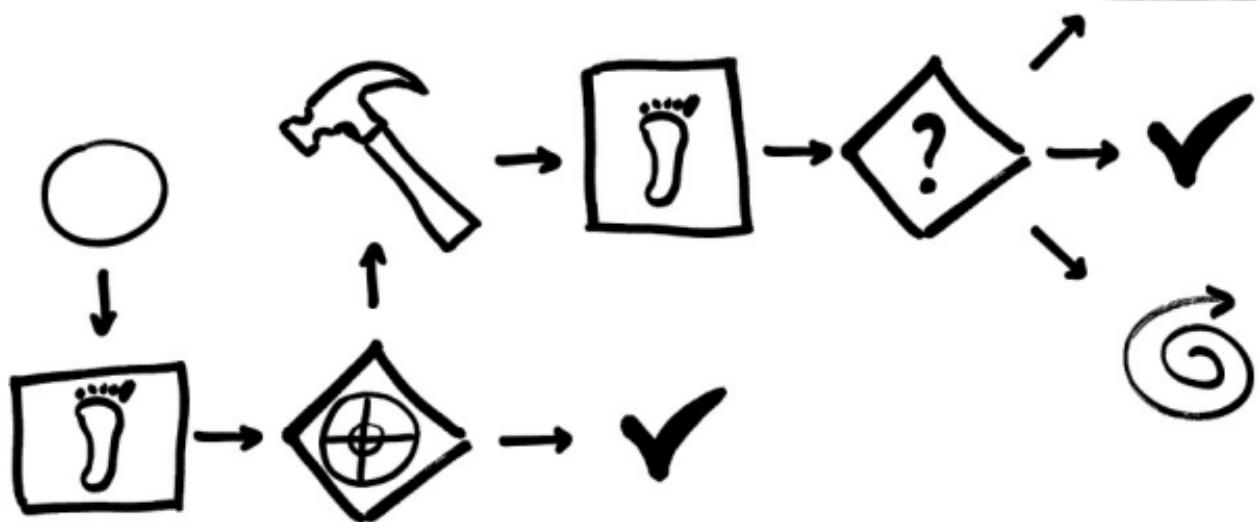












```
0804808e <putstring>:  
804808e:    55                      pushl  %ebp  
804808f:    89 e5                  movl   %esp,%ebp  
8048091:    8b 4d 08                movl   0x8(%ebp),%ecx  
8048094:    31 d2                  xorl   %edx,%edx  
  
08048096 <count_chars>:  
8048096:    8a 04 11                movb   (%ecx,%edx,1),%al  
8048099:    84 c0                  testb  %al,%al  
804809b:    74 03                  je     80480a0 <done_count_chars>  
804809d:    42                     incl   %edx  
804809e:    eb f6                  jmp    8048096 <count_chars>  
  
080480a0 <done_count_chars>:  
80480a0:    b8 04 00 00 00          movl   $0x4,%eax  
80480a5:    31 db                  xorl   %ebx,%ebx  
80480a7:    43                     incl   %ebx  
80480a8:    cd 80                  int    $0x80  
80480aa:    b8 04 00 00 00          movl   $0x4,%eax  
80480af:    8d 0d c0 90 04 08          leal   0x80490c0,%ecx  
80480b5:    31 d2                  xorl   %edx,%edx  
80480b7:    42                     incl   %edx  
80480b8:    cd 80                  int    $0x80  
80480ba:    89 ec                  movl   %ebp,%esp  
80480bc:    5d                     popl   %ebp  
80480bd:    c3                     ret
```

0804808e <putstring>:

804808e:	55	pushl %ebp
804808f:	89 e5	movl %esp,%ebp
8048091:	8b 4d 08	movl 0x8(%ebp),%ecx
8048094:	31 d2	xorl %edx,%edx

08048096 <count_chars>:

8048096:	8a 04 11	movb (%ecx,%edx,1),%al
8048099:	84 c0	testb %al,%al
804809b:	[REDACTED]	je 80480a0 <done_count_chars>
804809d:	42	incl %edx
804809e:	eb f6	jmp 8048096 <count_chars>

080480a0 <done_count_chars>:

80480a0:	b8 04 00 00 00	movl \$0x4,%eax
80480a5:	31 db	xorl %ebx,%ebx
80480a7:	43	incl %ebx
80480a8:	cd 80	int \$0x80
80480aa:	b8 04 00 00 00	movl \$0x4,%eax
80480af:	8d 0d c0 90 04 08	leal 0x80490c0,%ecx
80480b5:	31 d2	xorl %edx,%edx
80480b7:	42	incl %edx
80480b8:	cd 80	int \$0x80
80480ba:	89 ec	movl %ebp,%esp
80480bc:	5d	popl %ebp
80480bd:	c3	ret

0804808e <putstring>:

804808e: 55
804808f: 89 e5
8048091: 8b 4d 08
8048094: 31 d2

pushl %ebp
movl %esp,%ebp
movl 0x8(%ebp),%ecx
xorl %edx,%edx

08048096 <count_chars>:

8048096: 8a 04 11
8048099: 84 c0
804809b: ~~54 00~~ go go
804809d: 42
804809e: eb f6

movb (%ecx,%edx,1),%al
testb %al,%al
je 80480a0 <done_count_chars>
incl %edx
jmp 8048096 <count_chars>

nops

080480a0 <done_count_chars>:

80480a0: b8 04 00 00 00
80480a5: 31 db
80480a7: 43
80480a8: cd 80
80480aa: b8 04 00 00 00
80480af: 8d 0d c0 90 04 08
80480b5: 31 d2
80480b7: 42
80480b8: cd 80
80480ba: 89 ec
80480bc: 5d
80480bd: c3

movl \$0x4,%eax
xorl %ebx,%ebx
incl %ebx
int \$0x80
movl \$0x4,%eax
leal 0x80490c0,%ecx
xorl %edx,%edx
incl %edx
int \$0x80
movl %ebp,%esp
popl %ebp
ret

0804808e <putstring>:

804808e:	55	pushl %ebp
804808f:	89 e5	movl %esp,%ebp
8048091:	8b 4d 08	movl 0x8(%ebp),%ecx
8048094:	31 d2	xorl %edx,%edx

08048096 <count_chars>:

8048096:	8a 04 11	movb (%ecx,%edx,1),%al
8048099:	84 c0	testb %al,%al
804809b:	74 03	je 80480a0 <done_count_chars>
804809d:	42	incl %edx
804809e:	eb f6	jmp 8048096 <count_chars>

080480a0 <done_count_chars>:

80480a0:	b8 04 00 00 00	movl \$0x4,%eax
80480a5:	31 db	xorl %ebx,%ebx
80480a7:	43	incl %ebx
80480a8:	cd 00	int 0x80 \$0x00
80480aa:	b8 04 00 00 00	movl \$0x4,%eax
80480af:	8d 0d c0 90 04 08	leal 0x80490c0,%ecx
80480b5:	31 d2	xorl %edx,%edx
80480b7:	42	incl %edx
80480b8:	cd 80	int \$0x80
80480ba:	89 ec	movl %ebp,%esp
80480bc:	5d	popl %ebp
80480bd:	c3	ret

0804808e <putstring>:

804808e:	55	pushl %ebp
804808f:	89 e5	movl %esp,%ebp
8048091:	8b 4d 08	movl 0x8(%ebp),%ecx
8048094:	31 d2	xorl %edx,%edx

08048096 <count_chars>:

8048096:	8a 04 11	movb (%ecx,%edx,1),%al
8048099:	84 c0	testb %al,%al
804809b:	74 03	je 80480a0 <done_count_chars>
804809d:	42	incl %edx
804809e:	eb f6	jmp 8048096 <count_chars>

080480a0 <done_count_chars>:

80480a0:	b8 04 00 00 00	movl \$0x4,%eax
80480a5:	31 db	xorl %ebx,%ebx
80480a7:	43	incl %ebx
80480a8:	cd ff	int \$0x80 \$0x FF
80480aa:	b8 04 00 00 00	movl \$0x4,%eax
80480af:	8d 0d c0 90 04 08	leal 0x80490c0,%ecx
80480b5:	31 d2	xorl %edx,%edx
80480b7:	42	incl %edx
80480b8:	cd 80	int \$0x80
80480ba:	89 ec	movl %ebp,%esp
80480bc:	5d	popl %ebp
80480bd:	c3	ret

