64-bit Data Bus With 8-bit (BYTE) Granularity

Address Range	Active Portion of Data Bus							
ADR_I ADR_O (63..03)	DAT_I DAT_O (63..56)	DAT_I DAT_O (55..48)	DAT_I DAT_O (47..40)	DAT_I DAT_O (39..32)	DAT_I DAT_O (31..24)	DAT_I DAT_O (23..16)	DAT_I DAT_O (15..08)	DAT_I DAT_O (07..00)
Active Select Line	SEL_I(7) SEL_O(7)	SEL_I(6) SEL_O(6)	SEL_I(5) SEL_O(5)	SEL_I(4) SEL_O(4)	SEL_I(3) SEL_O(3)	SEL_I(2) SEL_O(2)	SEL_I(1) SEL_O(1)	SEL_I(0) SEL_O(0)
Ordering — Big Endian	BYTE(0)	BYTE(1)	BYTE(2)	BYTE(3)	BYTE(4)	BYTE(5)	BYTE(6)	BYTE(7)
Ordering — Little Endian	BYTE(7)	BYTE(6)	BYTE(5)	BYTE(4)	BYTE(3)	BYTE(2)	BYTE(1)	BYTE(0)

64-bit Data Bus With 16-bit (WORD) Granularity

Address Range	Active Portion of Data Bus			
ADR_I ADR_O (63..02)	DAT_I DAT_O (63..48)	DAT_I DAT_O (47..32)	DAT_I DAT_O (31..16)	DAT_I DAT_O (15..00)
Active Select Line	SEL_I(3) SEL_O(3)	SEL_I(2) SEL_O(2)	SEL_I(1) SEL_O(1)	SEL_I(0) SEL_O(0)
Ordering — Big Endian	WORD(0)	WORD(1)	WORD(2)	WORD(3)
Ordering — Little Endian	WORD(3)	WORD(2)	WORD(1)	WORD(0)

64-bit Data Bus With 32-bit (DWORD) Granularity

Address Range	Active Portion of Data Bus	
ADR_I ADR_O (63..01)	DAT_I DAT_O (63..32)	DAT_I DAT_O (31..00)
Active Select Line	SEL_I(1) SEL_O(1)	SEL_I(0) SEL_O(0)
Ordering — Big Endian	DWORD(0)	DWORD(1)
Ordering — Little Endian	DWORD(1)	DWORD(0)

64-bit Data Bus With 64-bit (QWORD) Granularity

Address Range	Active Portion of Data Bus
ADR_I ADR_O (63..00)	DAT_I DAT_O (63..00)
Active Select Line	SEL_I(0) SEL_O(0)
Ordering — Big Endian	QWORD(0)
Ordering — Little Endian	QWORD(0)