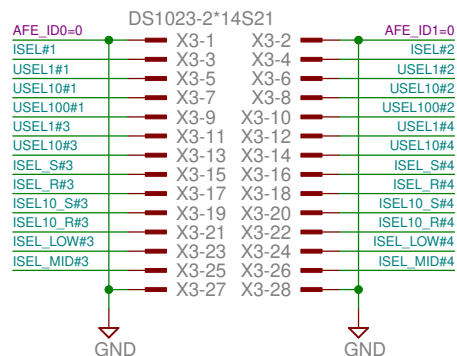
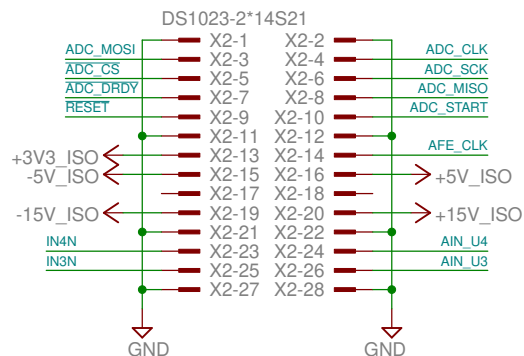
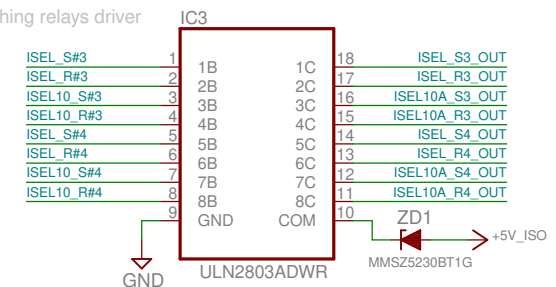


Voltage mode:  $\pm 2.4\text{ V}$ ,  $\pm 48\text{ V}$ ,  $\pm 240\text{ V}$   
 Current mode:  $\pm 48\text{ mA}$  (gain x1)

#### AFE connectors



#### Dual coil latching relays driver



#### AFE channel #1 (High voltage)

TITLE: EEZ DIB AFE1 r1B3

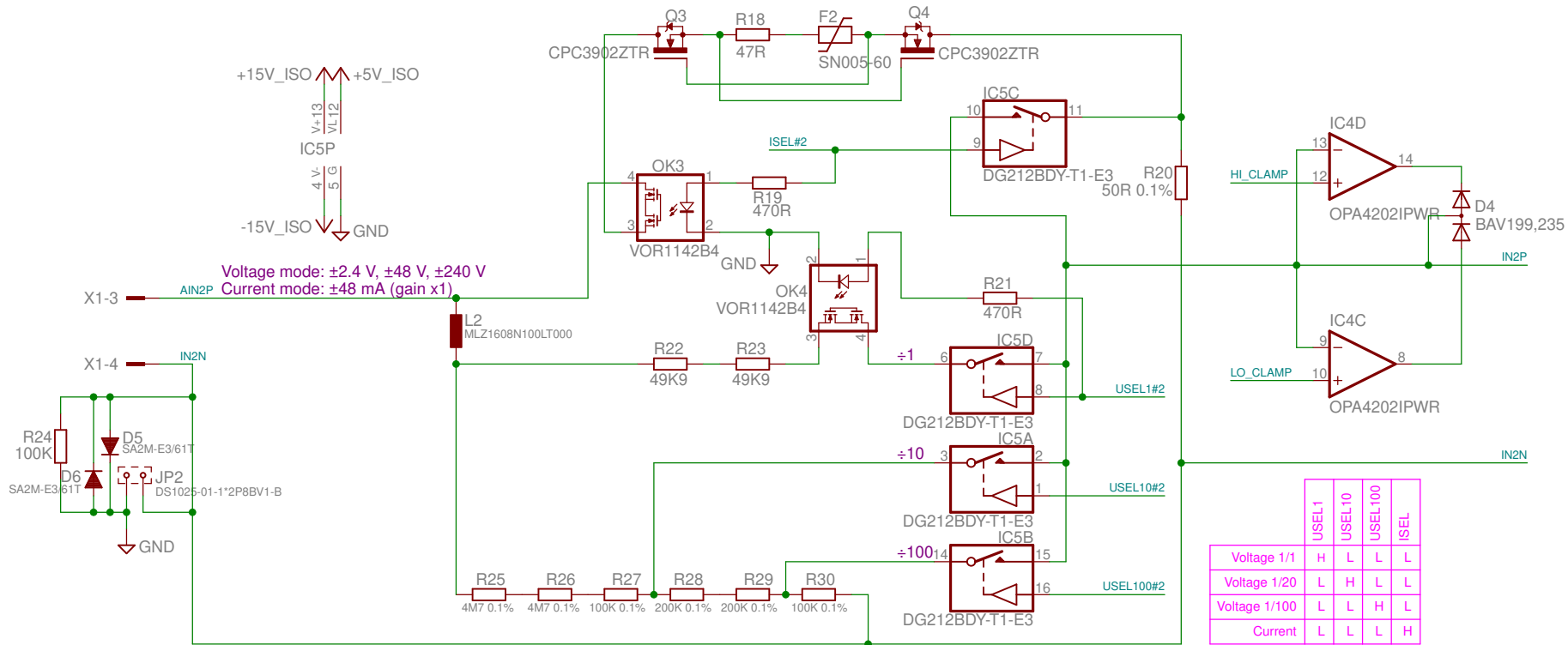
Document Number:

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Date: 24.1.2021. 14:46

Sheet: 1/5





AFE channel #2 (High voltage)

TITLE: EEZ DIB AFE1 r1B3

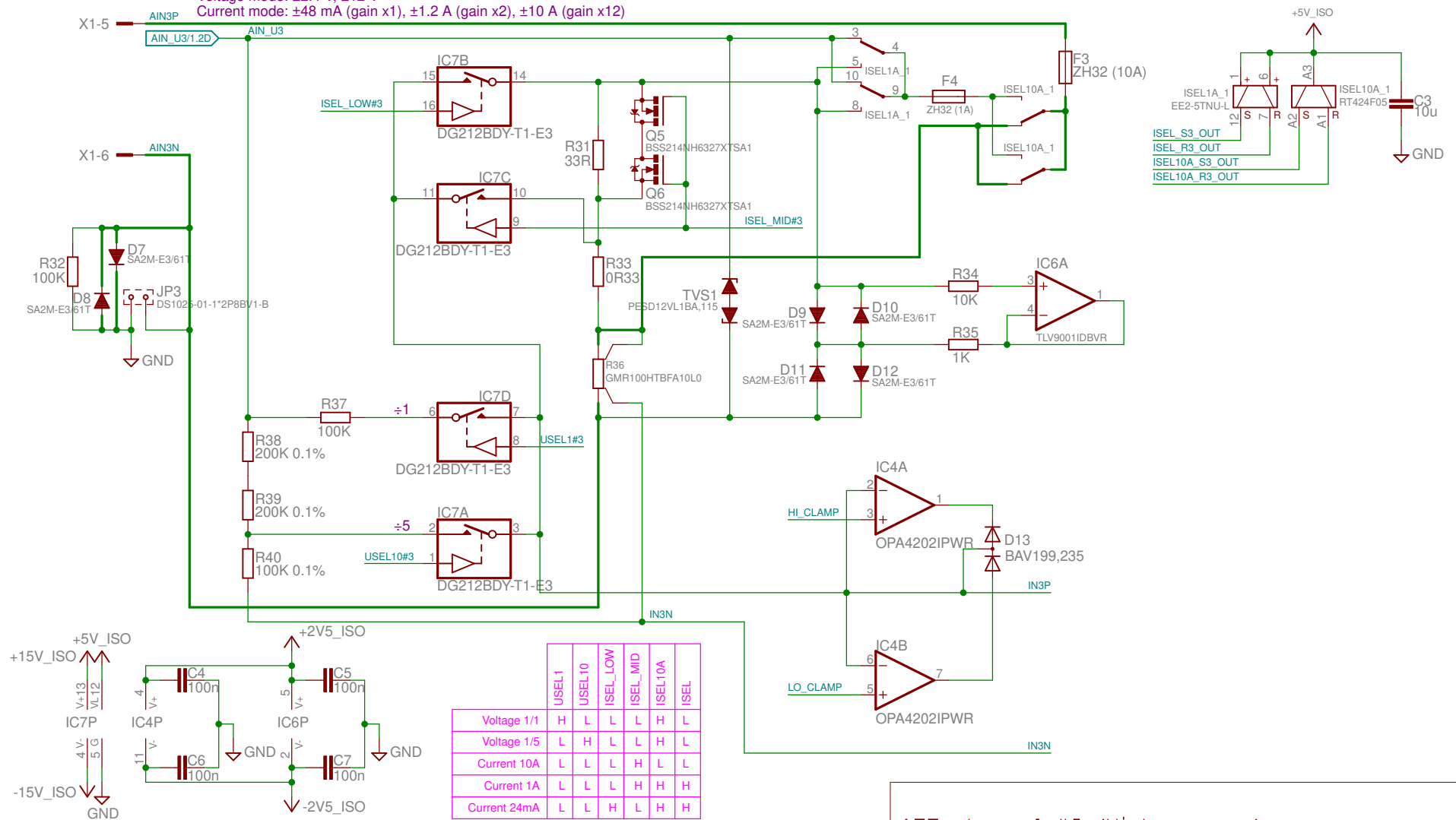
Document Number:

REV:

Date: 24.1.2021. 14:46

Sheet: 2/5

Voltage mode:  $\pm 2.4\text{ V}$ ,  $\pm 12\text{ V}$   
 Current mode:  $\pm 48\text{ mA}$  (gain x1),  $\pm 1.2\text{ A}$  (gain x2),  $\pm 10\text{ A}$  (gain x12)



AFE channel #3 (High current)

TITLE: EEZ DIB AFE1 r1B3

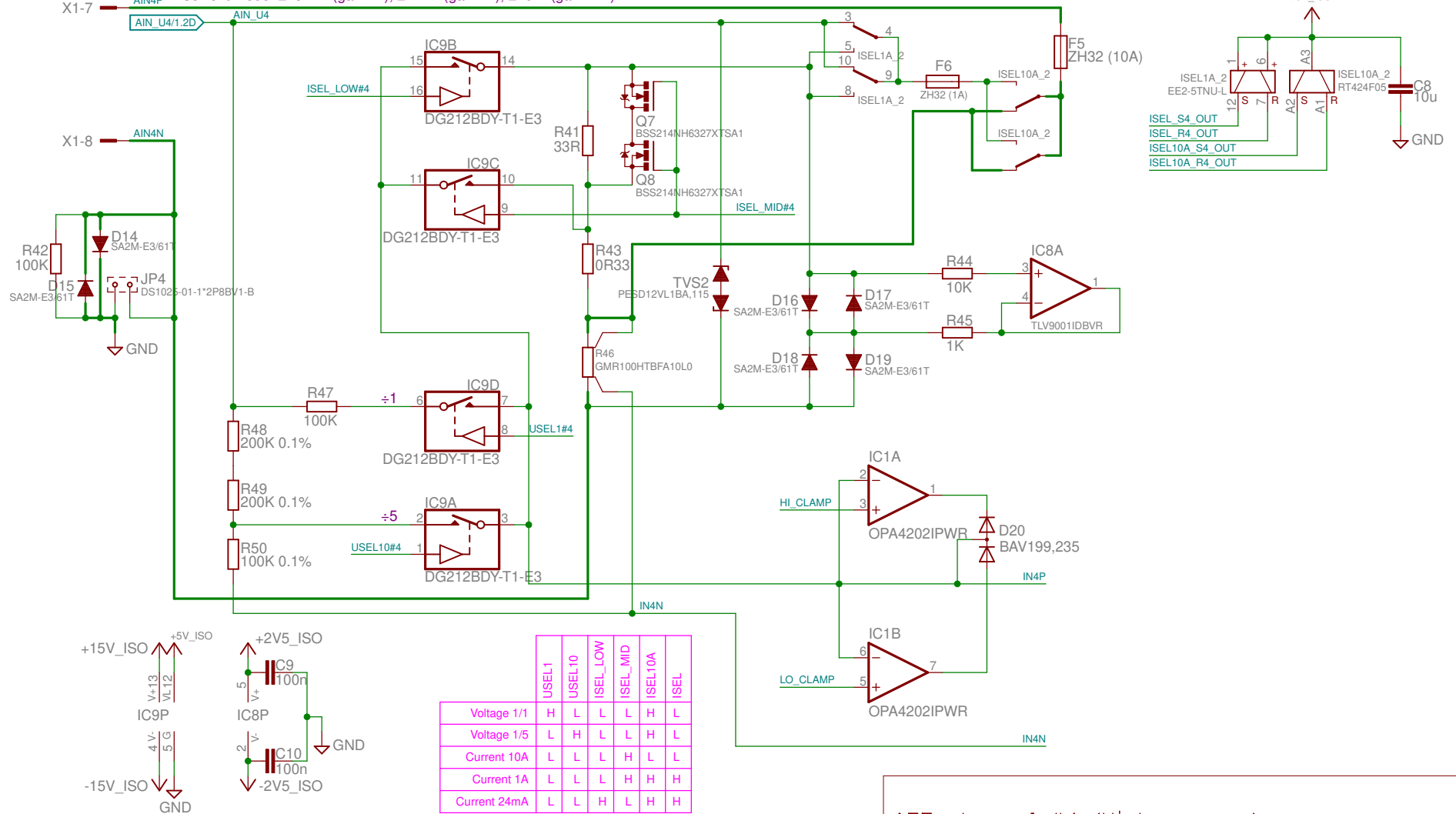
Document Number:

REV:

Date: 24.1.2021. 14:46

Sheet: 3/5

Voltage mode:  $\pm 2.4\text{ V}$ ,  $\pm 12\text{ V}$   
 Current mode:  $\pm 48\text{ mA}$  (gain x1),  $\pm 1.2\text{ A}$  (gain x2),  $\pm 10\text{ A}$  (gain x12)



AFE channel #4 (High current)

TITLE: EEZ DIB AFE1 r1B3

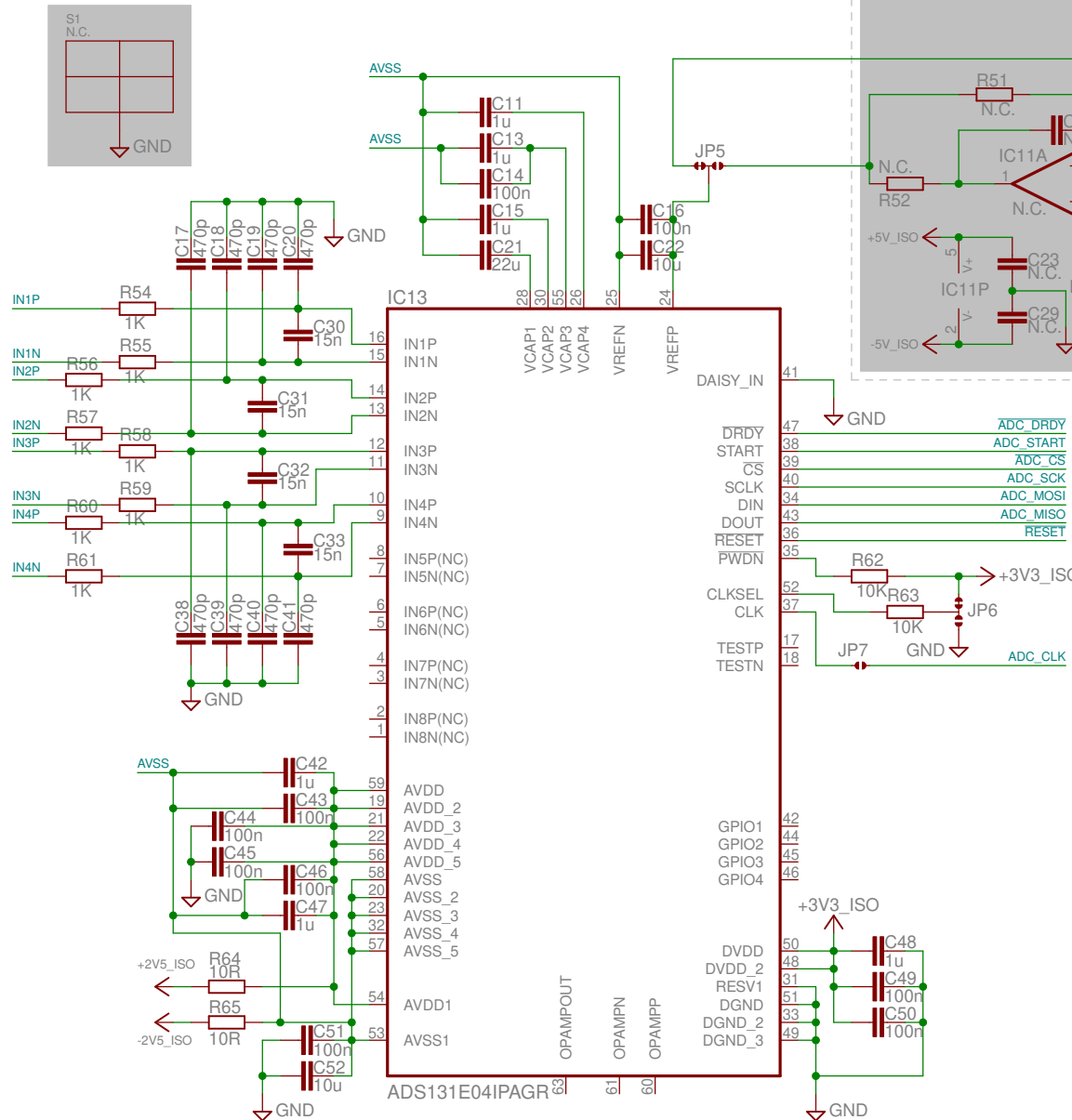
Document Number:

REV:

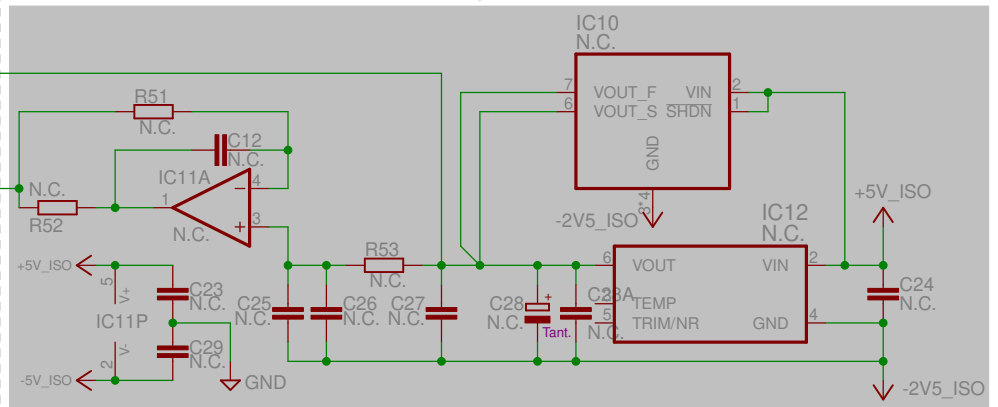
Date: 24.1.2021. 14:46

Sheet: 4/5

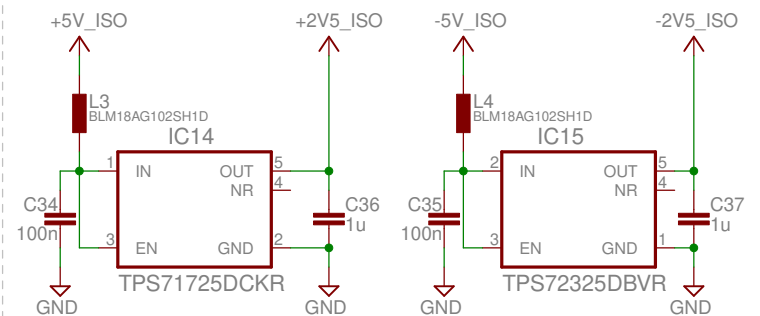
# 4-ch 24-bit ADC



## Vref buffer (optional)



## Isolated +/-2.5 V LDOs



4-ch 24-bit simultaneously sampling ADC  
Voltage reference, +/-2.5V LDOs

TITLE: EEZ DIB AFE1 r1B3

Document Number:

REV:

Date: 24.1.2021. 14:46

Sheet: 5/5