FlowPools

A Lock-Free Deterministic Concurrent Dataflow Abstraction

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LAMP, EPFL

September 12, 2012

Scala



- Functional
- Object-Oriented
- Runs on JVM







theguardian



Outline

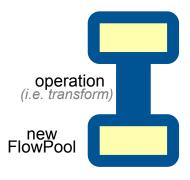
What is a FlowPool

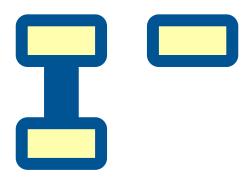
Implementation

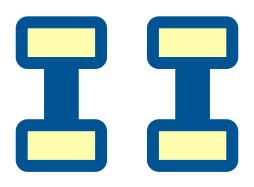
Multi-Lane FlowPools

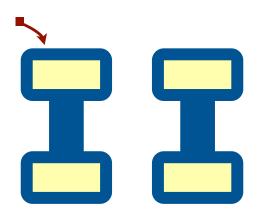
Benchmarks

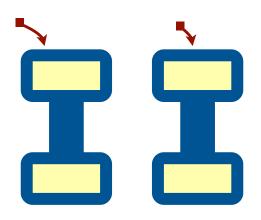


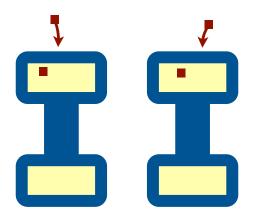


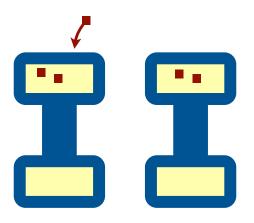


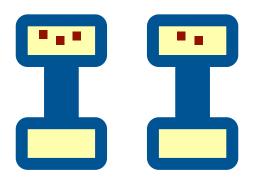


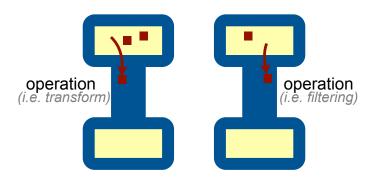


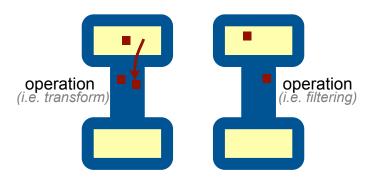


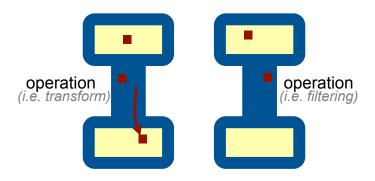


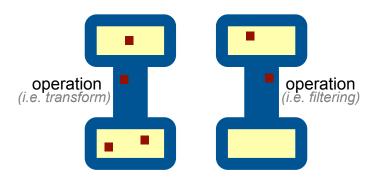


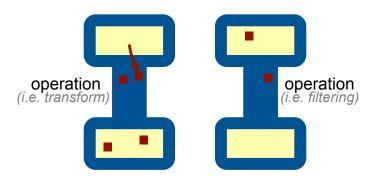


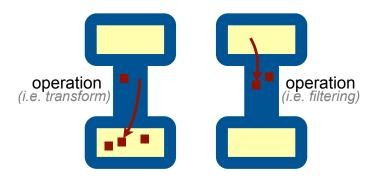


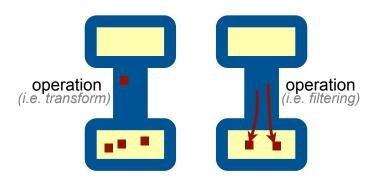


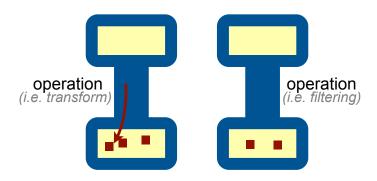


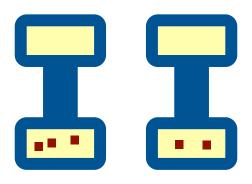


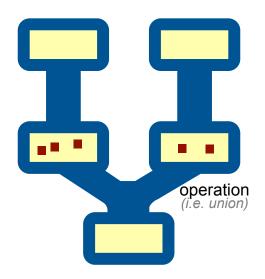


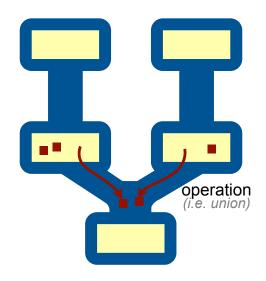


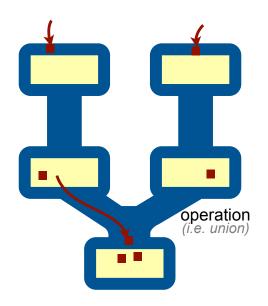


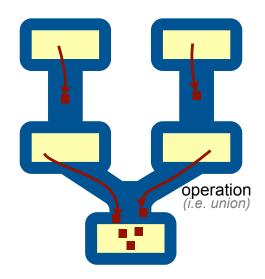


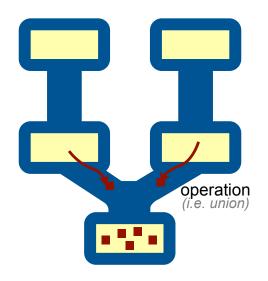


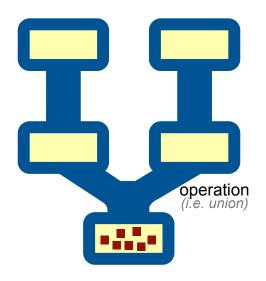












Big Picture

Pool: concurrent collections abstraction for Scala

FlowPool Properties

- Pool semantics
- Asynchronous
- Deterministic
- ► Lock-Free
- Garbage collection of unused elements

Pool semantics

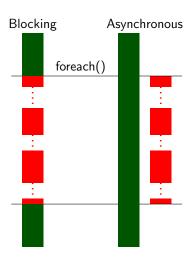
- Unordered
- Multiple occurrences
- ► Insertion (<<)
- Traversal (foreach)

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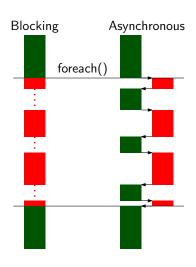


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Determinism

Every execution of a given program with given input eventually

Always reaches same state

or

Always fails

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Lock-Freedom

At least one operation will complete in a finite number of steps.

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Programming Model - Elementary Operations

- ► Insertion (<<)
- Traversal (foreach)
- ► Sealing (seal(n))
- ► Aggregation (aggregate)

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```
Thread 1: Thread 2: p << x p.seal()
```

- Insertion (<<)</p>
- Traversal (foreach)
- Sealing (seal(n))
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```
Thread 1: Thread 2: p << x p.seal(1)
```

```
▶ Insertion (<<)
  Traversal (foreach)
  Sealing (seal(n))
  Aggregation (aggregate)
def fill(n: Int, el: T) {
  p = new FlowPool[T]
  for (i = 1 to n) { p << el }
  p.seal(n)
  return p
```

- ► Insertion (<<)
- Traversal (foreach)
- Sealing (seal(n))
- Aggregation (aggregate)

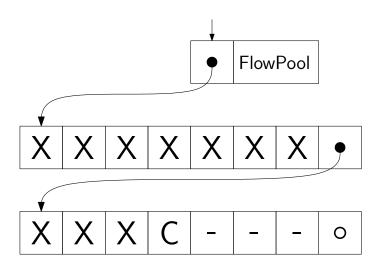
```
def count(cond) =
  this.aggregate(0)(_ + _) { x =>
   if (cond(x)) cnt + 1
   else cnt
}
```

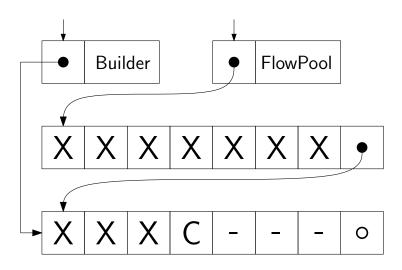


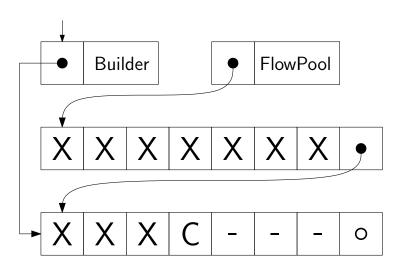
```
▶ Insertion (<<)
  Traversal (foreach)
  Sealing (seal(n))
  Aggregation (aggregate)
def foreach[U](f: T => U): Future[Unit] = {
  aggregate(())((_, _) => ()) {
    (acc, x) \Rightarrow f(x); ()
```

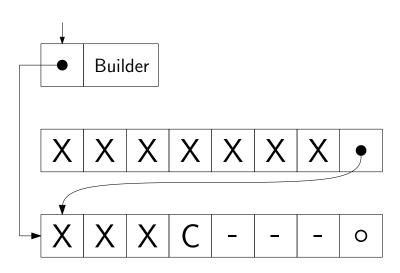
Programming Model - Higher Level Operations

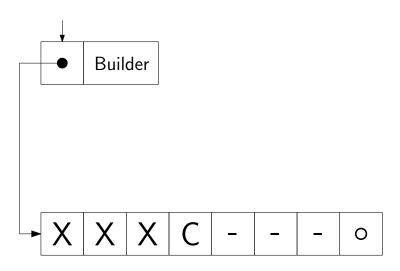
- ▶ Union (++)
- ▶ filter
- ► Transformation (map, flatMap)
- Reduction (fold)
- ► Generation (fill, iterate)

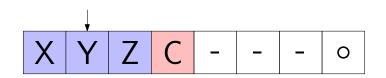




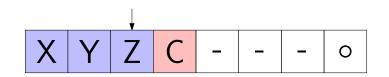




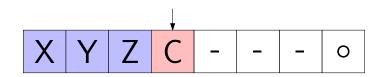




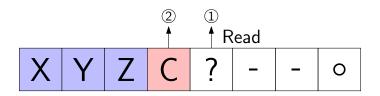
- 1. next = block(i+1)
- 2. curo = block(i)
- 3. CAS(block(i+1), next, curo)
- 4. CAS(block(i), curo, W)
- 5. invokeCallbacks(W, curo



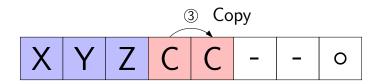
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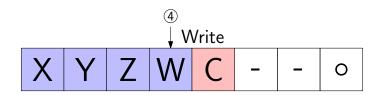
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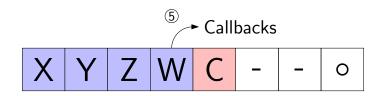
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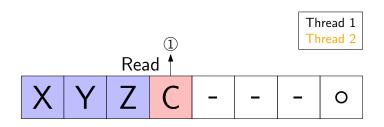
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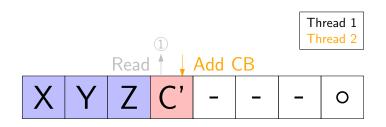
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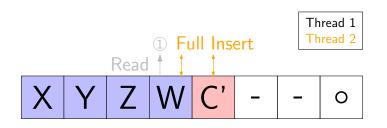
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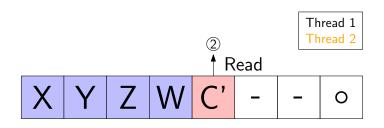
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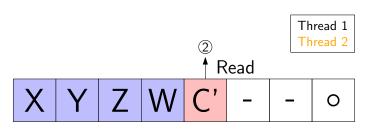


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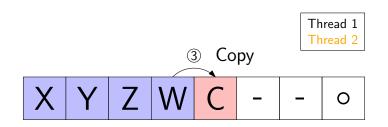
Wrong Insert <<(x: T)



Observed State (inconsistent)

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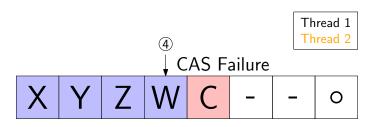


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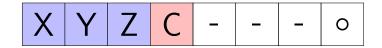
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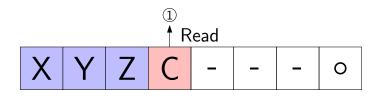
Seal seal(n: Int)



```
1. cbs = block(i)
s = Seal(sealsize, cbs)
```

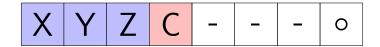
2. CAS(block(i), cbs, s)

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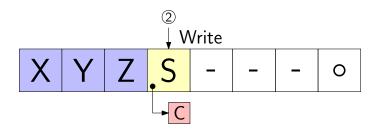
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Multi-Lane FlowPools

Single-Lane FlowPools: Issues

- Bad scaling (insertions)
 - CAS failures
 - ► Cache contention

Solution

- Use unordered property
- Extend to multiple lanes
- Scales nicely
- ▶ BUT: Seal is complex

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CPU-Scaling – Insertions

CPU-Scaling - Insert & Map

CPU-Scaling - Insert & Reduce

CPU-Scaling - Communication/Garbage Collection

Conclusion

FlowPools are . . .

Basic Properties

- ► Flow-Based Collection
- Asynchronous
- Deterministic

Performance & Scalability

- ► Speed comparable to Java standard queues
- Scalable
- Composable
- Unneeded elements garbage collected

¿ Questions?

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¿Questions?

Details about Benchmarks

Insert / Reduce

- ▶ $5 \cdot 10^6$ elements
- ▶ 20 measurements

Communication / GC

- ▶ Parallelization level: 1
- Measurements
 - ▶ UltraSPARC T2: 2
 - ▶ 4-core i7: 10
 - 32-core Xeon: 3

Java Command

- -Xmx2048m -Xms2048m
- -XX:+UseCondCardMark
- -verbose:gc
- -XX:+PrintGCDetails -server.

Java Version

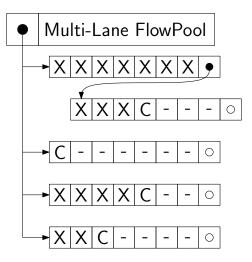
- ► Intel 1.7.0_04-ea-b15 HotSpot 64-Bit Server VM (build 23.0-b16, mixed mode)
- ► SPARC 1.7.0_03-b04 HotSpot Server VM (build 22.1-b02, mixed mode)

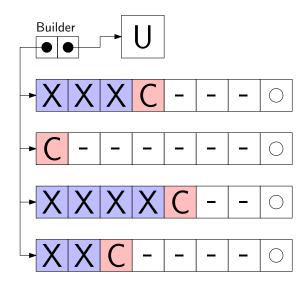
Details about Benchmarks

Architectures

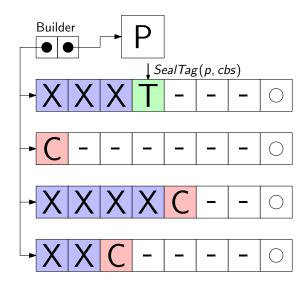
- octa-core 1.2GHz UltraSPARC T2 w/ 64 HW threads
- quad-core 3.4 GHz i7-2600 (w/ HT)
- 4x octa-core 2.27 GHz Intel Xeon x7560 (w/ HT)

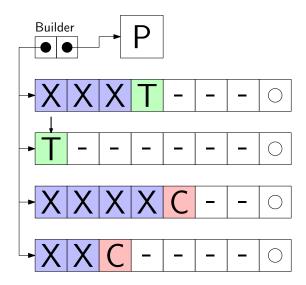
Basic Structure

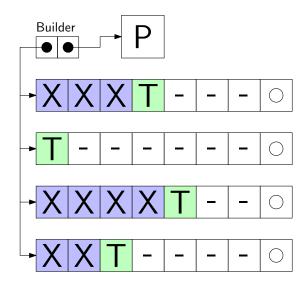




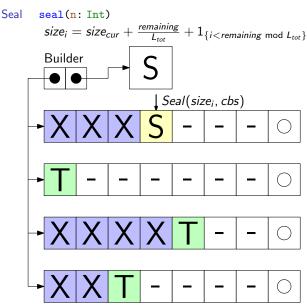
Seal seal(n: Int) Proposition(size) Builder -XXXXXC

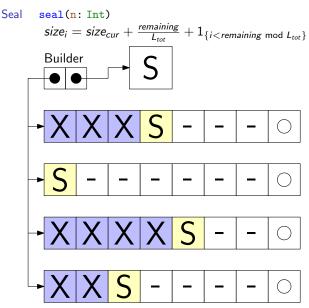


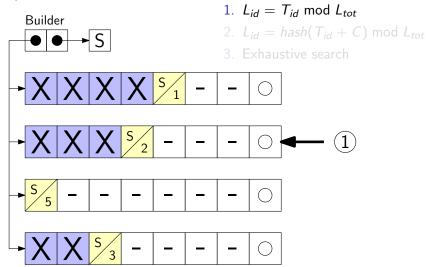


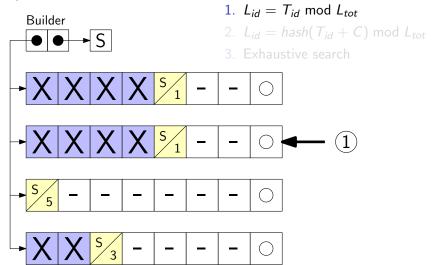


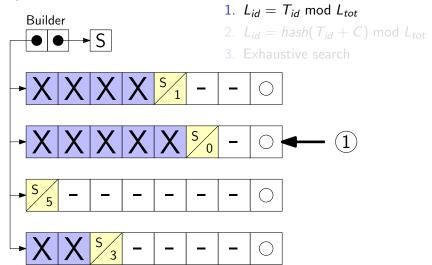
Seal seal(n: Int) Seal(size, remaining) Builder -XXXXX

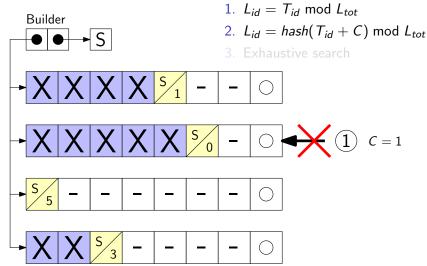


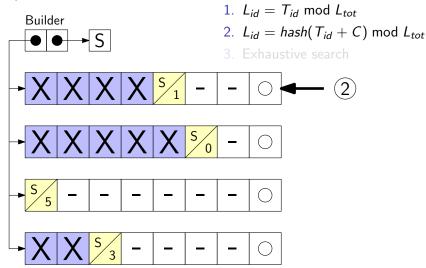


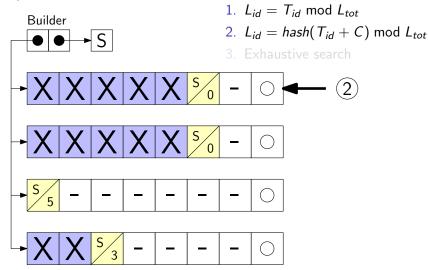


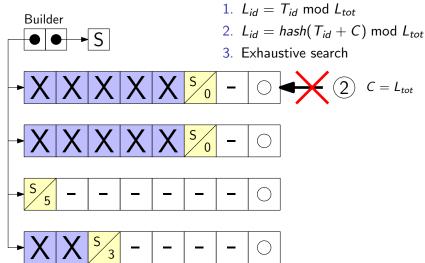


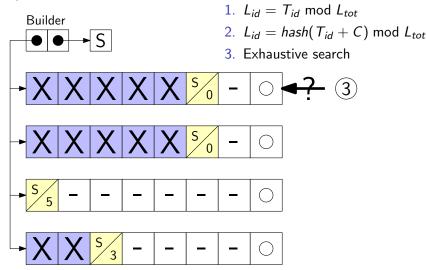


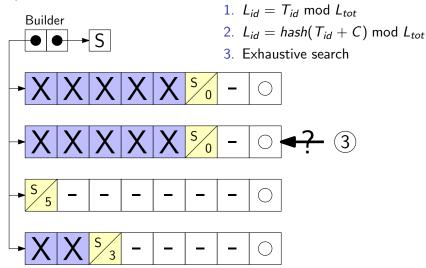


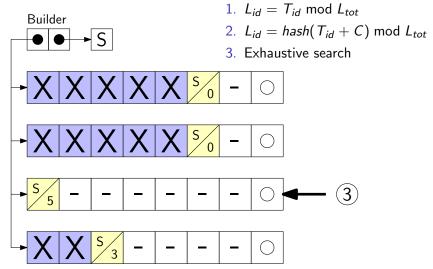


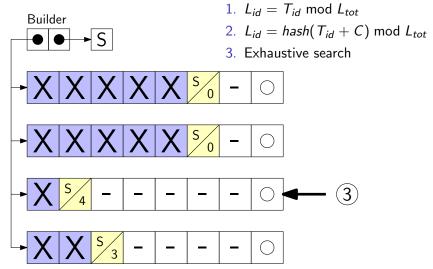












Hash Function

Byte-swap Hashing

$$hash(x) = rb(x \cdot 9e3775cd_{16}) \cdot 9e3775cd_{16}$$

Efficiency of Hashing

imes UltraSPARC T2, riangle 4-core i7