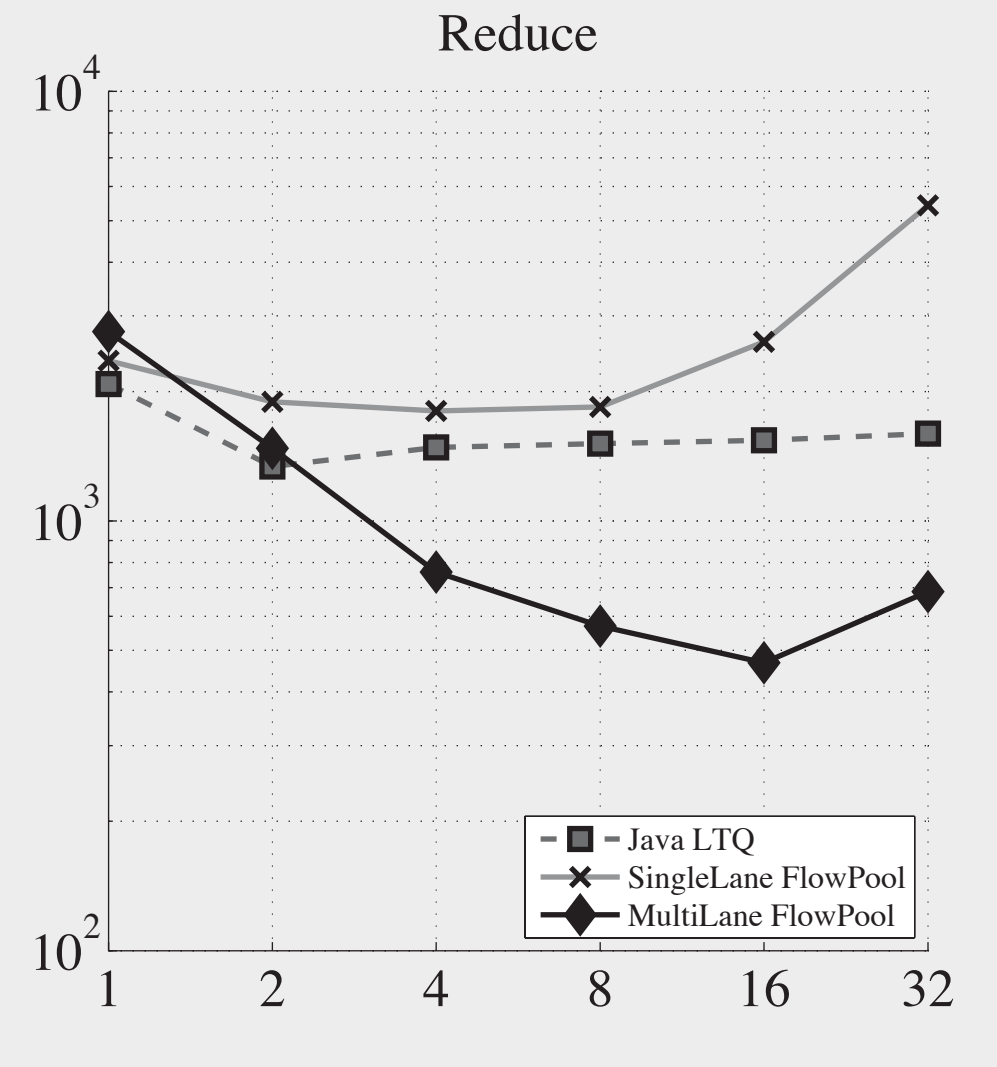
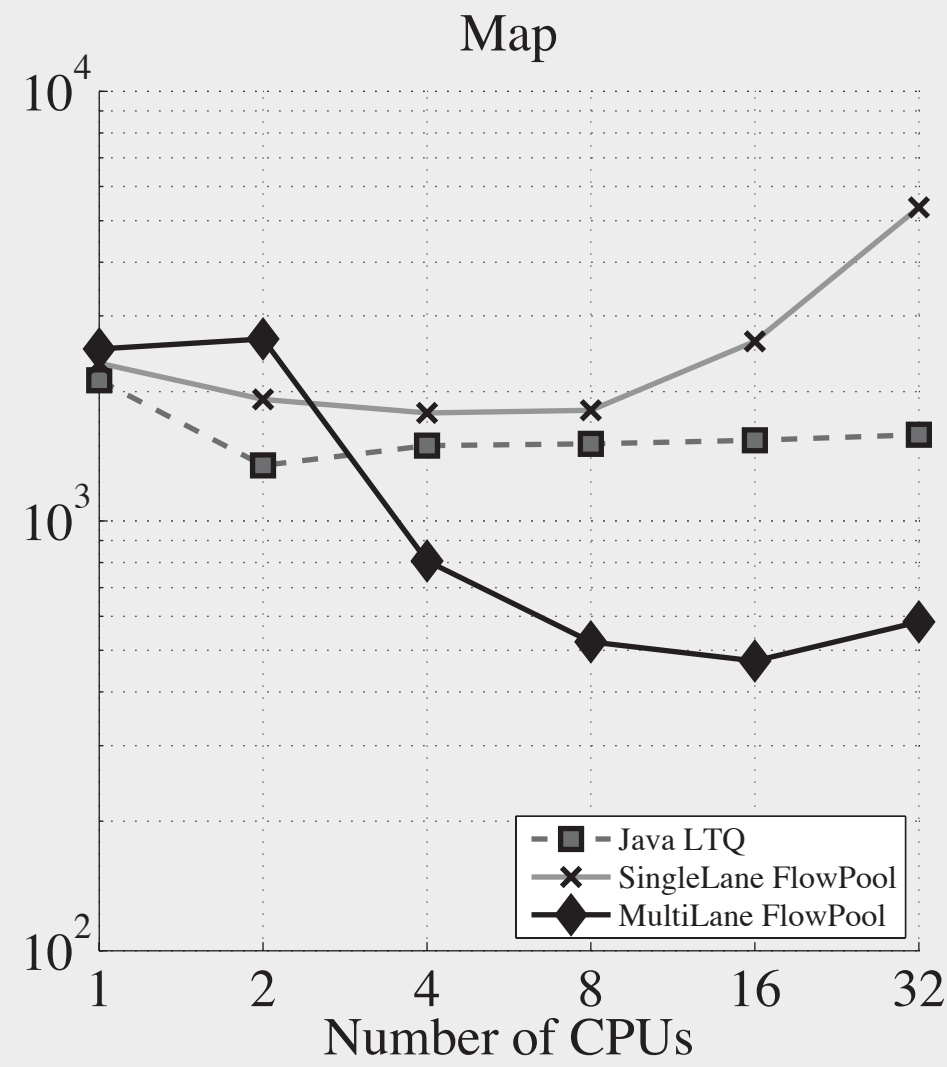
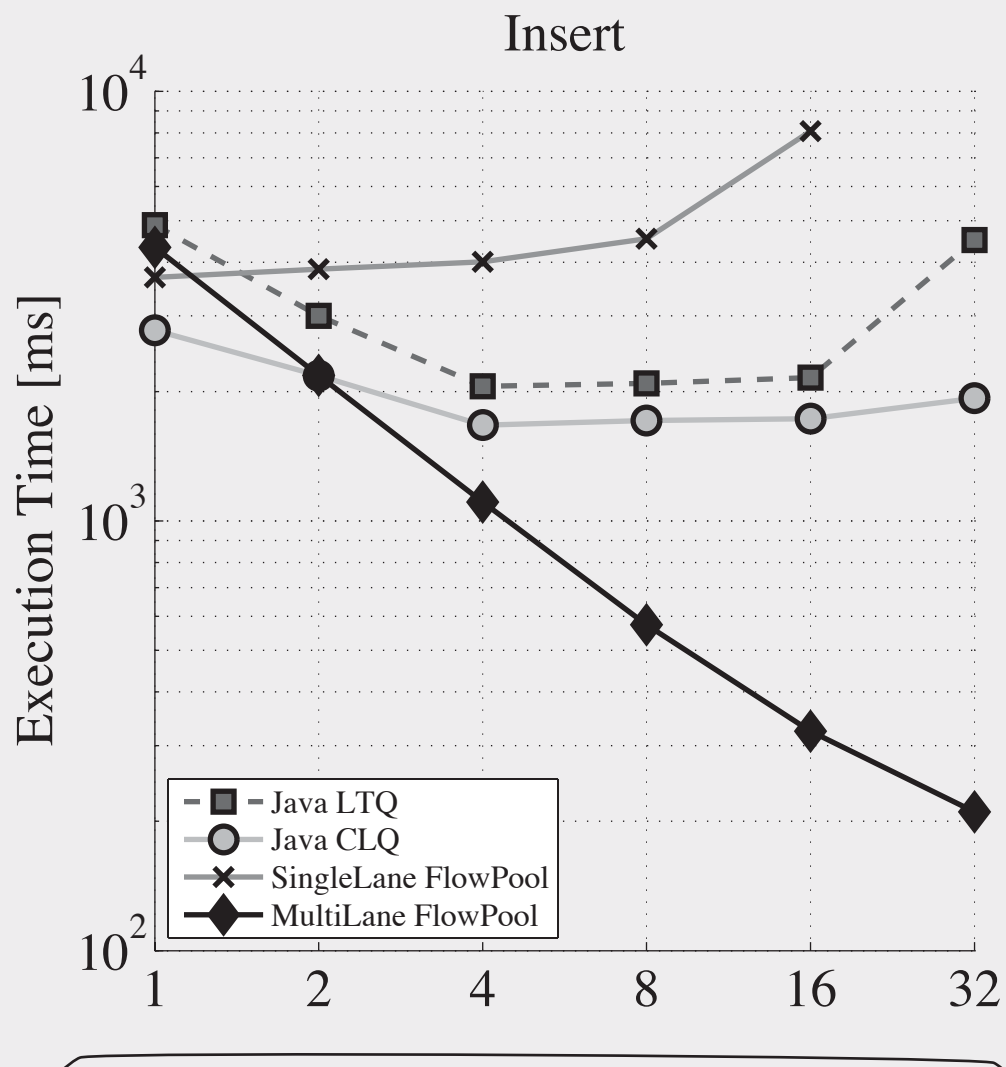
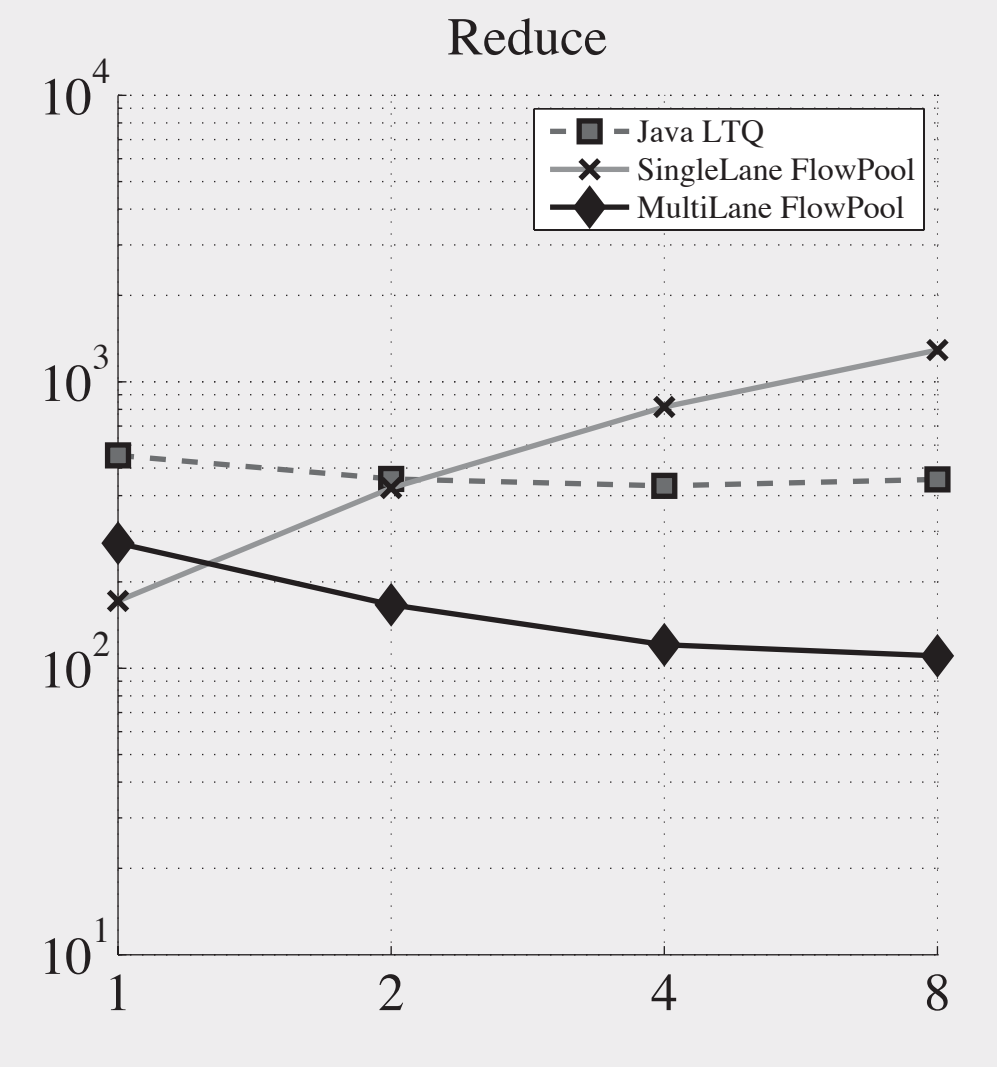
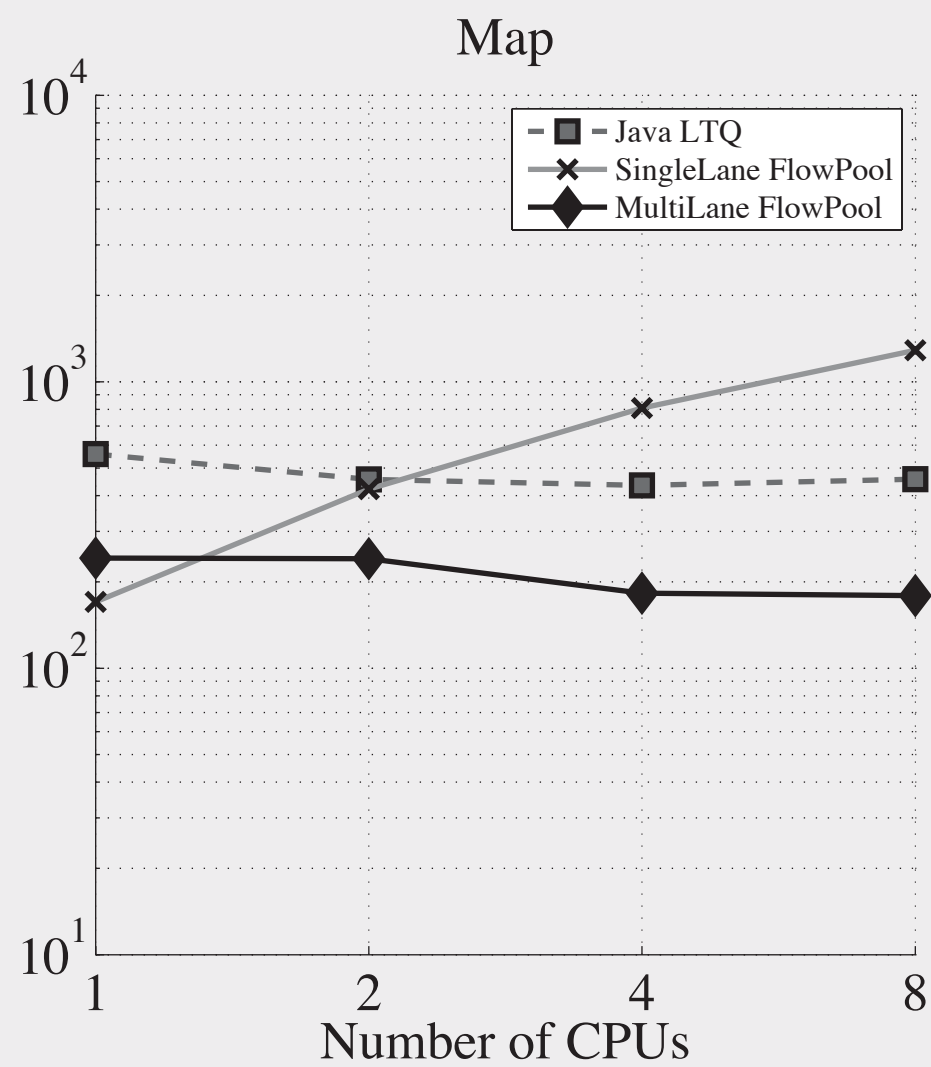
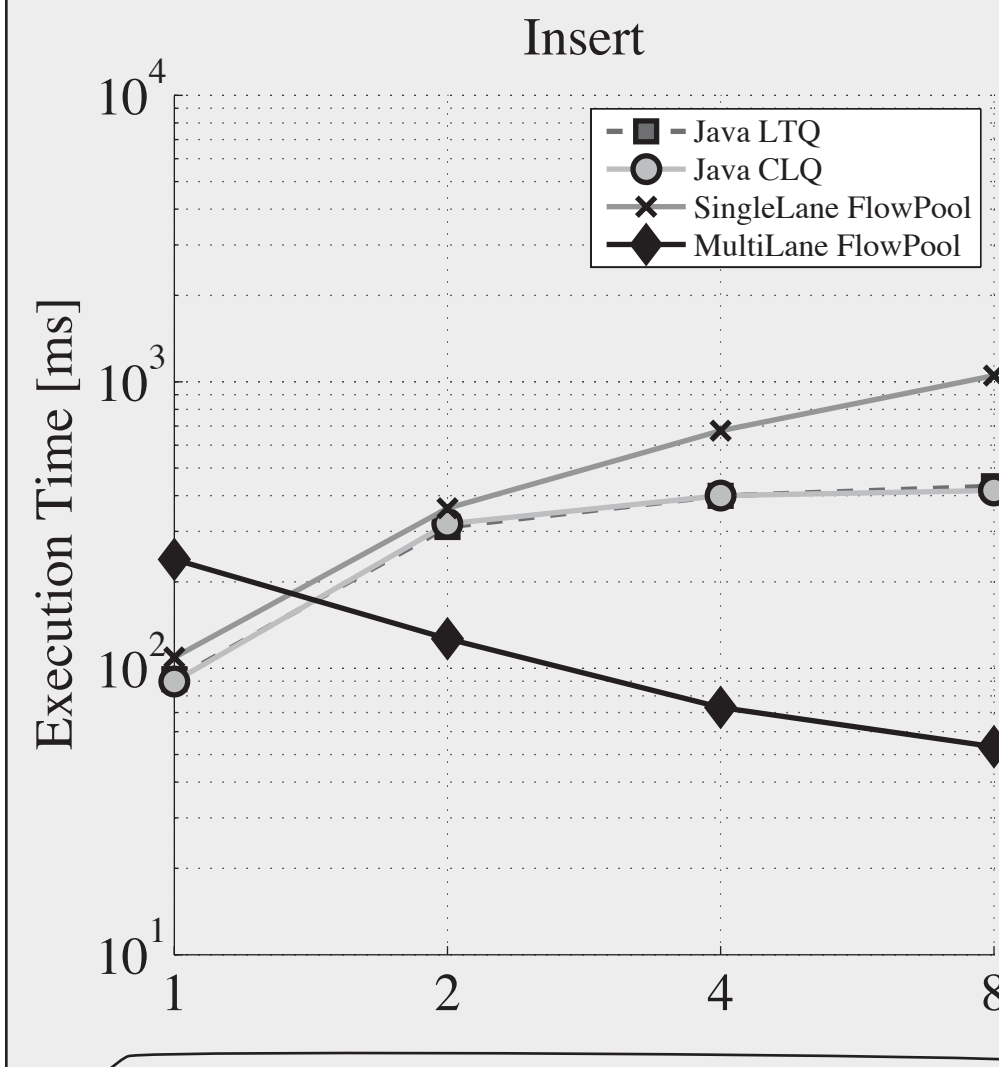


Operations on FlowPools Across Architectures

UltraSPARC T2 Architecture



Intel 4-core i7 Architecture



Intel 32-core Xeon Architecture

