

ModelSim PE Student Edition 10.4a

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100 ns

LayoutSimulate

ColumnLayoutDefault

sim - Default

Instance	Design unit	Design unit type	Top Category
AII_eval_tb	AII_eval_tb	Module	DU Instance
#INITIAL#12	AII_eval_tb	Process	-
#ysim_capacity#	AII_eval_tb	Capacity	Statistics

Objects

Name	Value	Kind	Mode
o	16'hfbff	Net	Internal
zr	1h0	Net	Internal
ng	1h1	Net	Internal
x	16'h03ff	Pack...	Internal
y	16'h07ff	Pack...	Internal
f	7h29	Pack...	Internal

Processes (Active)

Name	Type (filtered)	State	Order	Parent Path
#INITIAL#12	Initial	Active	1	/AII_eval_tb

Wave - Default

Signal	Value	Time
/AII_eval_tb/o	16'hfbff	16... 16...
/AII_eval_tb/zr	1h0	16... 16...
/AII_eval_tb/ng	1h1	16... 16...
/AII_eval_tb/x	16'h03ff	16... 16...
/AII_eval_tb/y	16'h07ff	16... 16...
/AII_eval_tb/f	7h29	7h2d 7h61 7h1c 7h64 7h04 7h06 7h25 7h00 7h29

Transcript

```
# I 1023 | 2047 | 11 | 11 | 10 | 10 | 10 | 10 | 11 | -2048 | (1'y ) | 10 | 11 |
# I 1023 | 2047 | 10 | 10 | 11 | 11 | 11 | 10 | 10 | 3070 | (x-1) | 10 | 10 |
# I 1023 | 2047 | 11 | 11 | 10 | 10 | 11 | 10 | 10 | 2046 | (y-1) | 10 | 10 |
# I 1023 | 2047 | 10 | 10 | 10 | 10 | 11 | 10 | 10 | 3070 | (x+y) | 10 | 10 |
# I 1023 | 2047 | 10 | 10 | 10 | 10 | 11 | 11 | 1151 | (x+128) | 10 | 10 |
```

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3:48 PM 23-Nov-20