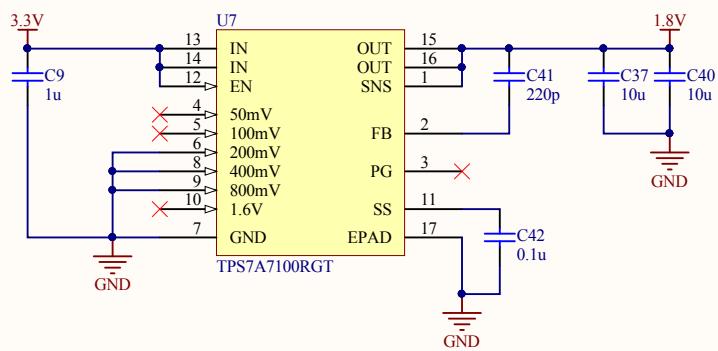
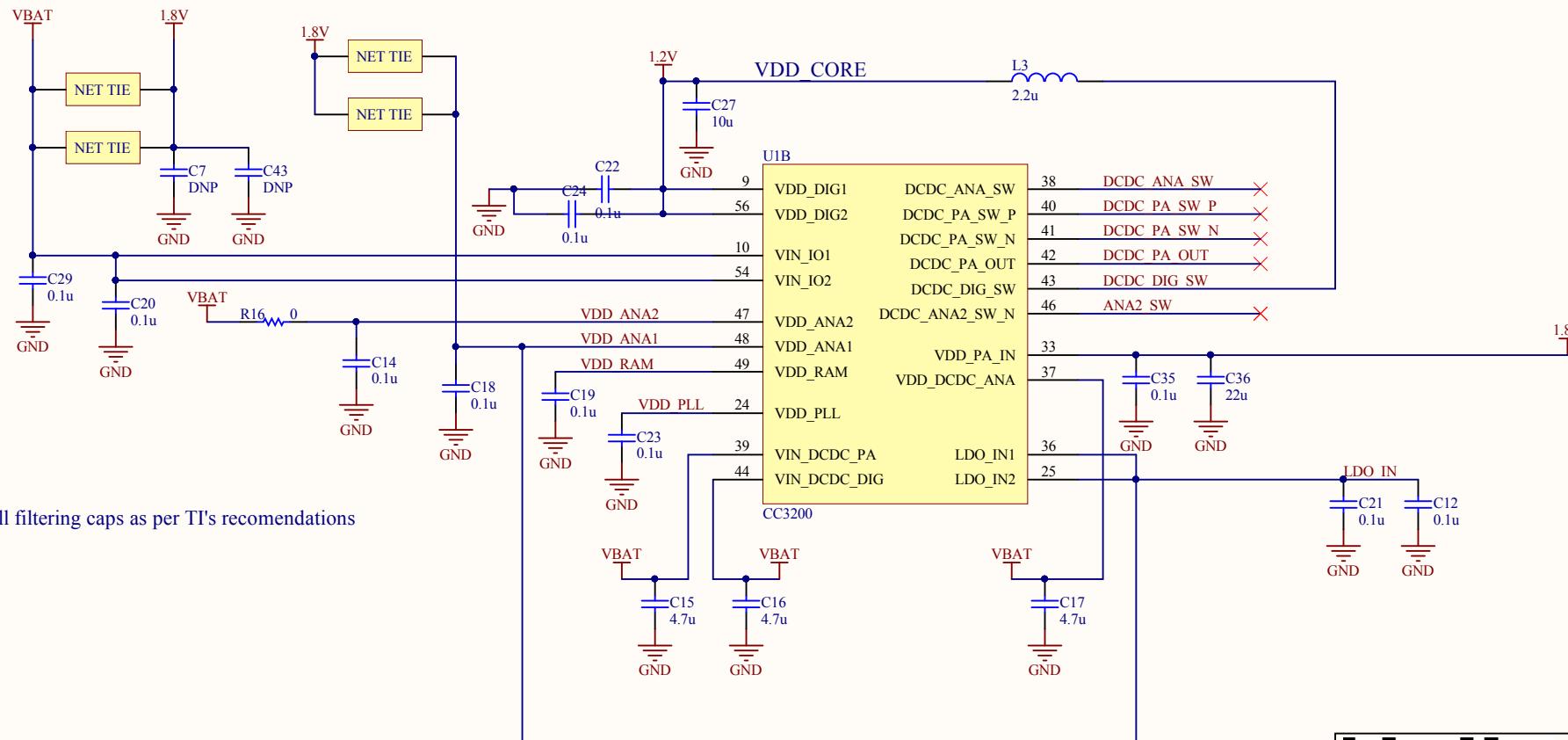
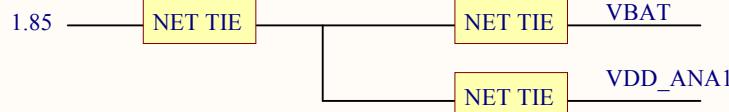


## LDO for 1.9V



## Power scheme only for reference



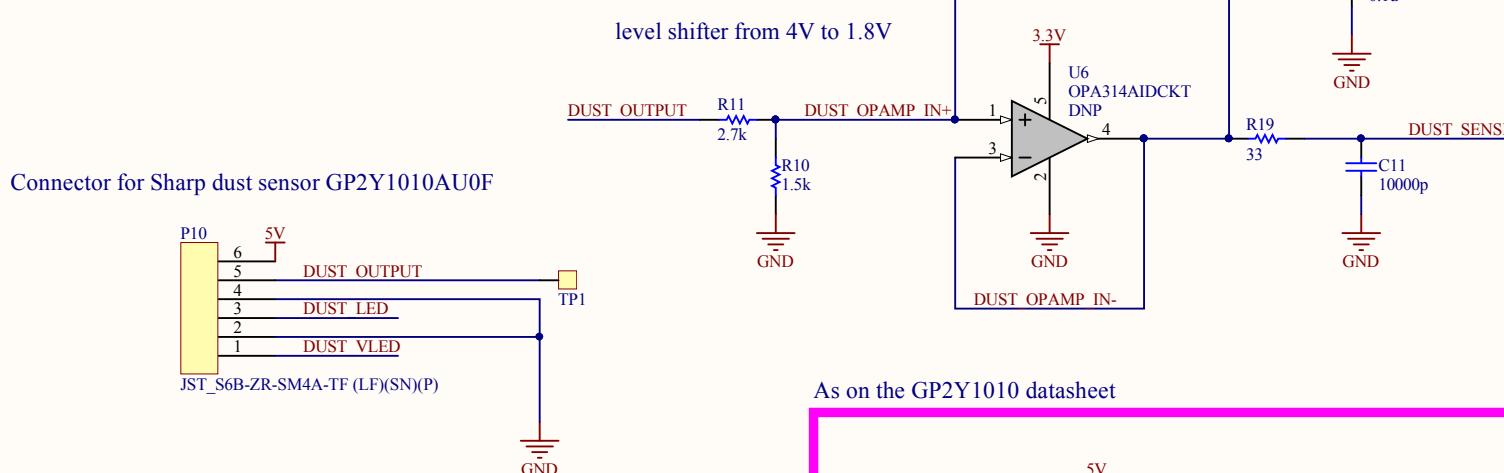
All filtering caps as per TI's recommendations

Route ground traces for C15, C16 and C17 to single vias on the EPAD. Isolate them from pours in inner layers

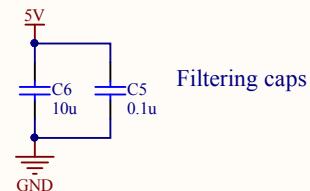
Hello

TITLE	M4F MCU Power	REV
DATE	12/18/15	DRAWN BY
	H. Asgarian	Proto

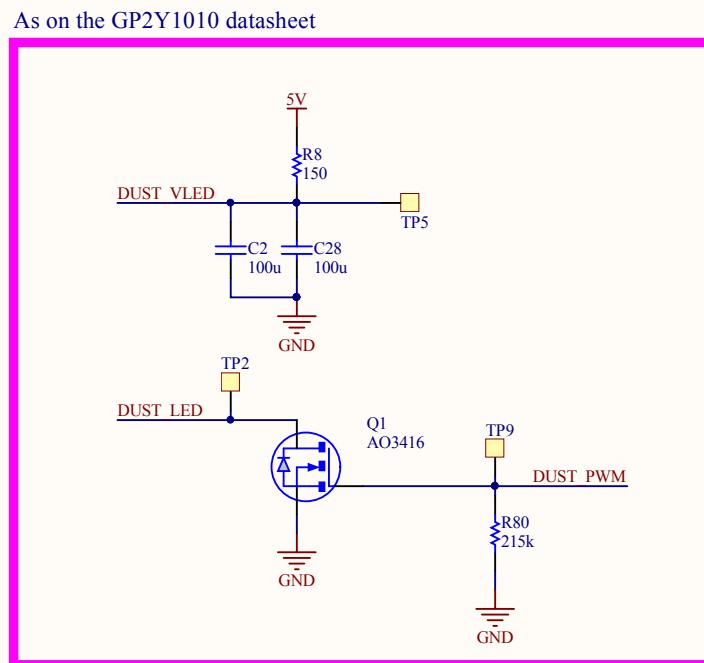
A



B



C



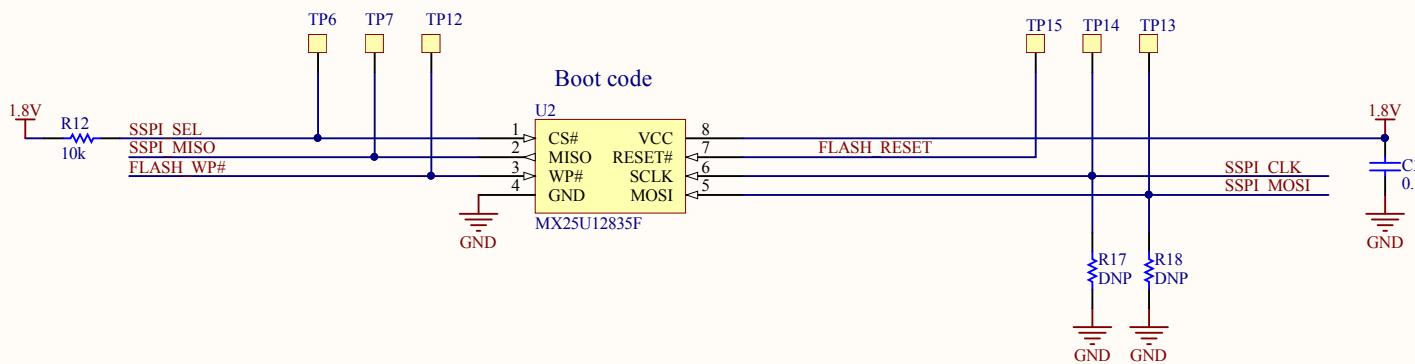
D

A

A

B

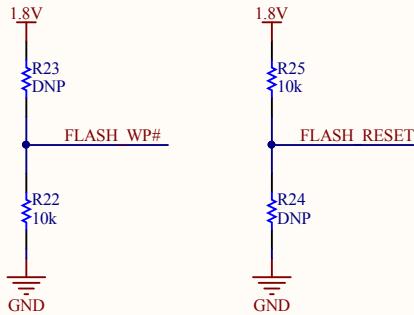
B



TI wants 17 and R18 to be installed but we've always been good without

C

C



D

D

Datasheet is unclear, so for now we pull reset high and WP low



A

A

B

B

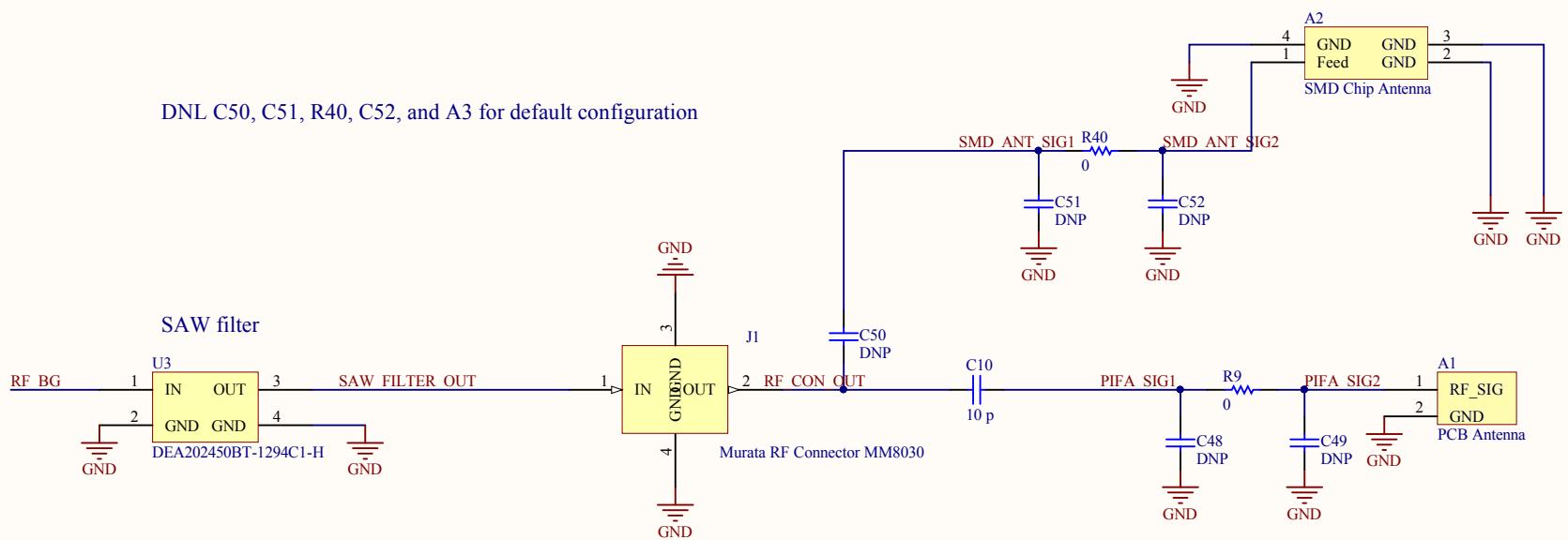
C

C

D

D

DNL C50, C51, R40, C52, and A3 for default configuration



Murata 0201 GJM0335C1ER20WB01

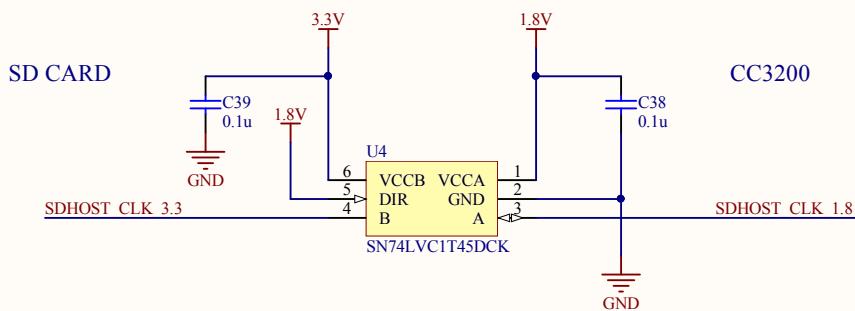
GJM series is better than GRM for RF



A

A

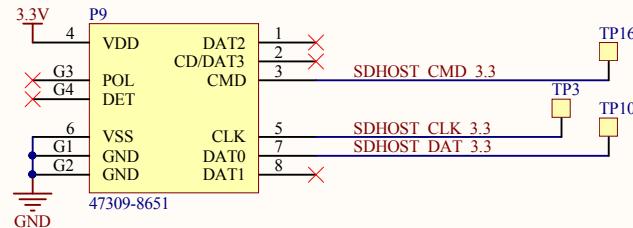
CLK needs a dedicated level shifter because of speed



B

B

CMD and DAT shifter on dedicated level shifter



SD Card connector

D

D



