

FA - 3

Total points 38/60 ?

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✓ How the processor enter into real mode? *

2/2

- ☐ By setting PE bit
- ☐ By setting VM bit
- ☐ By setting PG bit
- ☒ on Reset



✗ How many total no. of previlage levels? *

0/2

- ☐ 1
- ☐ 2
- ☒ 3
- ☐ 4



✓ Which of the following flag bit is used to check I/O sensitive instructions? * 2/2

- ☐ PG
- ☐ PE
- ☒ IOPL
- ☐ TI



✓ How many PTE entries in page table of 80386DX? * 2/2

- ☒ 1024
- ☐ 1000
- ☐ 8192
- ☐ 4096



Roll No *

22

✗ What are the contents of TR register? * 0/2

- ☒ Selector
- ☐ Task ID
- ☐ No. of tasks
- ☐ Base Address
- ☐ Limit



✓ Which of the bit is used to enable Protected mode? *

2/2

- ☒ PE
- ☐ PG
- ☐ MP
- ☐ VM



✓ Which of the following bit of Segment descriptor is used to keep track of no. of times segment accessed? *2/2

- ☐ P
- ☐ G
- ☒ A
- ☐ D



✗ How many total no. of segments present in virtual address space? *

0/2

- ☒ 8192
- ☐ 16384
- ☐ 1024
- ☐ 65536



✗ What is the size of segment descriptor in 80386DX processor? *

0/2

- ☐ 16 bits
- ☐ 64 bits
- ☐ 48 bits
- ☒ 32 bits

✗

✗ What is the size of page field in linear address? *

0/2

- ☐ 12 bits
- ☒ 32 bits
- ☐ 10 bits
- ☐ 13 bits

✗

Class *

- ☒ TE A
- ☐ TE B
- ☐ TE C



✓ What is the size of PDBR? *

2/2

- ☐ 32 bits
- ☐ 16 bits
- ☒ 20 bits
- ☐ 8 bits



Name *

Mahima Churi

✓ What is the size of segment register in protected mode? *

2/2

- ☐ 32 bits
- ☒ 16 bits
- ☐ 8 bits
- ☐ 48 bits



✓ Which of the following flag bit is used to set Virtual Mode of 80386DX?

2/2

- ☒ VM
- ☐ PE
- ☐ IOPL
- ☐ RF



✗ Does virtual mode handle interrupts? *

0/2

- ☒ Yes
- ☐ No
- ☐ Maybe



✓ Which of the following bit is used for nested task? *

2/2

- ☒ NT
- ☐ RF
- ☐ TS
- ☐ PE



✓ Which of the following bit is used to check co-processor present or not in 80386DX? *2/2

- ☒ MP
- ☐ EM
- ☐ PE
- ☐ RF



✗ What is the size of virtual address in 80386 DX? *

0/2

- ☒ 32 bits
- ☐ 46 bits
- ☐ 48 bits
- ☐ 16 bits



✓ What is the default size of operand in Virtual mode? *

2/2

- ☒ 16 bits
- ☐ 20 bits
- ☐ 32 bits
- ☐ 8 bits



✗ What is the size of IDT? *

0/2

- ☐ 64 KB
- ☐ 32 KB
- ☒ 1 KB
- ☐ 4 KB



✓ How many total no. of descriptors present in IDT? *

2/2

- ☐ 8192
- ☒ 256
- ☐ 65536
- ☐ 16384



✓ How many total number of segments present in 80386DX? *

2/2

- ☐ 8192
- ☐ 65536
- ☐ 1024
- ☒ 16384



✗ What is the default size of operand in protected mode? *

0/2

- ☒ 16 bits
- ☐ 32 bits
- ☐ 8 bits
- ☐ 64 bits



✓ What is the size of data and address bus in 80386SX? *

2/2

- ☒ 16 bits , 24 bits
- ☐ 8 bits , 16 bits
- ☐ 32 bits, 32 bits
- ☐ 16 bits , 20 bits



✓ What is the size of Instruction queue in 80386 Dx? *

2/2

- ☐ 6 bytes
- ☒ 16 bytes
- ☐ 256 bytes
- ☐ 4 bytes



✗ Which of the following field is used for segment level protection? *

0/2

- ☐ PL
- ☐ DPL
- ☐ RPL
- ☒ CPL



✓ What is the size of GDTR register in 80386DX ? *

2/2

- ☐ 32 bits
- ☐ 16 bits
- ☒ 48 bits
- ☐ 64 bits



✓ How many Control registers present in 80386DX? *

2/2

- ☐ 1
- ☐ 2
- ☐ 3
- ☒ 4



✓ What is the maximum size of segment in 80386Dx ? *

2/2

- ☐ 1 MB
- ☒ 4 GB
- ☐ 1 Byte
- ☐ 16 MB



✓ How many GDT and IDT present in PM? *

2/2

- ☒ 1,1
- ☐ 2,2
- ☐ Depends on number of tasks
- ☐ 0,0



✗ How many total no. of pages in physical memory? *

0/2

- ☐ 10,48,576
- ☒ 65,536
- ☐ 1024
- ☐ 16384



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