

Table 21. ST morpho connector for NUCLEO-F207ZG, NUCLEO-F412ZG, NUCLEO-F413ZH, NUCLEO-F429ZI, NUCLEO-F439ZI, NUCLEO-F446ZE, NUCLEO-F722ZE, NUCLEO-F746ZG, NUCLEO-F756ZG, **NUCLEO-F767ZI** and NUCLEO-H743ZI

SPI5 => DACs

SPI3 => from RPi

CN11 odd pins		CN11 even pins		CN12 odd pins		CN12 even pins	
Pin	Pin name	Pin	Pin name	Pin	Pin name	Pin	Pin name
1	SPI3 CLK PC10	2	LS138 A2 PC11	1	LS138 A1 PC9	2	LS138 A0 PC8
3	PC12	4	DEAD !!! PD2	3	PB8	4	EXTI6 - RPi CE PC6
5	VDD	6	E5V	5	PB9	6	PC5
7	BOOT0 ⁽¹⁾	8	GND	7	AVDD	8	U5V ⁽²⁾
9	PF6	10	-	9	GND	10	PD8
11	SPI5 CLK PF7	12	IOREF	11	PA5	12	PA12
13	PA13 ⁽³⁾	14	RESET	13	PA6	14	PA11
15	PA14 ⁽³⁾	16	+3.3 V	15	PA7	16	PB12
17	PA15	18	+5 V	17	PB6	18	PB11
19	GND	20	GND	19	PC7	20	GND
21	PB7	22	GND	21	PA9	22	SPI3 MOSI PB2
23	PC13	24	V _{IN}	23	PA8	24	PB1
25	PC14	26	-	25	Calib TIM2 CH3 (3340B) PB10	26	PB15
27	PC15	28	Calib TIM2 CH1 (3340A) PA0	27	PB4	28	PB14
29	PH0	30	PA1	29	PB5	30	PB13
31	PH1	32	PA4	31	PB3	32	AGND
33	V _{BAT}	34	PB0	33	PA10	34	PC4
35	PC2	36	PC1	35	PA2	36	PF5
37	PC3	38	PC0	37	Calib TIM2 CH4 (13K7) PA3	38	PF4
39	PD4	40	PD3	39	GND	40	PE8
41	PD5	42	PG2	41	PD13	42	PF10
43	PD6	44	PG3	43	PD12	44	PE7
45	PD7	46	VCF 4th PE2	45	PD11	46	PD14
47	PE3	48	VCF 2nd PE4	47	PE10	48	PD15
49	GND	50	PE5	49	PE12	50	PF14
51	PF1	52	PF2	51	PE14	52	PE9
53	PF0	54	PF8	53	PE15	54	GND
55	DRUM SNARE PD1	56	SPI5 MOSI PF9	55	PE13	56	PE11
57	411 PULSE PD0	58	PG1	57	PF13	58	PF3
59	LS138 EN! PG0	60	GND	59	PF12	60	PF15
61	DEAD !!! PE1	62	411 TRI PE6	61	PG14	62	PF11

Table 21. ST morpho connector for NUCLEO-F207ZG, NUCLEO-F412ZG, NUCLEO-F413ZH, NUCLEO-F429ZI, NUCLEO-F439ZI, NUCLEO-F446ZE, NUCLEO-F722ZE, NUCLEO-F746ZG, NUCLEO-F756ZG, NUCLEO-F767ZI and NUCLEO-H743ZI (continued)

CN11 odd pins		CN11 even pins		CN12 odd pins		CN12 even pins	
Pin	Pin name	Pin	Pin name	Pin	Pin name	Pin	Pin name
63	PG9	64	PG15	63	GND	64	PE0
65	PG12	66	PG10	65	PD10	66	PG8
67	-	68	PG13	67	PG7	68	PG5
69	PD9	70	PG11	69	PG4	70	PG6

1. Default state of BOOT0 is 0. It can be set to 1 when a jumper is plugged on the pins 5-7 of CN11.
2. U5V is the 5 V power coming from the ST-LINKV2-1 USB connector that rises before and it rises before the +5 V rising on the board.
3. PA13 and PA14 are shared with SWD signals connected to ST-LINK/V2-1. If ST-LINK part is not cut, it is not recommended to use them as I/O pins.

Table 22. ST morpho connector for NUCLEO-F303ZE

CN11 odd pins		CN11 even pins		CN12 odd pins		CN12 even pins	
Pin	Name	Pin	Name	Pin	Name	Pin	Name
1	PC10	2	PC11	1	PC9	2	PC8
3	PC12	4	PD2	3	PB8	4	PC6
5	VDD	6	E5V	5	PB9	6	PC5
7	BOOT0 ⁽¹⁾	8	GND	7	AVDD	8	U5V ⁽²⁾
9	PF6	10	-	9	GND	10	PD8
11	PF7	12	IOREF	11	PA5	12	PA12
13	PA13 ⁽³⁾	14	RESET	13	PA6	14	PA11
15	PA14 ⁽³⁾	16	+3.3 V	15	PA7	16	PB12
17	PA15	18	+5 V	17	PB6	18	PB11
19	GND	20	GND	19	PC7	20	GND
21	PB7	22	GND	21	PA9	22	PB2
23	PC13	24	V _{IN}	23	PA8	24	PB1
25	PC14	26	-	25	PB10	26	PB15
27	PC15	28	PA0	27	PB4	28	PB14
29	PF0	30	PA1	29	PB5	30	PB13
31	PF1	32	PA4	31	PB3	32	AGND
33	V _{BAT}	34	PB0	33	PA10	34	PC4
35	PC2	36	PC1	35	PA2	36	PF5
37	PC3	38	PC0	37	PA3	38	PF4
39	PD4	40	PD3	39	GND	40	PE8