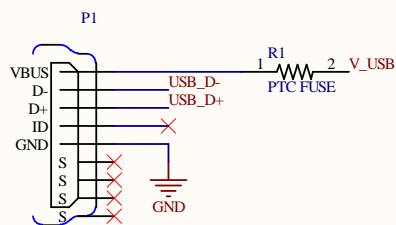
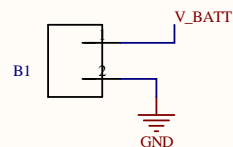


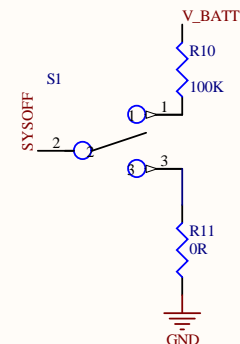
### USB Supply Input



### Battery Input

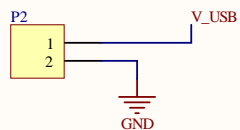


### Shutoff Switch



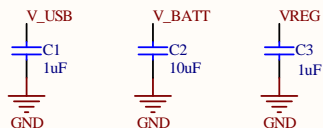
Power ON circuit. SYSOFF low to Power ON

### Alternate Supply Input

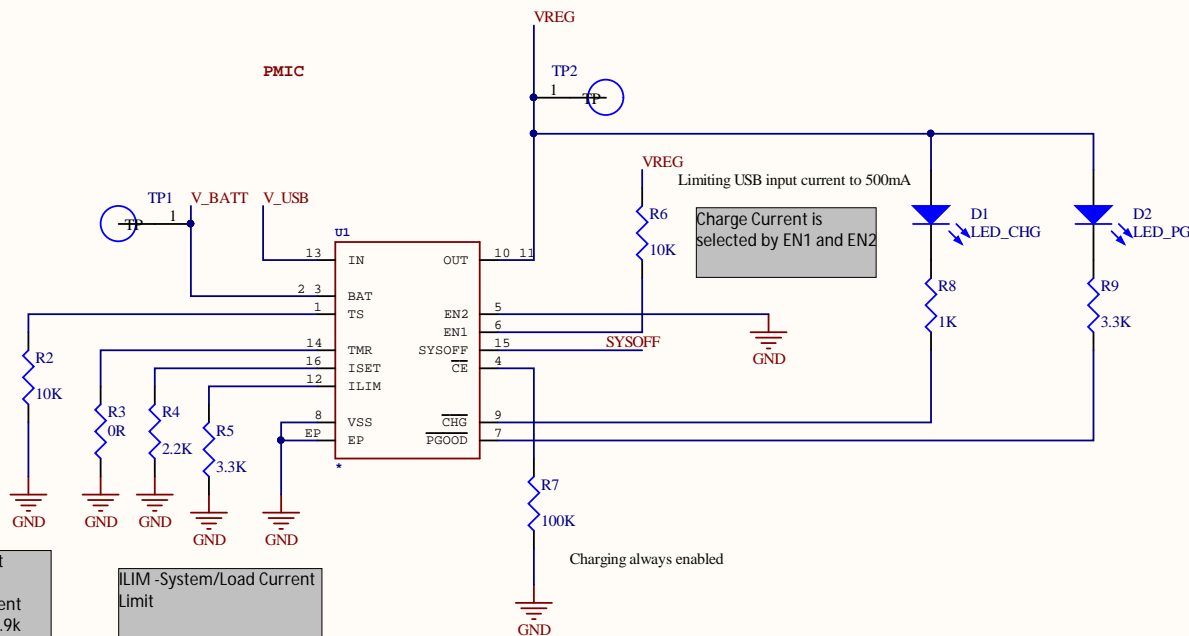


For Debug purpose only

### Decoupling Capacitor



### PMIC




#### ISET - Charge Current

Safe fast charge current  
-R\_ISET range: 590-8.9k  
-R\_ISET = 885 / I\_CHG  
= 885/(400mA)  
= 2.2 kOhm

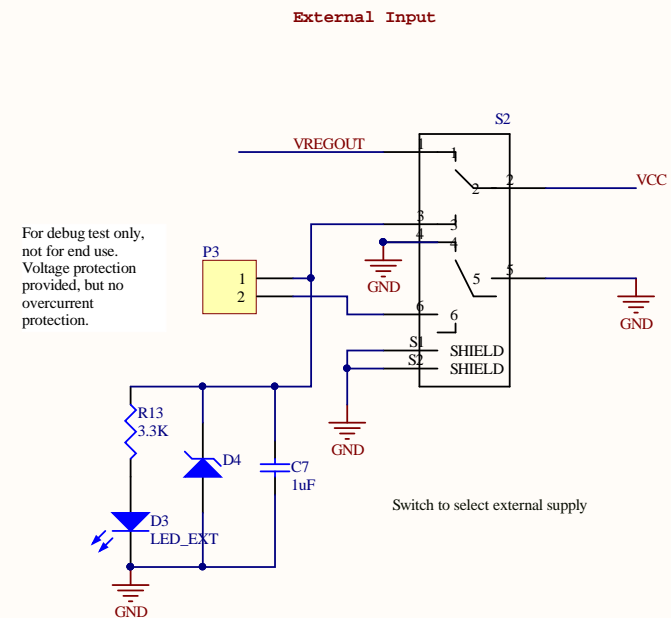
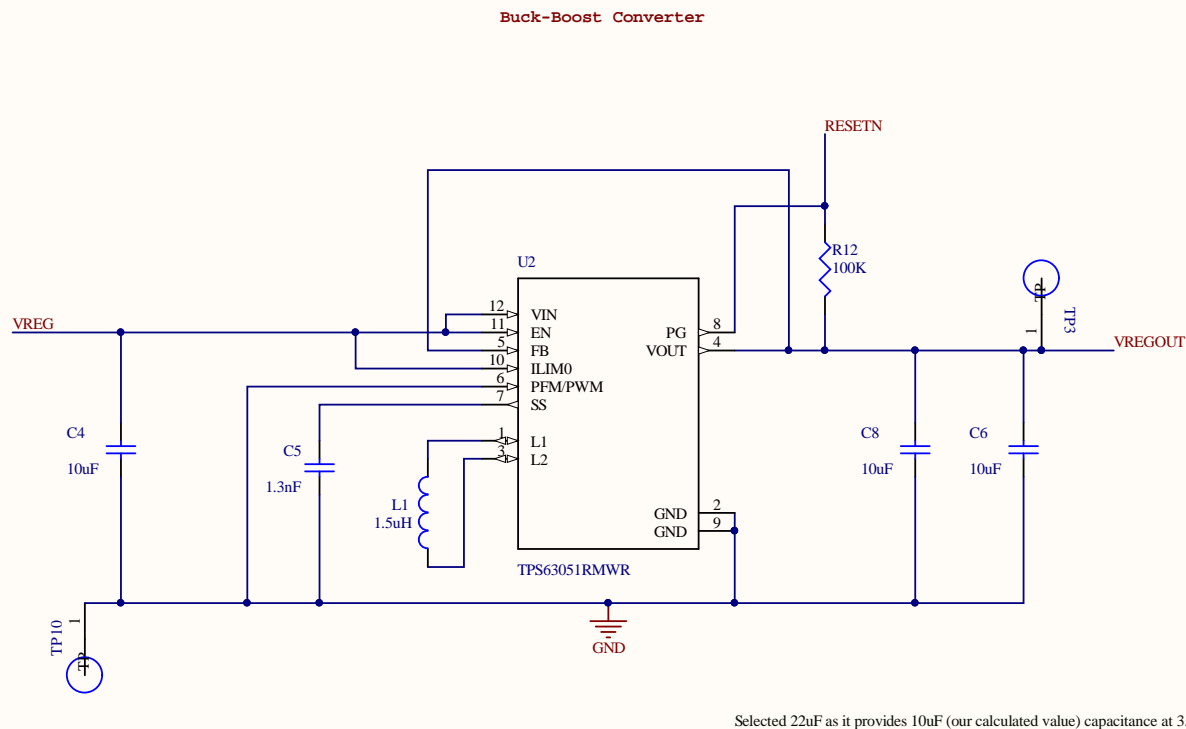
#### ILIM -System/Load Current Limit

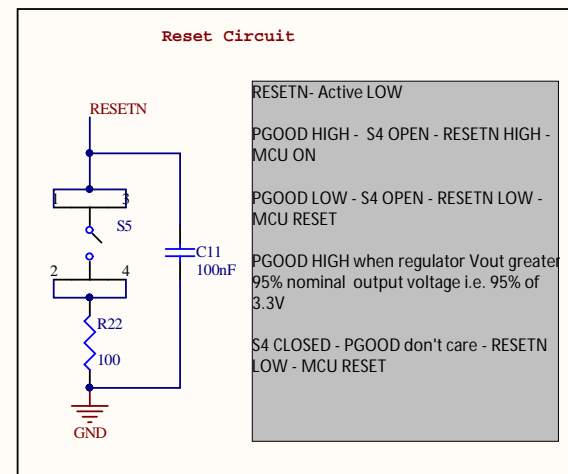
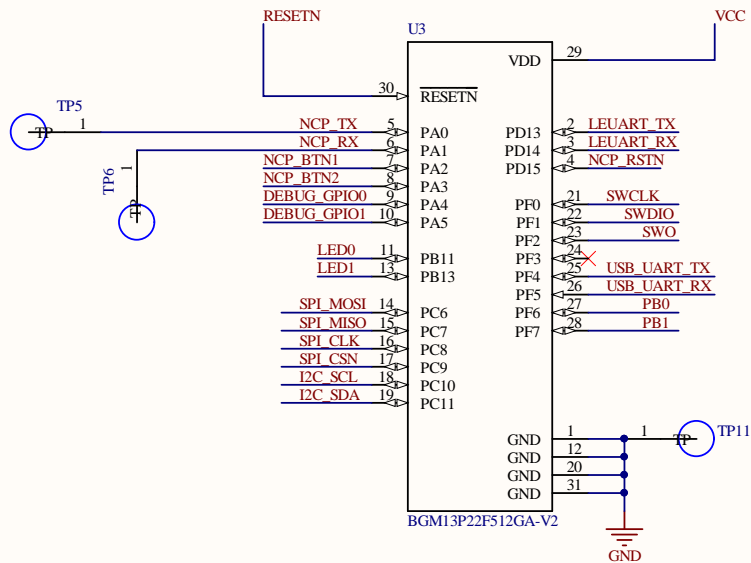
Sets combined system load and battery charge currents pulled from USB input


- R\_ILIM range: 1.1k-8k  
- R\_ILIM= 1650 / I\_INMAX

Title <b>PMIC</b>			Gateway University of Colorado Boulder * *	
Size: <b>A4</b>	Number: <b>2</b>	Revision: <b>1</b>		
Date: <b>10/26/2018</b>	Time: <b>2:04:55 AM</b>	Sheet <b>2</b> of <b>6</b>		
File: <b>D:\UC\Boulder\Acads\Sem_3\ECEN5023-001 Low Power\Project\Low_Power_Embedded_Design\Gateway\PMIC.SchDoc</b>				

File: D:\UCBoulder\Acads\Sem\_3\ECEN5023-001 Low Power\Project\Low\_Power\_Embedded\_Design\Gateway\PMIC.SchDoc





Title <i>Gecko</i>			Gateway University of Colorado Boulder	
Size: <i>A4</i>	Number: 4	Revision: 1		
Date: <i>10/26/2018</i>	Time: <i>2:04:55 AM</i>	Sheet 4 of 6		
File: D:\UCBoulder\Acads\Sem_3\ECEN5023-001 Low Power\Project\Low_Power_Embedded_Design\Gateway\Gecko.SchDoc				

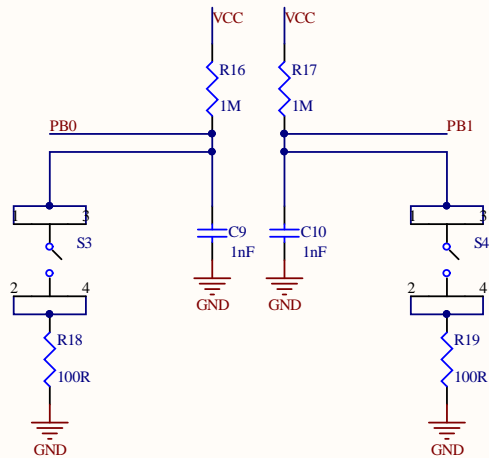
1

2

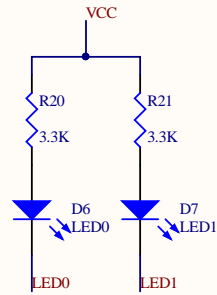
3

4

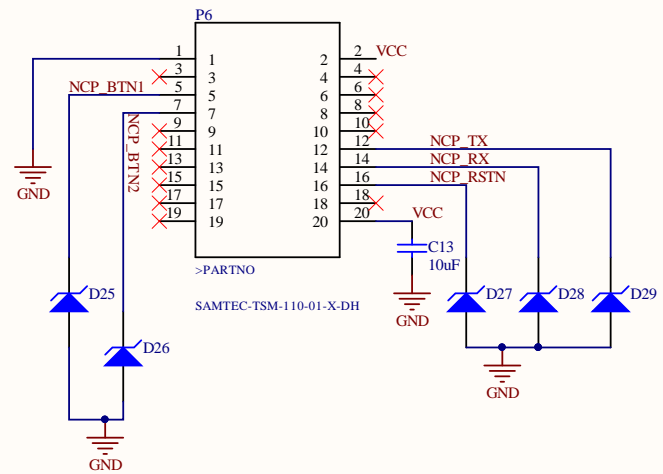
## Push Buttons



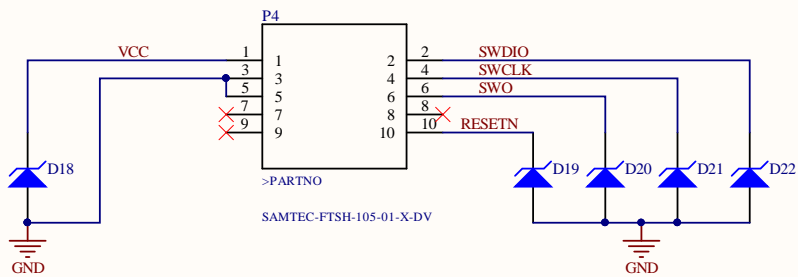
## Debug LEDs



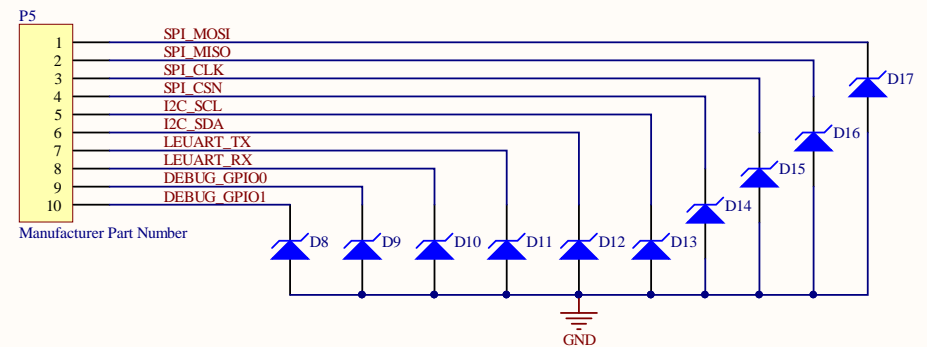
## Expansion Header




## Debug Interface



## GPIO



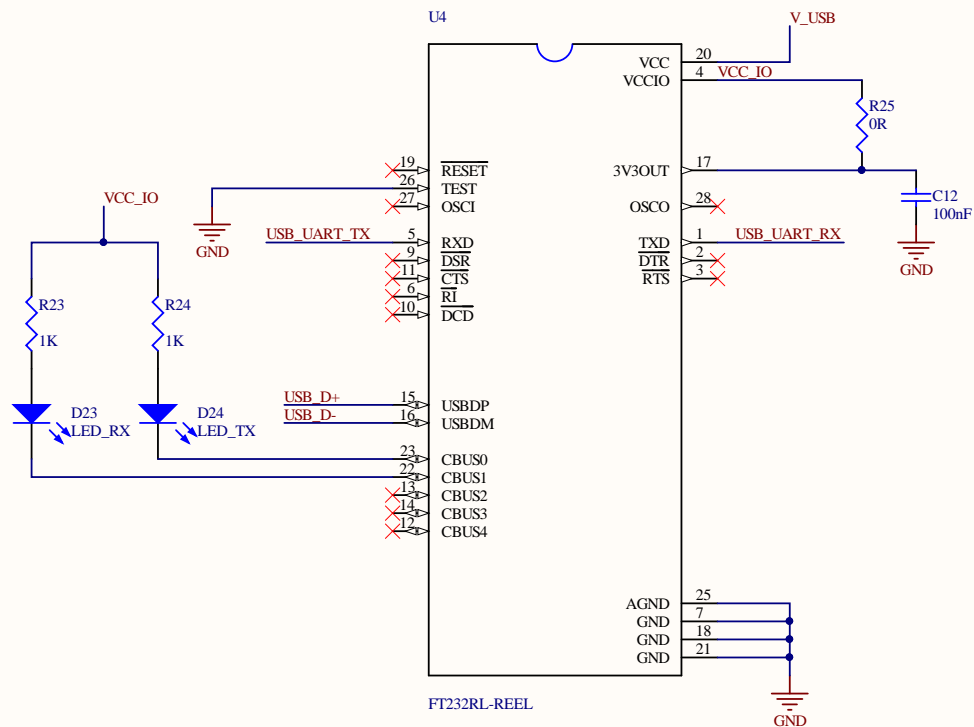
Title <i><b>Gecko_IO</b></i>			Gateway University of Colorado Boulder * *	
Size: <b>A4</b>	Number: <b>5</b>	Revision: <b>1</b>		
Date: <b>10/26/2018</b>	Time: <b>2:04:56 AM</b>	Sheet <b>5</b> of <b>6</b>		
File: <b>D:\UCBoulder\Acads\Sem_3\ECEN5023-001 Low Power\Project\Low_Power_Embedded_Design\Gateway\Gecko_IO.SchDoc</b>				


1

2

3

4



Title <i><b>UART to USB FT232</b></i>			Gateway University of Colorado Boulder	
Size: <b>A4</b>	Number: <b>6</b>	Revision: <b>1</b>		
Date: <b>10/26/2018</b>	Time: <b>2:04:56 AM</b>	Sheet <b>6</b> of <b>6</b>		
File: <b>D:\UCBoulder\Acads\Sem_3\ECEN5023-001 Low Power\Project\Low_Power_Embedded_Design\Gateway\UART_USB.SchDoc</b>				