

iCEBreaker Bitsy v1.1c

Open Source FPGA development board

icebreaker-fpga.org

Pin Name

FPGA Package Pin Number

FPGA Pin Name

Differential Pair

Pin Name	FPGA Package Pin Number	FPGA Pin Name	Notes
RGB[0-2]	39-41		
LEDR	25	IOT_36b	V _{IN} (1.45 to 5.5 V)
LEDG	0 + - 1 2 + - 3 4 + - 5	P23 P22 P21 P20 P19 P18 P17 P16 P15 P14 P13	3.3V (500mA total) GND + - + - + - + - + -
BTN	P0	P1	
IOB_6a	47	P2	
IOB_13b	44	P3	
IOB_2a	48	P4	
IOB_3b	45	P5	
IOB_4a	4	P6	
IOB_5b	3	P7	
IOB_8a	10	P8	
IOB_9b	11	P9	
IOB_16a	9	P10	
IOB_18a	12	P11	
IOB_20a	21	P12	
IOB_22a	13	P11	C _{DONE}
IOB_23b	14 + - 13	P12	C _{RESET}
IOB_24a	25	P13	GND
IOB_25b	26	P14	3.3V

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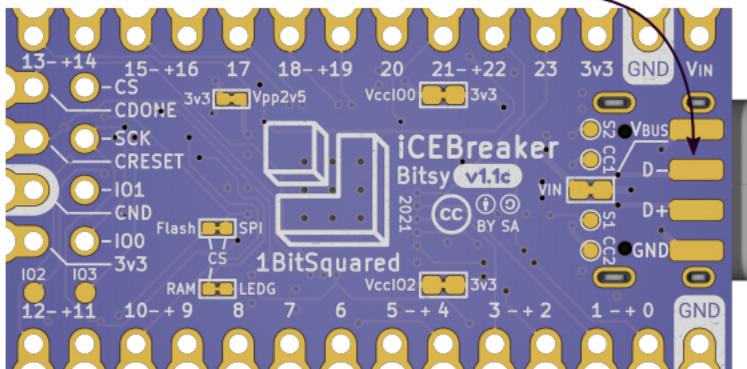
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CLK	35	IOT_46b		USB_PU	37	IOT_45a
FLASH_CS	16	IOT_35b		USB_DN	38	IOT_50b
RAM_CS	6	IOB_13b		USB_DP	42	IOT_51a



C _{DONE}		CS	16	I0B_35b_SPI_CSN
C _{RESET}		SCK	15	I0B_34a_SPI_SCK
GND		I01	17	I0B_33b_SPI_SI
3.3V		I00	14	I0B_32a_SPI_SO
I0B_31b	18	I03	19	I0B_29b



1 BIT SQUARED

