	ADC2 - DAC EXAMPLE
	# include Kstdio.h>
	#include (stallib.h)
4	#include (freertas/Free RTOS.h)
	#include (freertas/task.h)
	#include (freertos/queue h)
	#include {driver/gpio.h}
	#include {driver/adc.h}
	#include {driver/dac.h}
	# include (esp-system.h)
	#include < < esp_adc_cal.h > 1
	A SECRETARY OF STATE OF THE STA
J.	#define DAC_ EXAMPLE_ CHANNEL CONFIGE EXAMPLE_ DAC-CHANNEL
	#define ADC2 EXAMPLE CHANNEL CONFIG. EXAMPLE ADC CHANNE
ě	#define ADC2 EXAMPLE CHANNEL CONFIG. EXAMPLE ADC CHANNE
	void app-main (void) &
	void app_main (void) { vints_t output_data = 0;
	void app_main (void) & vints_t output_data = 0; int read_main;
	void app_main (void) { uint = t output_data = 0; int : read_raw; exp_err_t r; gpio_num_t adc_gpio_num; dac_gpio_num;
	void app_main (void) { void app_main (void) { vints_t output_data = 0; int read_row; exp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; rum r= adcz_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_
	void app_main (void) { uint8_t output_data = 0; int read_raw; esp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; r= adcz_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_ assert (r== ESD_AK).
	void app_main (void) { void app_main (void) { vints_t output_data = 0; int read_row; exp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; rum r= adcz_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_
	void app_main (void) { uints_t output_data = 0; int read_row; exp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; r= adc2_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_num); assert (r = ESP_OK); r = dac_pad_get_io_num(Dac_EXAMPLE_CHANNEL, adac_gpio_num);
	void app_main (void) { uints_t output_data = 0; int read_row; exp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; r= adc2_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_num); assert (r = ESP_OK); r = dac_pad_get_io_num(Dac_EXAMPLE_CHANNEL, adac_gpio_num);
	void app_main (void) { uints_t output_data = 0; int read_raw; exp_err_t r; gpio_num_t adc_gpio_num, dac_gpio_num; r= adc2_pad_get_io_num (ADC2_EXAMPLE_CHANNEL, & adc_gpio_num); assert (r== ESP_OK); sdac_gpio_num); r= dac_pad_get_io_num(Dac_EXAMPLE_CHANNEL,

dac-output-enable (DAC-EXAMPLE CHANNEL). prints ("adacz_init ... \n"); adez_config_channel_atten CADCZ: EXAMPLE: CHANNEL ADC_ATTEN_Odb). VTask Delay (2 * portTICK_PERIOD_MS); print ("start conversion. \n"), while (i) & dac output voltage Dac-Example CHANNEL, output datatt) r = adcz_got_raco CADICZ - EXAMPLE_CHANNEL; ADC_WIDTH_12Bit, & read_raw), of (r == ESP_OK) & prints ("xd: xd\n" = output_data, read_raw); 2 clos of (r == ESP_ERR_INVALID_STATE) & printf ("Ys: ADC2 is in use by WiFi. Ish esper to name" I else & print(("xs\n", esp_crr_to_name(r)),... v Tack Delay (2 * port TICK_PERIOD_MS). 4