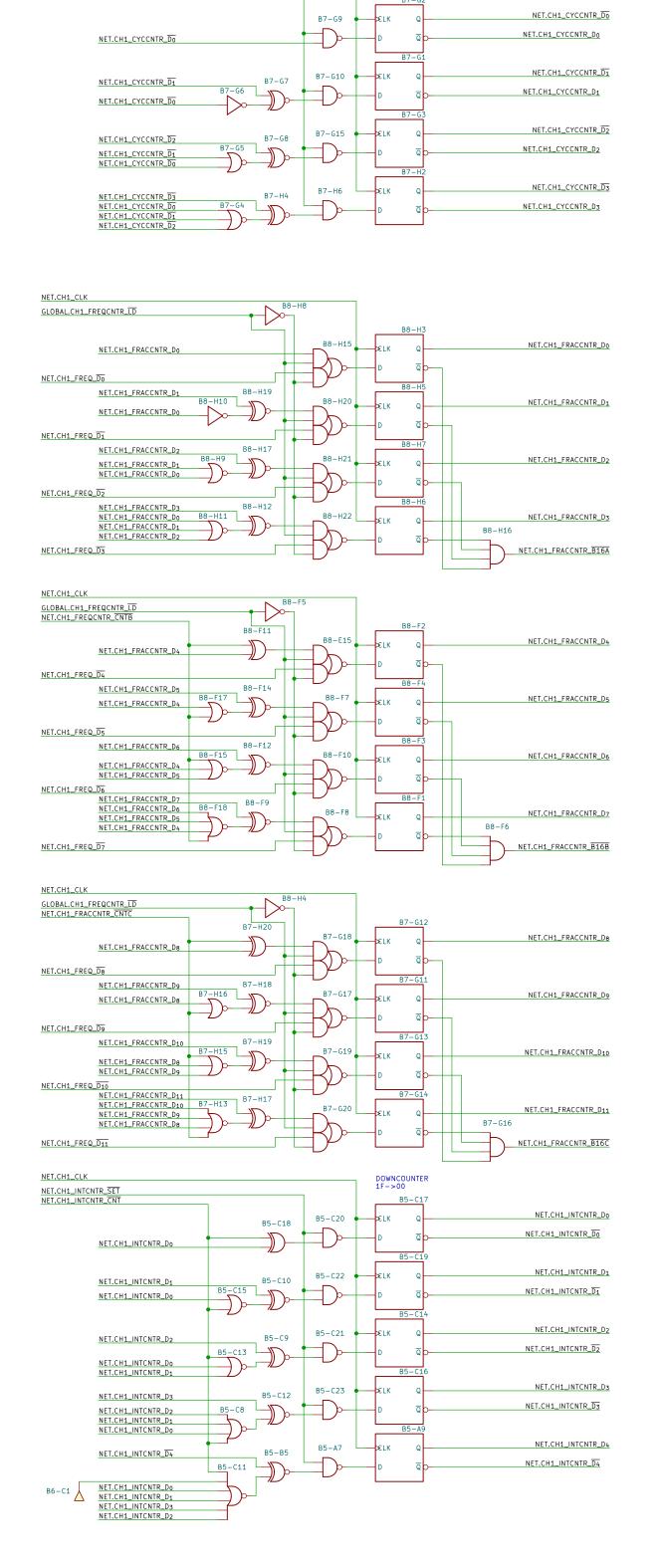
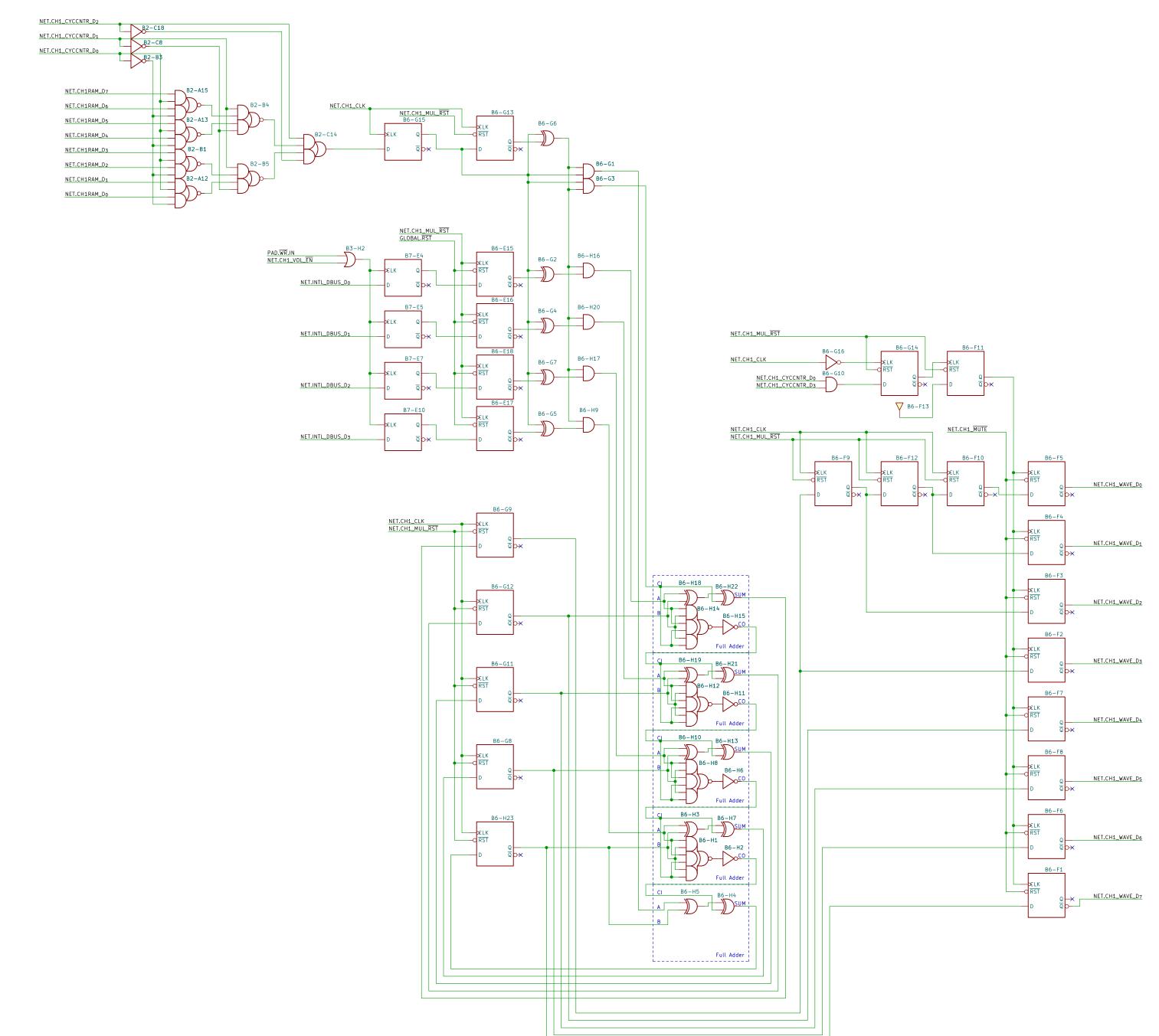
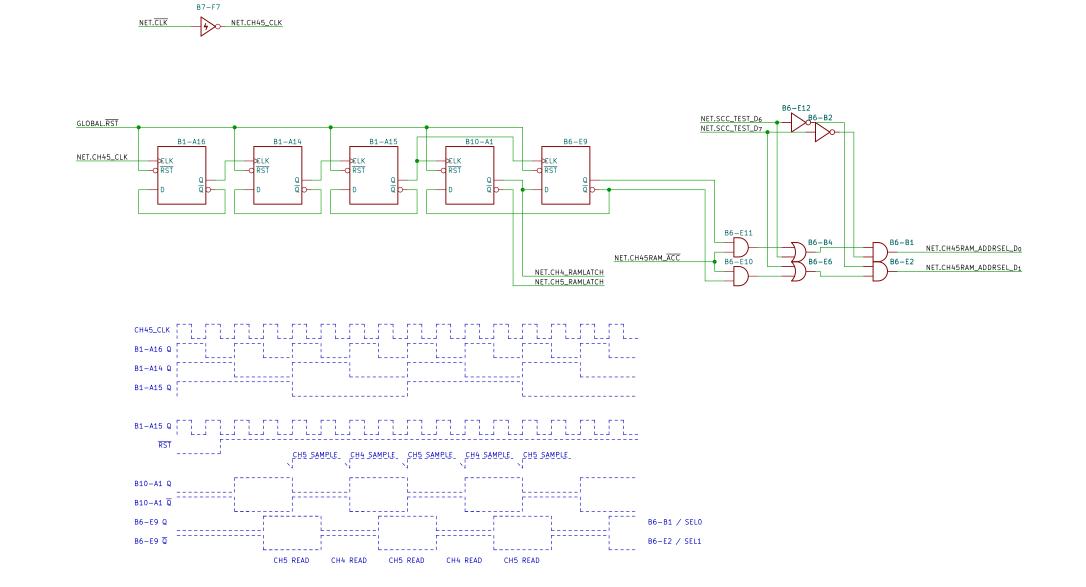


NET.CH1_CLK

GLOBAL.CH1_FREQCNTR_LD



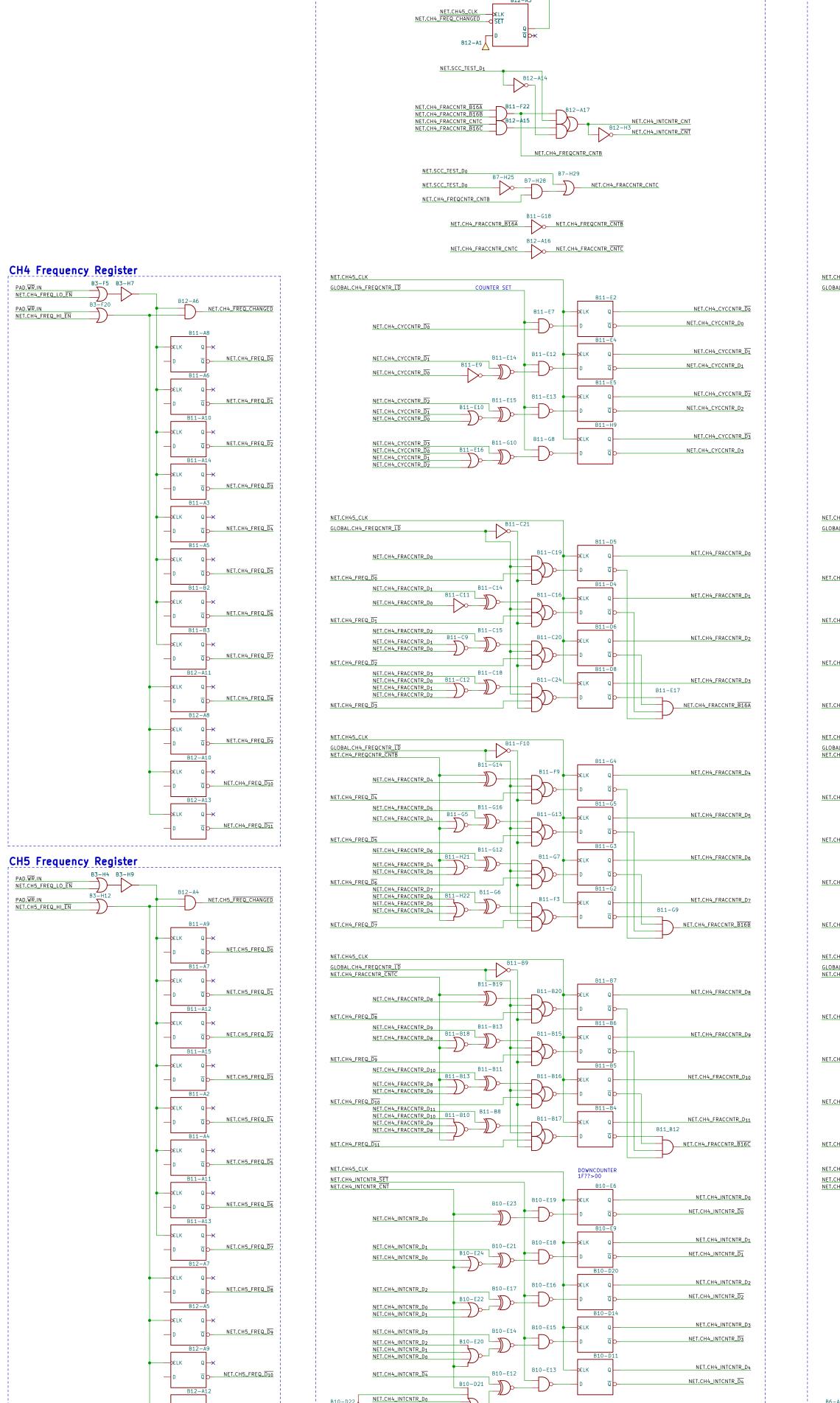




NET.CH4_INTCNTR_CNT B12-H7 B12-H5 GLOBAL.CH4_FREQCNTR_TD

NET.CH4_INTCNTR_SET

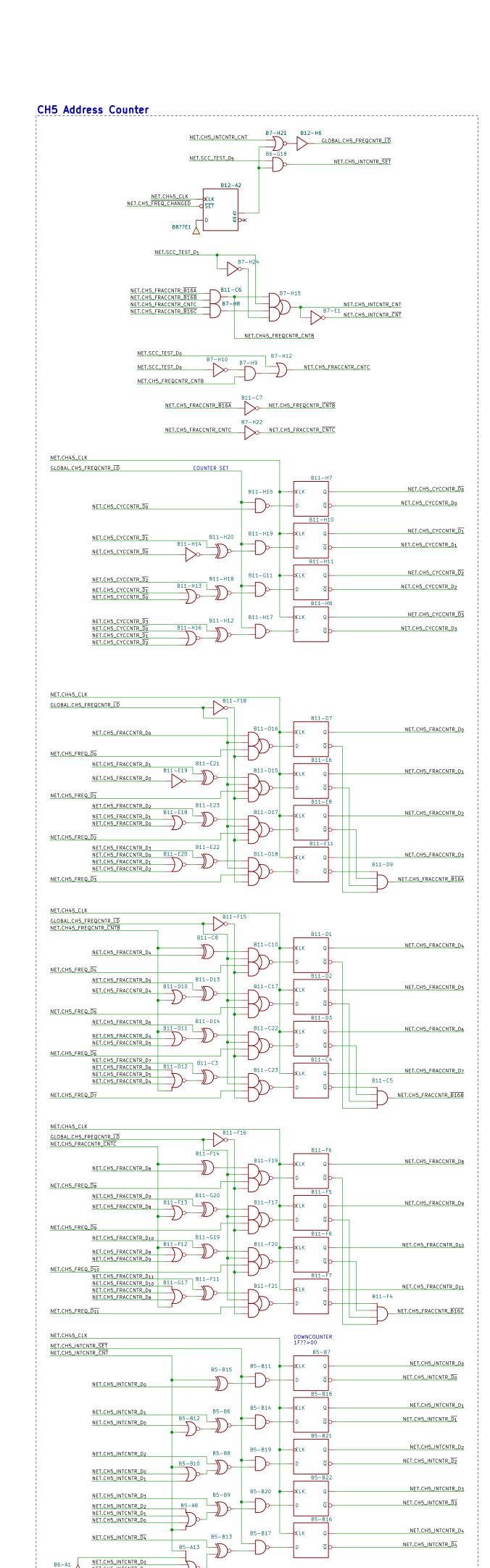
NET.SCC_TEST_D5

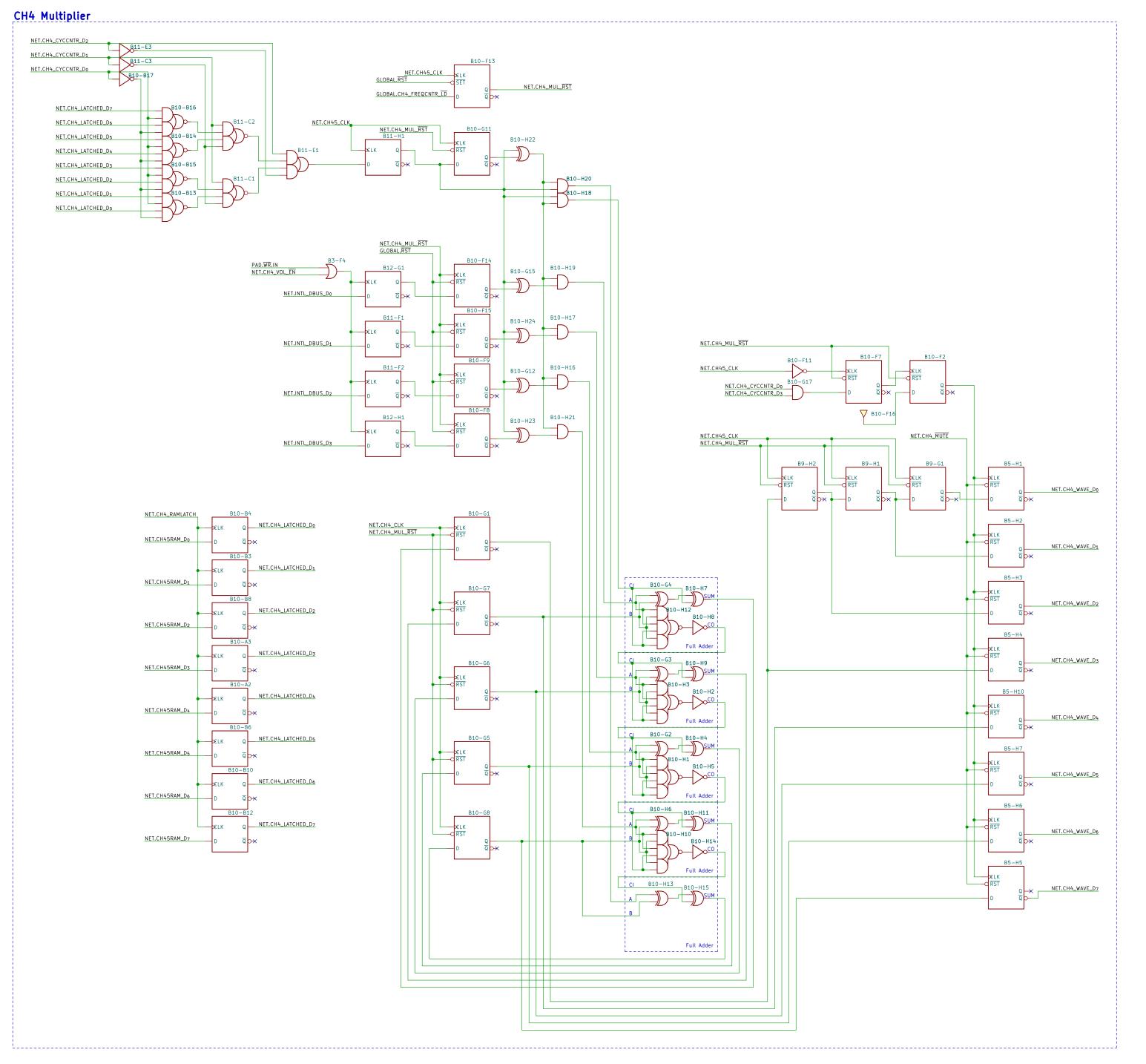


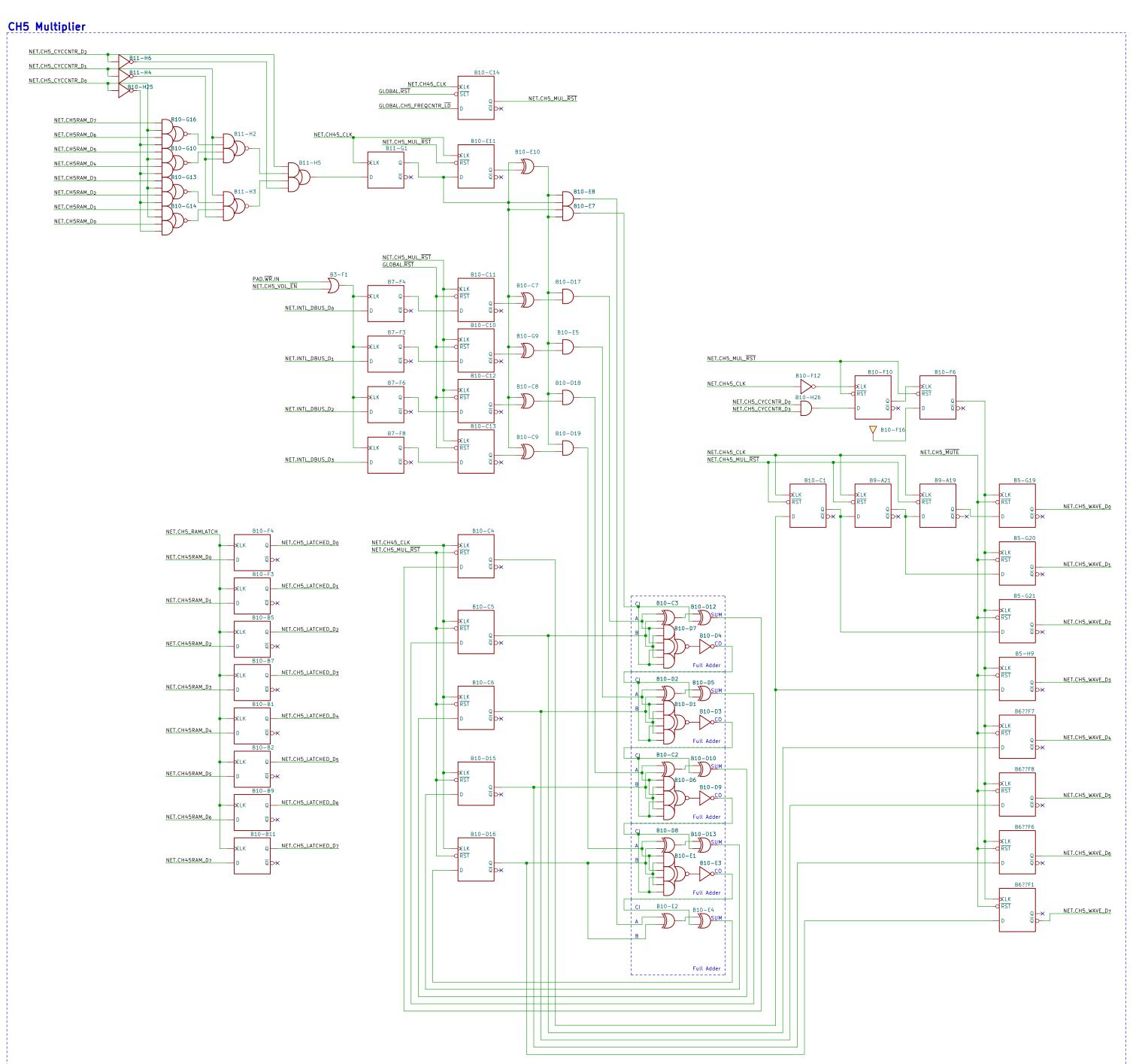
B10-D22 NET.CH4_INTCNTR_D0 NET.CH4_INTCNTR_D1 NET.CH4_INTCNTR_D3 NET.CH4_INTCNTR_D2

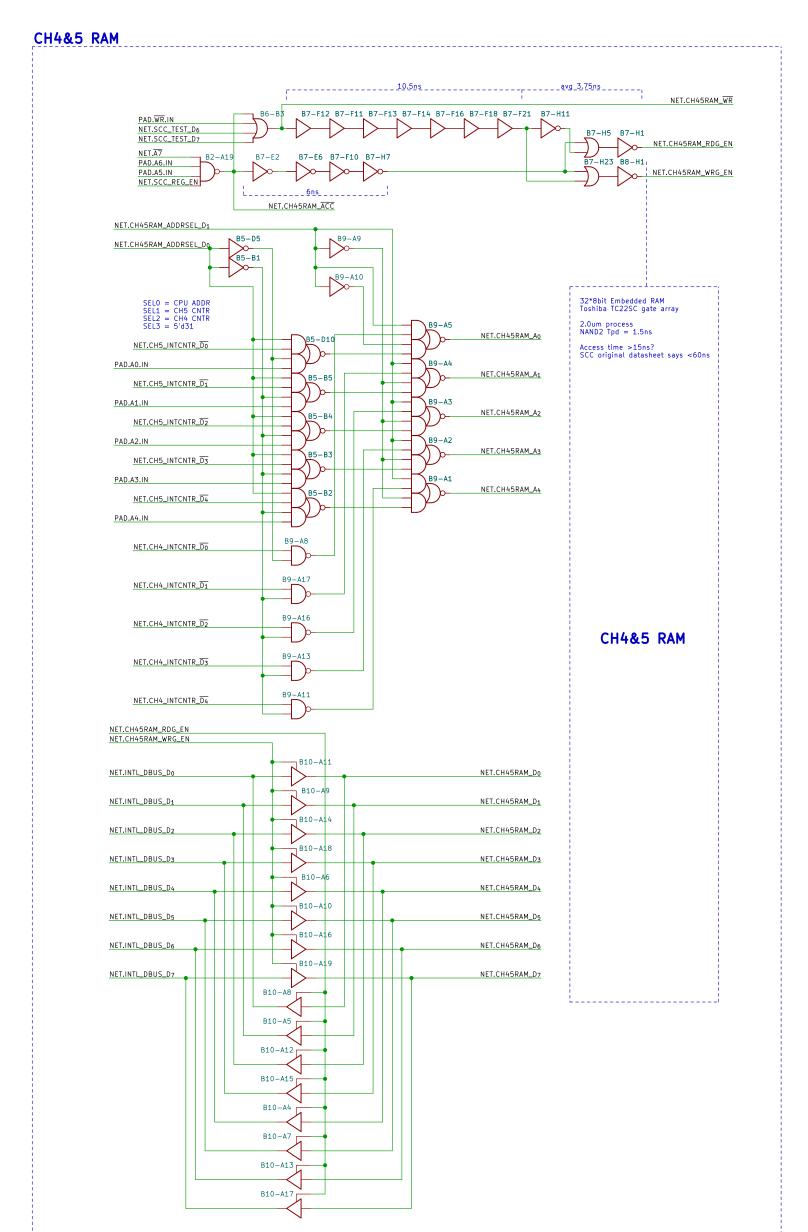
NET.CH5_FREQ_D11

CH4 Address Counter









This schematic does not include Ch.2 and 3
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