DATABUS SERIAL INPUT —< RDØ V101 74HC157D XII 2 0 NDATA1
XII 14 0 NCLOCK1
XII 13 0 NCLOCK2 D1 69 MD1
D2 68 MD2
D3 67 MD3
D4 65 MD4
D5 64 MD5
D6 63 MD6
D7 62 MD7 D1 D2 D1 D3 D4 D5 D6 MA4 \rightarrow RESET MAS DATØ DATABUS MAIØ 75 GPI01 74 GPI02 MUTE (RES_DAC ← XII 40-→ UNIT_SDA 73 GP103 MA14 GP103 > XK1 12 0-MC53/MA15 XII 3 O t → UNIT_SCL 57 SDIZ 56 SDII 51 SCKR 55 WSR GP103 MCAS/MCSZ/MAI6 XII 6 O-R23 10K MRAS/MCST/MA17 XII 7 0-WRØ← XII g O RDØ (MCSO XII 11 O-MWR 41 MOS1/HAØ 42 SS/HA2 SDOØ SD01 46 - 35 MISO/SDA XII 10 O-UNIT_SDA > 26 SCK/SCL SD02 45 XII 12 O-UNIT_SCL > SCKT NCLOCK3 TO AUDIO OUTPUT XK1 30 P DSCK/OS1 XII 15 O DSI/0S0 -O TP3 +5V ▼ I25_DAT OXI2 37 38 MODA/IROB 39 MODE/NMT PROPERTY OF PHILIPS TV TEST EQUIPMENT ALL RIGHTS STRICTLY RESERVED. 1ACC 3 V2-B 74HC00D PCAP 3 125_CLK OXI2 3 36 RESET PINIT C[23-28] R2 10K I2S_WS O X I 2 SGND DGND AGND QGND

|34|44|54|66|72|1 |8 |15 |21 |10 |29|52 RES_DAC> -0 X I 2 TP5 OXKI 6 MUTE 0 X I 2 6 VI-B 74HC368D 0 X I 2 7 RESET> + XK1 40 ---OXI2 8 XII 16 O o XIZ g DATABUS V104 74HC574D V108 74HC4040D MUTE R21 33R2 DAC_CLK OXIZ 10 TO DATA VEE OØ PHASE DETECTOR VCXO D2 03 OXK1 F Q4 -0 XK1 14 -0 XK1 15 & 11 D3 D4 D5 С3 Q6 -0 XK1 16 Q4 12 CIBI —0 XK1 1 CIB2 -0 XK1 2 ____O X12 5 C3 8P2 010 V2-C 74HC00D XK1 8 0-XK1 7 0-XK1 10 0-PM5689 FIRST USED IN: PCB REF.: 4008 117 0612 V7-D 74HC00D 1 95-06-16 1 85-03-02 NICAM DECODER 4008 109 8199 & M.B. 1 85-03-03 NAME: SAA/SMa/JML 1 SH SH 130 - 1 1 95-03-08 CHECK: ABI991 SCM KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT. 95-01-17 A3L

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