RECTIFIER DC/DC CONVERTER VOLTAGE REGULATORS V2 LM3171 ± €3 60a 📥 ± C26 ± C32  $\bot$ R8 75 C25 — ± C27 ± C28 ₩ C29 C15 338P ☐ Bi\* ☐ I PROPERTY OF PHILIPS TV TEST EQUIPMENT A/S
ALL RIGHTS STRICTLY RESERVED. SYNC AMPLIFIER INPUT BUFFER → EXT SYNCHR REF.OSCILLATOR LOOP FILTER TCXO OFF >-BLANK AMPLIFIER \*1 R21 475 TCXO REF. FREQ. \*1 C69 \*1 \_\_\_\_ C7Ø | IBP # C92 # C93 \*2 05C02 → TCXO REF. TCXO NOT MOUNTED ON 4008 109 7380 \* NOT MOUNTED

\*\* DEVIATING VALUES FOR OTHER VERSIONS

\*\*\* NOT MOUNTED IN SOME VERSIONS FIRST USED IN: PM5643 PCB REF.: 4008 117 0568 THIS SCHEMATIC DIAGRAM ALSO COVERS: 2 93-08-09 4008 109 7245 MAINBOARD 4008 109 7380 1 92-83-12 NAME: GBG/SM/ML/SMa SH SH 130 - 1 \*2 TCXO TYPE CCO 200 E ABC72453 SCM KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT. 89-03-17 A3

28.78

1 KHZ AUDIO GENERATOR BALANCED OUTPUT SINE GENERATOR LOW PASS FILTER 13.5MHZ > R328 38K1 AUDIO OUT 13.5MHZ > COAD > C388 AUDIO AMPL \*8V D228 BAW62 R329 38K1 V216-B NE5533 MODULATOR BLACK BURST GENERATOR CP C CC LOAD > PHASE ADJUST PAL SHIFT \* \* \* L 185 18UH R136 IK PROPERTY OF PHILIPS TV TEST EQUIPMENT ALL RIGHTS STRICTLY RESERVED. 2.77 ≻ PI > BURST FILTER PULSE GENERATOR 1/03 NTSC SET-UP GEN CHROMA FILTER OUTPUT AMPLIFIER BLANK \_ BLANKING PAL PULSE GEN. NOT MOUNTED ON LOWPASS FILTER 4008 109 7380 \* NOT MOUNTED \*\* DEVIATING VALUES FOR OTHER VERSIONS FIRST USED IN: PM5643 PCB REF.: 4008 117 0568 \*\*\* NOT MOUNTED IN SOME VERSIONS PAPU 1 90-03-26 PAPU >-2 92-12-02 MAINBOARD 4008 109 7245 2 93-02-08 SH SH 130 - 2 3 94-08-02 NAME: GBG/SM/ML/SMa THIS SCHEMATIC DIAGRAM ALSO COVERS: BCA72453.5CM KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT. 89-03-17 A3 4008 109 7380

28.74

7

U2 UЗ U3XA 2 0----U4XA 1 0---- $\longrightarrow$  RXD TXT C SXEU ⊔4XA 2 0----OU4XC 1 ⊔4×A 3 ₀---REMOTE ← U3XC 4 U2XC 5 OU4XC 3 -≺EXT SYNCHR U4XA 4 0---U2XA 6 O U2XC 6 OU4XC 4 U4XA 5 0-OU4XC 5 O U4XC B ST42 SDA > XR 3 0 0 XR 13 O U4XC 7 U3XA 12 SCL > XR 4 → ○ ○ XR 14 U4XA 8 0----U4XC B O DE PHA O U4XC 9 U2XC II PHB ₹ > \_\_O XD 3 U4XA 11 0----CLAMP > O U4XC II  $0 \longrightarrow NS$ U4XA 12 0-\_oXD s FIELD 1 > U4XC 13 0 ST64 \_o XD 7 U2XA 14 0 0 12XC 14 U3XA 14 U3XC 14 U4XA 13 0 \_\_\_\_\_\_O 5753 +3V > \_oXD s TXD > OUBXC 15 BLANK 2 > \_\_\_\_O XD 12 FH REF > U2XC 16 BI AXEU U3XC 16 U4XC 16 \_o XD 13 5DA ≻ U3XA 17 0-U2XA 17 0 U2XE 17 U3XC 17 U4XC 17 BLANK > \_\_\_O XD 15 \_0 U3XC 18 U2XC 18 13.5MHZ > PROPERTY OF PHILIPS TY TEST EQUIPMENT ALL RIGHTS STRICTLY RESERVED, O SDA O EI AXEU U2XC 19 ≺TCXO REF. U2XC 28 U3XA 28 U3XC 28 UZXA 21 0 UZXC 21 U3XA 21 U3XC 21 XR 2 U3XA 22 U2XA 22 0-O\_\_\_\_\_\_\_ TXT1 U3XA 23 O U2XC 23 U3XC 23 OUBXC 24 BLANK ↑ U3XC 25 OU2XC 25 CLAMP U3XA 26 \_o XE 3 ⊔2XA 25 O---O 25 \_o XE 5 U3XC 26 \_o×Е в U2XC 27 \_ XE 7 \_o XE s FIELD 1 \_o U3XA 28 U2XA 28 0----0 U4XC 27 \_\_ XE 11 OUZXE 28 \_\_\_\_\_O ST34 U3XC 28 \_\_\_\_O XE 12 O U4XC 28 VERT ID LED U2XA 27 0-⊕ U3XA s \_O XE 13 R203 4K75 \_\_o XE 15 O 29 OUZXC 29 → TCXO OFF MII/BETA тхтэ > U4XC 38 O\_\_\_\_\_ —**⟨INT**/EXT U4XC 31 VERT ID ⊸о ⊔зхс ь SWB U2XC 31 \_O U3XC 1 OUZXC 32 \_\_\_ U3XC 2 ⊔4XA 32 o— U2XA 32 0----REMOTE U3XA 32 U3XC 32 FIRST USED IN. PM5643 PCB REF., 4008 117 0568 1 90-23-28 THIS SCHEMATIC DIAGRAM ALSO COVERS: MAINBOARD 4008 109 7245 2 93-82-08 4008 109 7380 3 94-08-02 NAME: GBG/SM/ML/SMa 3 SH SH 130 - 3 2 92-12-02 

78.78