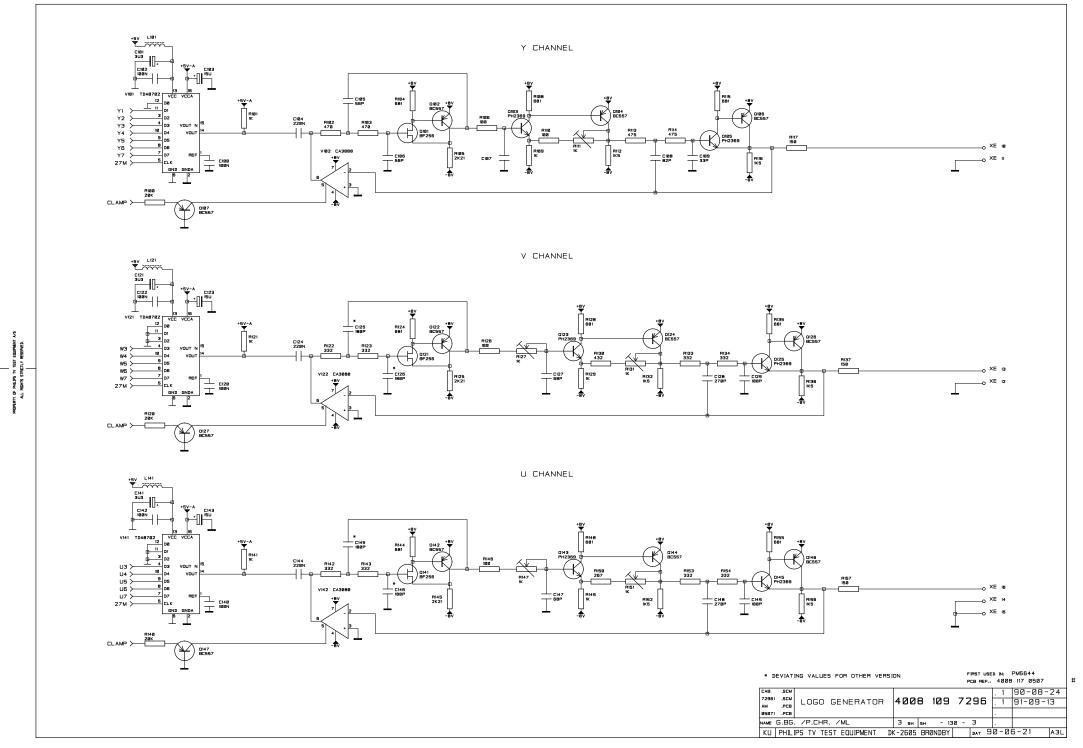
TIME CLOCK D2 +5V BAT85 DI BATB5 XD 8 0 +3V CPU ADDR. ROM LATCH - C3 - 228N → ADDR → DATA V2 PCF8583 V3 8831 V4 74HC573 OSCI VDD KTALI 2 OSCO DI 3 DI → סו → A1 $\stackrel{\longrightarrow}{\rightarrow}$ D1 $\stackrel{\longrightarrow}{\rightarrow}$ D2 D1 12 D1 13 D2 15 D3 16 D4 17 D5 18 D6 D7 D7 \rightarrow D2 → A2 → D3 EA < (EA 04 15 05 13 06 12 EN \rightarrow A5 \rightarrow A6 \rightarrow A7 ightarrow D5 TIMECODE DATA > XD 5 OFIELDI → D6 \rightarrow D7 → AB 00 PP2 00 PP3 → 49 **→** A1Ø -00 PP4 TEST SELECT \rightarrow A11 → A12 → A13 SCL → A14 SDA V6-A 74HC00 PSEN > XE 3 O MRES & → PSEN XD 8 0 RXD XD 7 0 TXD - 11 TXD TIME CODE INPUT 12 INTE XD 2 O VZ & R23 47K5 TIMECODE INT > CLAMP > XD 3 CLAMP & R3Ø 6K82 C78 IU XE 1 O TIME 1 → TIMECODE INT TIMECODE DATA XE 2 TIME 2 D28 BAW62 D21 BAW62 A14 AI5 > V9-A 74ACØØ R27 -392K VIØ-A 74ACØØ → LD PORTI → LD PORT2 XD 16 0 13.5MHZ > EN DUAL PORT V9-8 74ACØØ & XD 12 O FH REF V6-D 74HC88 & TEXT BLANK XD 18 O BLANK2 VIØ-D 74ACØØ XD 4 o PI <u>13</u> & XD s O RI XE 8 - C(2-7) - 188N XE 7 FIRST USED IN: PM5644 PCB REF.: 4008 117 0507 中端 72961 .SCM 4008 109 7296 LOGO GENERATOR 1 91-01-29 AM .PCB 85871 .PCB 1 91-09-13 1 92-11-23 NAME G.BG. /P.CHR. /ML/SMa З вн вн - 130 - 1 рат 90-06-21 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY

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V13 74HC161 •5∨ 27M≻ V9-D 74ACØØ SHIFT LOAD > * V21 74HC166 & QC 12 QD RC OUT CP C CC v2 ≻ TEXT BLANK > FH > D5 (0 D5 (1 RI > 19 PG7 V28 22VI8 VIS +5V 22VIB 24 1 IB/CLK VII 74HC574 08 18 SH2
01 18 SH1
02 17 SH2
03 16 SH3
04 15 SH4
05 14 SH5
08 13 SH6 19 SHØ → H4 → Y2 ** V25 74HCI64 → н6 → Y3 → нв → Y4 → H1Ø 01 4 02 5 03 6 04 18 05 11 06 12 01 02 . → Y5 , Э ни → ня T7 ≻ 03 6 04 18 05 11 06 12 07 13 03 6 04 10 05 11 06 12 07 13 12 SH7 → H7 BANK > *5V 8 CP MR +5V 8 CP *5V 08 15 H5
09 14 SHIFT LOAD VI9 27128 PBØ LD PORTI > VI2 74HC574 19 SH8 18 SH9 12 14 +5V 9 MR CP CE D6 18 PB7 Q8 Q1 D1 3 D1 D1 3 D1 C1
D2 4 D2 C2
D3 5 D3 C3
D4 6 D4 C4
D5 7 D5 C5
D6 8 D6 C6
D7 9 D7 CLK 17 SHIØ → SEL Ø 16 SHII 02 21 RIDZ SEL 1 → W4 → W5 → W6 → W7 15 SELØ 01 22 14 SEL1 13 START $\rightarrow va$ → V1 → V2 12 BANK SEL ÷ vэ → из → V4 20 22 → U4 → U5 LD PORT2 > → v5 → BANK * V23 74HC166 08 15 U6 BANK > *5V V28 27128 → IDX VI > AI ⊥ RGB/YUV DELAY IDX > +5V Y R48-B R48-C 2K7 R48-A 2K7 VI7 CY7C138 EN DUAL PORT > CE R/W T7 >-45 BUSY BANK > 44_00 SP12 AØ > A1 > < н5 A2 > ≺ H6 < H7 ∠ EA ADDR ≺нв A5 > -ζ н9 A6 > < H10 ≺ H11 AB > ≺SEL Ø A9 > 1/07 1/08 **Э**ть 1/05 → T5 1/04 * 74F166 IN 4008 109 75820 → T4 FIRST USED IN: PM5644 1/03 . → тэ ** NOT MOUNTED IN 4008 109 75820 PCB REF.: 4008 117 0507 1/02 → T2 1/08 1 90-08-24 → T1 72961 .SCM → TØ LOGO GENERATOR 1 91-09-13 4008 109 7296 AM ,PCB 85871 .PCB NAME G.BG. /P.CHR. /ML З ян ян - 130 - 2 DAT 90-06-21 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRØNDBY A3L

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