

CPU ADDR. ROM RAM LED DRIVERS LED INDICATORS LATCH HD ≻ XTAL1 DØ) D1 3 D1 D2 D2 D3 5 D3 D4 6 D4 D5 7 D5 D6 8 D6 D7 9 D7 A2 > U3XC 29 O FIELDI A5 > A6 ≻ A7 ≻ V17 74HC574 A6 A8 A7 A8 A7 A8 A9 A9 A10 A10 23 A10 O PP2 2 P1.8 P1.1 O PP3 3 P1.2 SYNC MODE SELECT CB2 — R35 10K 4 D2 5 D3 6 D4 7 D5 8 D8 B D7 TEST MODE ENABLE D2 > A11 23 A18 A11 A12 00 PP4 R31 18K ID > D3 ≻ Q3 D4 > Q4 00 PP5 A13 26 A13 D5 > SCL U3XA 19 O SCL U3XC 19 O SDA A15 28 A15 V15-A 74HCØØ PSEN > MRES U3XC 15 0-LD.LEDT > A14 > & → PSEN A15 > ADDRESS MRES DECODER U3XA 2 O TXD II TXD VI4 74HCI38 12 INTE → LD.LEDT U3XA 12 O-→LD.LED2 **4** & A2 > → CD.LED3 U3XA 28 CLAMP → CD.CONTROL Y5 18 → CD.CHT → CLAMP G2A G28 → CD.CH273 DØ >-D1 >-D2 >-5 D3 B D6 B D7 D5 >-D6 >-D7 >-LD.LEDZ > SUB-UNIT E10 XD 4 OX) a OX XD 6 XD 2 KEYBRD 512-A XE 5 XE 2 XE 6 $\begin{array}{c} \longrightarrow \text{ DØ} \\ \longrightarrow \text{ D1} \\ \longrightarrow \text{ D2} \\ \longrightarrow \text{ D3} \\ \longrightarrow \text{ D4} \end{array}$ S14 - A S15-A DI >-D2 >-02 D3 > S16-A 5 D4 7 D5 8 D6 04 15 D4 D5 D6 D7 D5 >-D6 >-D7 >-⊔3XC 2 ₀— 05 REMOTE 0 U3XC 4

INT /EXT 0 U3XC 3

VERT ID OFF 0 U3XA 5 uaxc ₁ o− SWB VERT ID SELECT FIRST USED IN: PM5644/U3 PCB REF.: 4008 117 0506 THIS SCHEMATIC DIAGRAM ALSO COVERS 72921 .SCM COMPOSITE 4008 109 7292 1 91-02-04 4008 109 7378 A .PCB GENERATOR 05062 .PCB NAME G.BG. 5 вн вн - 130 - 2 DAT 90-01-16 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRØNDBY

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TEST BLANK > -0 U3XC 25 SEL. IN > BLANK 0 U3XC 24 V31 74HC574 [V32 74HC574 → APII D2 D3 02 D2 02 → AP12 → API3 03 DЭ QЭ 04 → AP14 7 B D6 → AP15 D5>-Q6 → AP16 → AP17 HALF LINE ED.CHT > FIRST/EAST 18/CLK SEL. OUT > APØ> * V34 74HC574 03 20 AP2> TEST BLANK 04 05 18 → PROM ADDRES AP3> AP4 > AP5 07 16 03 03 AP7 04 D4 Q4 APB > 05 D5>-AP9> D6 Q6 ры≻ SEL. OUT > □D.CH2/3 >--SH-2 AP21 SPII → AP2Ø → AP19 V39 74HCl38 V35 74HC574 CLK 1 18/CLK 00 23 APØ > ENØ 02 23 API 01 22 API 02 21 AP2 03 AP3 04 IB AP4 → ENT IB LSB OUT CH2.OFF > ENZ CH3.OFF D2 Q2 02 → EN3 CH1.SY.OFF D3 D4 DЗ 03 03 05 IB CARRYI 15 MSB IN 14 MSB OUT CH2.SY.OFF 8 D4 7 D5 Q4 Q5 CH3.SY.OFF D5 D6 D5>-13 FIRST/EAST SYNC.GATE D6>-12 HALF LINE HALF BLANK B CARRYZ 10 19 → SEL. IN ED.CONTROL > → APØ I → API → AP2 . → AP3 → AP4 \rightarrow CLK.CH1 \rightarrow CLK.CH2 V38 22V18 24 CLK 1 18/CLK SH-1 CLK > → AP5 APØ 2 → AP6 AP1 3 → AP7 AP2 → APB AP3 → AP9 AP4 B 15 ightarrow SEL. OUT V6-D 74HC84 CARRYI 7 18 FIRST/CAST 8 17 9 1 ___0U3XC 28 HALF LINE 9 18 CARRY2 EH REE →FH.REF SH-1 FIRST USED IN: PM5664/U3 * NOT MOUNTED IN SOME VERSIONS PCB REF.: 4008 117 0506 COMPOSITE THIS SCHEMATIC DIAGRAM ALSO COVERS: 72921 .SCM 1 91-02-04 4008 109 7292 GENERATOR 4008 109 7378 A .PCB 05062 .PCB 1 91-02-14 NAME G.BG. /ML 5 ян ян - 130 - 3 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT 89-12-23

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DATA Y ENN > FNZ > EN3 > VIØ2 74HC574 VIØ4 74HC574 V106 74HC574 V188 74HC574 VIBI 27101 VIØ3 27181 VIB5 27181 VIB7 27101 B ENT YØA YIA Y2A ¹⁹→ D1Ø2 19 → D102 18 → D103 ¹⁹ → D1Ø2 QØ AP2 → DIØ2 AP2 > 11 A1 A2 AP3 SIN De 14 3 D1 15 4 D2 D3 17 5 D3 D4 18 6 D4 D5 D6 28 8 D6 D7 7 D7 02 18 D102 19 D103 02 17 D104 03 15 D105 PROM ADDRESS АРЭ AP3 AP3 AP4 | 18 AI AI AP4 | 18 AI AP5 | 9 AI AP5 | D2 | 15 4 D2 | 17 5 D3 | 18 6 D4 | 19 7 D5 D6 D7 | 20 8 D7 | 17 5 D7 | 18 5 D6 D7 | 18 5 D7 | 18 18 A2 02 17 D104 03 15 D105 04 15 D106 05 14 D107 08 13 D108 AP4 AP5 AP6 AP7 D2 15 4 18 A2 D2 15 4 AP4 D2 02 Y2A 16 3 Y3A 15 18 Y8B 14 11 Y1B 13 12 Y2B 9 43 AP5 QЗ 8 A4 A5 A5 A6 A7 04 05 06 15 → D1Ø6 Q4 AP6 14 D107 AP7 ZB DIØ1 07 12 13 Y2B CLK 07 1 12 → D1Ø9 12 → D1Ø9 AP9 CLK 07 APIØ > 27 AP11 > 26 A9 API1 28 AS API2 23 AS API2 23 AS API3 28 AS API3 28 AS API5 28 AS AP12 23 A18
AP13 25 A11
AP14 4
AP15 28 A13 AP12 23 A18 AP13 25 A11 AP14 4 A12 AP16 28 AP16 29 AP17 3 AP17 3 AP15 28 AP16 29 AP16 29 AP15 ≻ AP16 29 AP17 3 AP17 3 API7 > APØ > APIB > APIB > API > APIG > APIG > APIG ` APIG S APIG > APZØ > APZØ > APZØ > AP20 > APZØ > AP21 > AP21 > AP21 > AP21 > AP21 > CLK.CH1 > CLK.CHI > CLK.CHI > CLK.CH1 > CLK.CH1 > FH.REF ENØ > ENZ > V282 74HC574 V284 74HC574 V283 27181 DØ → D202 DØ D2Ø2 AP2 > AP2 08 23 +5V 01 22 АРЭ АРЗ D2Ø3 D204 9 43 02 2 AP5 AP6 AP7 AP5 D205 75 | 14 | D205 | D206 | D207 | 03 28 8 A4 7 A5 8 A8 5 A7 D282 > → v6 AP6 AP7 04 19 D203 > → v5 DATA V D2Ø4 > → v4 AP8 AP9 APIØ APII API2 AP8 CLK 07 CLK 07 D205 ÷ vэ AP9 D209 07 16 27 AB D206 > → v2 AP10 > APII 25 A9 A18 D207 > 08 ¹ . Vı 18 19 DZØB > AP13 25 AII AP13 D209 > APØ AP14 4 AP15 28 A13 AP15 AP16 29 A14 AP17 3 A15 API6 API7 APIB > APIB > APIQ \ APIG > AP20 > AP20 > AP21 > AP21 > CLK.CH2 > CLK.CH2 > ENØ > ENZ > V382 74HC574 V384 74HC57 V303 27101 V305 22VI0 AP2 | 12 | AB
AP3 | 11 | AB
AP3 | 10 | AB
AP4 | 90 | A2
AP5 | 90 | A3
AP6 | 80 | A4
AP7 | 7 | A5
AP8 | 5 | A7
AP10 | 22 | A8
AP11 | 28 | A8
AP12 | 23 | A8
AP13 | 28 | A8
AP14 | 4 | A1
AP14 | 4 | A1
AP16 | 28 | A1
AP16 | 28 | A1
AP17 | 3 | A6
AP18 | 28 | A1
AP18 | 28 | A1
AP18 | 28 | A1 AP2 > → D302 → рзиг D302 18 D303 17 D304 16 D305 18 → D3Ø3 17 → D3Ø4 01 01 D1 14 3 D1 15 4 D2 D3 18 6 D4 D5 19 7 D5 D6 21 9 D7 AP3 | 11 AI AI AI AP4 | 18 A2 AP5 | 9 A3 AP6 | 6 A4 AP7 | 7 A5 AP8 | 6 AF9 | 5 A7 AP10 | 27 A8 AP10 | 27 **Q2** Q3 Q4 Q5 QЭ → рзø5 → рэй5 Ц7 03 D3Ø5
04 D3Ø6
05 H D3Ø7
06 D3Ø9
07 D3Ø9 15 → D3Ø6 ל כמבת → U6 → U5 → U4 → D3Ø7 C ENEC DATA LI D3Ø8 06 D3Ø4 > 07 шз 305 > D306 > → U2 APIØ > 27 AB
APII > 26
APII > 23
API2 > 25
API3 > 25
API4 > 4
API5 > 28
API6 > 29
API6 > 29
API7 > 3
API7 > 3
API7 > 3 9 18 18 19 11 118 13 111 D307 > → ш D3Ø8 ≻ → ⊔ø D3Ø9 >─ _*5V APIB > APIB > NOT MOUNTED ON 4008 109 7378 AP19 AP19 > AP20 AP20 AP21 > AP21 > FIRST USED IN: PM5644/U3 CLK.CH3 > CLK.CH3) PCB REF.: 4008 117 0506 1 90-07-13 THIS SCHEMATIC DIAGRAM ALSO COVERS: COMPOSITE 72921 .SCM 4008 109 7292 4008 109 7378 1 91-02-04 GENERATOR PCB PAL ID м.в. U3XC 23 0-05062 .PCB NAME G.BG. 5 вн вн - 130 -DAT 89-12-28 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRØNDBY A3L

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Y CHANNEL OUTPUT AMP. CHI DC-OFFSET CI85 | 68P V112 TRWIØ12 RII5 IK GAIN ADJUST C186 CLK > HUE.E OUT DIDE ושום D102 R122 75 Ø.1% D103 ___ U4XA 19 D105 VREE CIIB CIII 330P C112 338P C114 330P C115 C119 C117 278P C119 C117 פשות R188 7K5 _O U4XC 29 D107 C187 228P _____C188 2N7 _0 U4XA 29 DIØB L 182 5.5UH VEEA __O U4XC 31 DGND VEED L 183 5.5UH ___ U4XA 31 R968 68R1 ‡ C183 = C182 -5.2V __O U4XA 26 __O U4XA 27 _0 U4XC 27 0914 PH2369 D281 BAW62 ____0 U4XA 28 U4XC 22 0 TXT Y TXTY IN _0 U4XC 28 R62 1K3 R216 301 R217 511 __0 U4XA 3Ø ___0 U4XC 38 __0 U4XA 32 _0 U4XC 32 R204 16K2 CHROMA OUTPUT R219 IK R55 332 R213 825 GAIN (S-VHS) R206 500 V213 TCA248D OF PHILIPS TV T RIGHTS STRICTLY L282 R288 188 _O U4XC 15 ___ U4XA 15 0-2 11 8280 V4 > ____C218 7 6 82 v5 ≻ V6 ≻ CHROMA PHASE V- COMP OPTIONAL R282 2K67 +8V P53 10K DELAY ADJUST _____ C86 L286 TXT V | U4XC 8 0-R54 4K75 SUBC , I U4XC 3 O-| U4XC 1 0-__0 U4XA 11 D381 ¥ 3XFSUP 2XFSUB _0 U4XC II R57 U4XA 2 0-R52 3K32 L284 R51 IK | U4XA 3 0-L285 _0 ∐4XA 12 ___ U4XA 13 . — О ____ U4XC 13 ___O U4XA 16 C131 2P2 R58 1K5 _____ C89 ___ U4XC 16 -90DEG __0 U4XA 17 R304 16K2 —0 U4XC 17 —0 U4XA 21 L303 C92 56P C132 __ U4XE 21 V313 TCA248D -0 U4XA 4 __O U4XC 4 R387 5K11 ___ U4ХА Б UI > 11 B7 ___O U4XC 5 __0 ⊔4XA 7 U4XC 7 R3Ø9 IK3 R310 IK5 __ U4XC 8 C30 C389 188P NOT MOUNTED ON VR-4008 109 7378 16 C302 4N7 V- COMP R303 2K67 R382 2K67 FIRST USED IN: PM5644/U3 +<u>B</u>∨ PCB REF.: 4008 117 0506 **DEVIATING VALUES FOR OTHER VERSIONS EABCD .SCM COMPOSITE THIS SCHEMATIC DIAGRAM ALSO COVERS: 72921 .SCM 4008 109 7292 1 91-06-26 GENERATOR 4008 109 7378 A .PCB | U4XA 1 0 TXT U м.в. 05062 .PCB 1 92-03-12 NAME G.BG. 5 sh sh - 130 - 5 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY | DAT 90-01-13

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75%