

CPU ADDR. ROM RAM LED DRIVERS LED INDICATORS LATCH HD ≻ XTAL1 D1 3 D1 D2 D2 D3 5 D3 D4 6 D4 D5 7 D5 D6 8 D6 D7 9 D7 A2 > U3XC 29 0 2-WIRE SELECT A5 > V17 74HC574 A6 ≻ A7 ≻ A6 A8 A8 A8 A9 A8 A10 21 A10 23 A10 O PP2 2 P1.8 P1.1 O PP3 3 P1.2 SYNC MODE SELECT C82 ---R35 10K 4 D2 5 D3 6 D4 7 D5 8 D8 B D7 TEST MODE ENABLE D2 > A11 23 A18 A11 A12 00 PP4 R31 18K ID > D3 ≻ Q3 D4 > Q4 00 PP5 A13 26 A13 D5 > SCL U3XA 19 O SCL U3XC 19 O SDA A15 28 A15 V15-A 74HCØØ PSEN > MRES U3XC 15 0 LD.LEDT > A14 > & → PSEN A15 > ADDRESS MRES DECODER U3XA 2 O TXD II TXD VI4 74HCI38 12 INTE → LD.LEDT U3XA 12 O- $\rightarrow$  LD.LED2 **4** & A2 > → CD.LED3 U3XA 28 O CLAMP → CD.CONTROL Y5 18 → CD.CHT → CLAMP G2A G28 → CD.CH273 DØ >-D1 >-D2 >-D5 >-D6 >-D7 >-LD.LEDZ > SUB-UNIT o XD 4 ŶхDв XD 5 XD 2 KEYBRD XE 5 XE 2 +5∨ ▼  $\begin{array}{c} \longrightarrow \text{ DØ} \\ \longrightarrow \text{ D1} \\ \longrightarrow \text{ D2} \\ \longrightarrow \text{ D3} \\ \longrightarrow \text{ D4} \end{array}$ S14 - A DI >-D2 >-02 D3 > S16-A 5 D4 7 D5 8 D6 04 15 D4 D5 D6 D7 D5 >-D6 >-D7 >-⊔3XC 2 <sub>0</sub>— 05 REMOTE 0 U3XC 4

INT /EXT 0 U3XC 3

VERT ID OFF 0 U3XA 5 uaxc ₁ o− SWB VERT ID SELECT FIRST USED IN: PM5644/U3 PCB REF.: 4008 117 05751 93-03-26 THIS SCHEMATIC DIAGRAM ALSO COVERS: 76841 .SCM COMPOSITE 4008 109 7684 4008 109 7378 .PCB GENERATOR 05751 .PCB NAME G.BG./ML 5 вн вн KU PHILIPS TV TEST EQUIPMENT DK-2605 BRØNDBY DAT 93-03-26 A3L

TEST BLANK > -0 U3XC 25 SEL. IN > BLANK 0 U3XC 24 V31 74HC574 [ V32 74HC574 → APII D2 D3 . → AP12 02 D2 02 → API3 03 DЭ QЭ 04 → AP14 7 B D6 → AP15 Q6 → AP16 → AP17 HALF LINE CD.CHT > FIRST/EAST 18/CLK SEL. OUT > APØ> \* V34 74HC574 03 28 AP2> TEST BLANK → PROM ADDRES AP3> AP4 > AP5 07 16 03 03 AP7 04 D4 Q4 APB > 05 D5>-AP9> D6 Q6 ры≻ SEL. OUT > □D.CH2/3 >--→ AP21 → AP2Ø → AP19 V39 22V10 SPII > ENØ → ENT  $\rightarrow$  EN2 V35 74HC574 → EN3 CLK 1 18/CLK 00 23 APØ 02 23 API 01 22 API 02 21 AP2 03 AP3 04 IB AP4 IB LSB OUT CH2.OFF CH3.OFF D2 Q2 02 CH1.SY.OFF D3 D4 DЗ 03 03 05 IB CARRYI 15 MSB IN 14 MSB OUT CH2.SY.OFF 8 D4 7 D5 Q4 Q5 CH3.SY.OFF D5 D6 D5>-13 FIRST/EAST SYNC.GATE D6>-12 HALF LINE HALF BLANK B CARRYZ 18 → SEL. IN ED.CONTROL > → APØ → API → AP2 . → AP3 → AP4  $\rightarrow$  CLK.CH1  $\rightarrow$  CLK.CH2 V38 22V18 24 CLK 1 18/CLK SH-1 CLK > → AP5 APØ 2 → AP6 AP1 3 → AP7 AP2 → APB AP3 → AP9 AP4 B 15 ightarrow SEL. OUT V6-D 74HC84 CARRYI 7 16 17 18 17 9 1 \_\_\_0U3XC 28 HALF LINE 9 18 CARRY2 EH REE →FH.REF SH-1 FIRST USED IN: PM5664/U3 \* NOT MOUNTED IN SOME VERSIONS PCB REF.: 4008 117 05751 1 93-03-26 COMPOSITE THIS SCHEMATIC DIAGRAM ALSO COVERS: 76841 .SCM 4008 109 7684 1 GENERATOR 4008 109 7378 .PCB Ø5751 .PCB NAME G.BG. /ML 5 sн sн - 130 - 3 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY | DAT 93-03-26 A3L

DATA Y ENN > ENT > FNZ > EN3 > VIØ2 74HC574 VIØ4 74HC574 V106 74HC574 V188 74HC574 VII8 74HC574 1 D8 13 2 D8 VIBI 27101 VIØ3 27181 VIB5 27181 VIB7 27101 B ENT YØA YIA Y2A <sup>19</sup> → D1Ø2 19 → D102 18 → D103 <sup>19</sup> → D1Ø2 QØ AP2 → DIØ2 AP2 > 11 A1 A2 AP3 SIN 02 | D102 | O10 | PROM ADDRESS АРЭ AP3 AP3 D1 | 15 4 D2 | 17 5 D3 | 18 6 D4 | 19 7 D5 | 19 7 D7 | 17 5 D6 | 19 7 D7 | 19 7 D7 ) IB A1 AP4 | 18 AI AI AP4 | 18 AI AP5 | 9 AI AP5 | D2 | 15 4 D2 | 17 5 D3 | 18 6 D4 | 19 7 D5 D6 D7 | 20 8 D7 | 17 5 D7 | 18 5 D6 D7 | 18 5 D7 | 18 18 A2 Q2 17 Q3 16 Q4 15 AP4 AP5 AP6 AP7 D2 15 4 18 A2 D2 15 4 AP4 → DIØ4 D2 02 Y2A 16 3 Y3A 15 18 Y8B 14 11 Y1B 13 12 Y2B 9 43 AP5 . → DIØ5 QЗ 8 A4 A5 A5 A6 A7 04 05 06 15 → D1Ø6 Q4 → D106 → D107 AP6 05 14 D107
06 13 D108 14 D107 AP7 Q5 ZB DIØ1 12 DIØ9 07 12 13 Y2B CLK 07 1 12 → D1Ø9 12 → D1Ø9 AP9 CLK 07 27 AB APIØ > AP11 > 26 A9 API1 28 AS API2 23 AS API2 23 AS API3 28 AS API3 28 AS API5 28 AS AP12 23 A18
AP13 25 A11
AP14 4
AP15 28 A13 API1 > 23 API2 > 25 API3 > 25 API4 > 4 API4 > 4 AP12 23 A18 AP13 25 A11 AP14 4 A12 AP16 28 AP16 29 AP17 3 AP17 3 AP15 28 AP16 29 AP16 29 AP15 ≻ AP16 29 AP17 3 AP17 3 API7 > APØ > APIB > APIB > API > APIG > APIG > APIG ` APIG S APIG > APZØ > APZØ > APZØ > AP20 > APZØ > AP21 > AP21 > AP21 > AP21 > AP21 > CLK.CH1 > CLK.CHI > CLK.CHI > CLK.CH1 > CLK.CH1 > FH.REF ENØ > ENZ > V282 74HC574 V284 74HC574 V283 27181 DØ DØ D2Ø2 AP2 > → D2@2 AP2 08 23 +5V 01 22 АРЭ АРЗ D2Ø3 AP4 D204 02 2 9 43 AP5 AP6 AP7 AP5 D205 75 | 14 | D205 | D206 | D207 | 03 28 8 A4 7 A5 8 A8 5 A7 D282 > — > ∨6 AP6 AP7 04 19 D203 > → v5 DATA V D2Ø4 > → v4 AP8 AP9 APIØ APII API2 AP8 CLK 07 CLK 07 D205 OB 17 ÷ vэ AP9 D209 07 18 27 AB D206 > → v2 AP10 > AP11 > 26 AP12 > 23 AP12 > AP D207 > 08 <sup>1</sup> . Vı 18 19 DZØB > → vø AP13 25 AII AP13 D209 > APØ AP14 4 AP15 28 A13 AP15 AP16 29 A14 AP17 3 A15 API6 API7 APIB > APIB > APIQ \ APIG > AP20 > AP2Ø > AP21 > AP21 > CLK.CH2 > CLK.CH2 > ENØ > ENZ > V382 74HC574 V384 74HC57 V381 27181 V303 27101 V305 22VI0 AP2 > → D302 → рзиг D302 18 D303 17 D304 16 D305 ∑-11 A1 18 → D3Ø3 17 → D3Ø4 01 AP3 01 D1 14 3 D1 15 4 D2 D3 18 6 D4 D5 19 7 D5 D6 21 9 D7 AP3 | 11 AI AI AI AP4 | 18 A2 AP5 | 9 A3 AP6 | 6 A4 AP7 | 7 A5 AP8 | 6 AF9 | 5 A7 AP10 | 27 A8 AP10 | 27 **Q2** Q3 Q4 Q5 QЭ → рзø5 → рэ05 U7 03 D3Ø5
04 D3Ø6
05 H D3Ø7
06 D3Ø9
07 D3Ø9 15 → D3Ø6 ל כמבת → U6 → U5 → U4 → D3Ø7 C ENEC DATA LI D3Ø8 06 D3Ø4 > 07 шз D3Ø5 > D306 > → U2 APIØ > 27 AB
APII > 26
APII > 23
API2 > 25
API3 > 25
API4 > 4
API5 > 28
API6 > 29
API6 > 29
API7 > 3
API7 > 3
API7 > 3 9 18 18 19 11 118 13 111 D307 > → ш D3Ø8 ≻ → ⊔ø D309 >-\_\*5V APIB > APIB > NOT MOUNTED ON 4008 109 7378 AP19 AP19 > AP20 AP20 AP21 > AP21 > FIRST USED IN: PM5644/U3 CLK.CH3 > CLK.CH3 ) PCB REF.: 4008 117 05751 THIS SCHEMATIC DIAGRAM ALSO COVERS: 93-03-26 COMPOSITE 76841 .SCM 4008 109 7684 4008 109 7378 GENERATOR PCB PAL ID  $\rightarrow$  ID м.в. U3XC 23 0-05751 .PCB NAME G.BG./ML 5 вн вн - 132 -KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT 89-12-28

Y CHANNEL OUTPUT AMP. CHI DC-OFFSET CI85 | 68P V112 TRWIØ12 RII5 IK GAIN ADJUST C186 CLK > TXTY IN > 4 4 3.3UH DIDE ושום D102 D103 \_\_\_ U4XA 19 D105 Cite CIII 330P C112 338P C114 330P C115 C119 C117 278P C119 C117 פשות R188 7K5 U4XC 29 D107 C187 228P \_\_\_\_\_C188 2N7 \_0 U4XA 29 DIØB L 182 5.5UH VEEA \_\_O U4XC 31 DGND VEED L 183 5.5UH \_\_\_ U4XA 31 R968 68R1 ± €183 = C182 -5.2V \_\_O U4XA 26 \_\_O U4XA 27 \_0 U4XC 27 0914 PH2369 D281 BAW62 \_0 U4XA 28 U4XC 22 0 TXT Y TXTY IN \_0 U4XC 28 R62 1K3 R216 301 R217 511 \_\_0 U4XA 3Ø \_\_\_0 U4XC 38 \_\_0 U4XA 32 R204 16K2 CHROMA OUTPUT R219 IK R55 332 GAIN (S-VHS) R206 500 V213 TCA248D OF PHILIPS TV T RIGHTS STRICTLY L282 R288 188 \_O U4XC 15 \_\_\_ U4XA 15 0-2 8280 V4 ≻ \_\_\_\_C218 7 6 82 v5 ≻ V6 ≻ CHROMA PHASE V- COMP OPTIONAL R282 2K67 +8V P53 10K DELAY ADJUST \_\_\_\_\_ C86 L286 TXT V | U4XC 8 0-R54 4K75 SUBC , I U4XC 3 O-U4XC 1 0-\_\_0 U4XA 11 D381 ¥ 3XFSUP 2XFSUB \_0 U4XC II R57 U4XA 2 0-R52 3K32 L284 R51 IK U4XA 3 0− L285 \_0 ∐4XA 12 \_\_\_ U4XA 13 . — О \_\_\_\_ U4XC 13 \_\_\_O U4XA 16 C131 2P2 R58 1K5 \_\_\_\_\_ C89 \_\_\_ U4XC 16 -90DEG \_\_0 U4XA 17 R304 16K2 —0 U4XC 17 —0 U4XA 21 L303 C92 56P C132 \_\_ U4XE 21 V313 TCA248D -0 U4XA 4 \_\_O U4XC 4 R387 5K11 \_\_\_ U4ХА Б UI > 11 B7 \_\_\_O U4XC 5 \_\_0 ⊔4XA 7 —0 U4XC 7 —0 U4XA 8 L DATA U → U3 → R3Ø9 IK3 R310 IK5 \_\_ U4XC 8 C30 C389 188P NOT MOUNTED ON 4008 109 7378 16 C302 4N7 V- COMP R3Ø3 2K67 R382 2K67 FIRST USED IN: PM5644/U3 +<u>B</u>∨ PCB REF.: 4008 117 05751 \*\*DEVIATING VALUES FOR OTHER VERSIONS EABCD .SCM 1 93-03-26 COMPOSITE THIS SCHEMATIC DIAGRAM ALSO COVERS: 76841 .SCM 4008 109 7684 GENERATOR 4008 109 7378 A .PCB | U4XA 1 0 TXT U м.в. 05751 .PCB NAME G.BG. 5 вн вн - 132 -5 KU PHILIPS TV TEST EQUIPMENT DK-2605 BRONDBY DAT 93-03-26 A3L