

Jorge Azevedo

Electronics Engineer



Availability: July 2013 onwards

Looking for: Real-Time Linux

Embedded Systems Development

E-mail: jorge.amado.azevedo@gmail.com

Tel: (+351) 936270876

Experience

UNISOL	Universidade de Aveiro, Portugal	
RESEARCHER		
DATES	March 2012 - Present	
DESCRIPTION	Development of datalogging infrastructure. Embedded control system prototype. Deployment and administration of a MATLAB computational cluster, OpenStack cloud computing environment.	
Xenomai Lab	www.xenomailab.org	
SOFTWARE ENGINEER		
DATES	Jan 2011 - Present	
DESCRIPTION	Deployment and administration of a MATLAB computational cluster, OpenStack cloud computing environment.	
MSC COLABORATOR		
DATES	Sep 2012 - Present	
DESCRIPTION	Technical coordinator for Thesis entitled TODO.	
DATES	Jan 2012 - Dez 2012	
DESCRIPTION	Technical coordinator for Thesis entitled "Adaptive Control Algorithms using Xenomai Lab".	

Publications

SEPTEMBER 2012	Xenomai Lab - A Platform for Digital Real-Time Control
IN	INFORUM 2012 Proceedings

Education

2005–2012	M.Sc. Electronics and Telecommunications Engineering
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INSTITUTION	Universidade de Aveiro Campus Universitário de Santiago, 3810-193 Aveiro, Portugal
THESIS	Xenomai Lab - A Platform For Digital Real-Time Control
DESCRIPTION	5 year degree (Bachelor and Master's included). Thesis graded 19/20 after public defense.
2008–2009	Erasmus Exchange Program
INSTITUTION	TU/e Technische Universiteit Eindhoven Den Dolech 2, 5612 AZ Eindhoven, Netherlands
DESCRIPTION	One year spent abroad under the Erasmus Exchange Program.

Languages

PORTUGUESE	Native speaker
ENGLISH	Advanced speaker. Five years of education in the British Council achieving level A in First Certificate of English (2001) and level B in Certificate of Advanced English (2004)
SPANISH	Basic understanding in both written and spoken form.

Technical Skills

PROGRAMMING	Advanced knowledge of C. Medium knowledge of C++, Java and the Android platform, Qt framework for GUI programming/design, Linux kernel modules and filesystems. Basic knowledge of Python, Objective C and iOS, Pascal, BASH scripting, VHDL, MIPS and x86 Assembly assembly language.
ELECTRONICS	Medium Knowledge and hands-on experience with SPICE circuit simulation, Analog and Digital filter design, Microcontrollers. Basic knowledge of Digital circuit design and circuit layout. Extensive hands-on experience with circuit prototyping and signal analysis.
GENERAL	Advanced use of Microsoft Windows and Linux Operating Systems. Extensive experience with Linux real-time variants, such as Xenomai and RTAI. Word processing, spreadsheets and presentations with Microsoft Office, Open Office and L ^A T _E X. Basic experience with administrating Windows and Linux systems and networks. Basic knowledge of classic and modern control engineering

Soft Skills

SOCIAL SKILLS	Experience in multicultural environments Open-Minded Highly responsible and professional
ORGANIZATIONAL SKILLS	Independent research Strong drive for initiative Planning and scheduling of team/individual project.

Additional Skills

DRIVING LICENSE	Category B-I
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