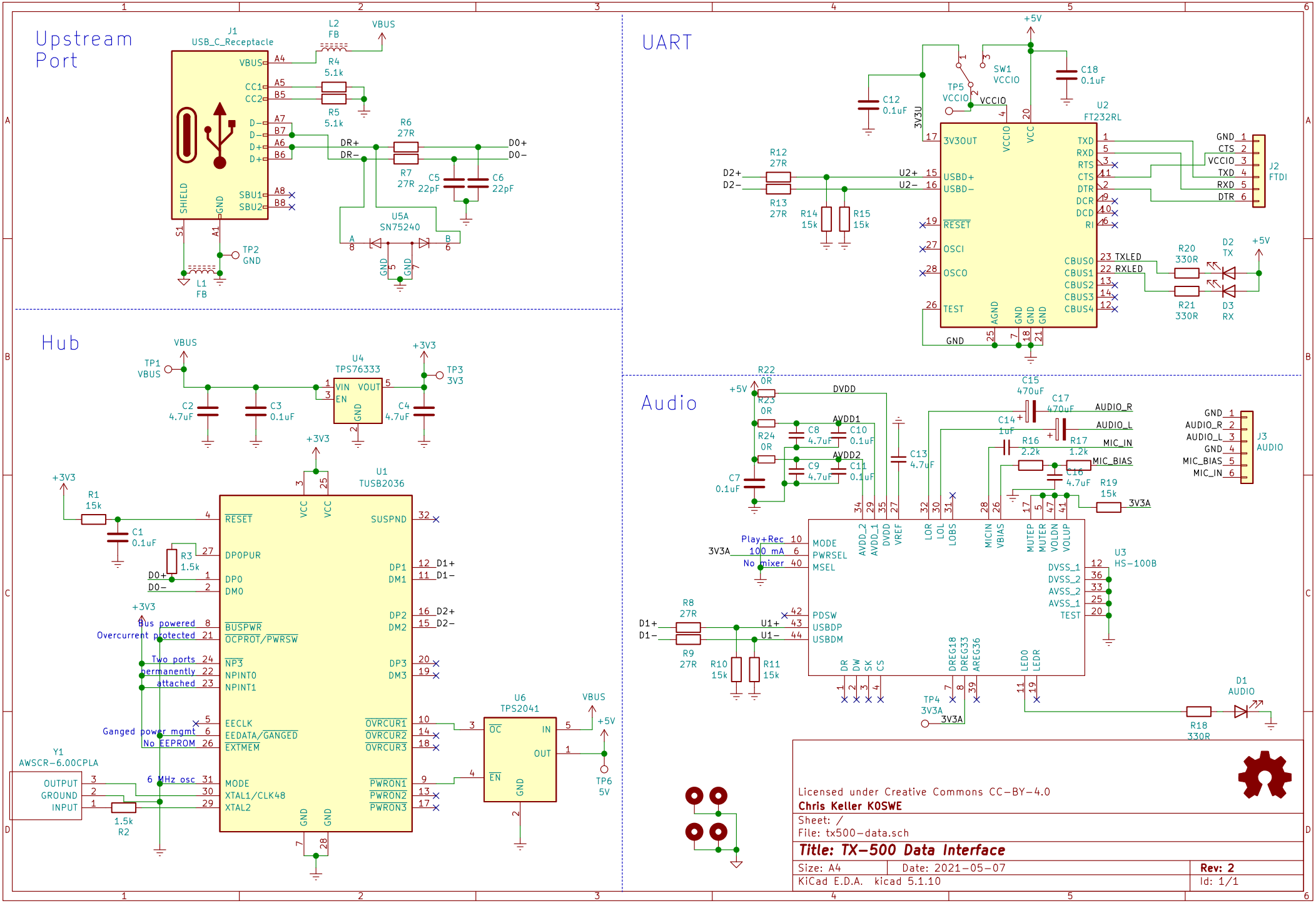
Upstream Port
J1
USB_C_Receptacle
VBUS
CC1
CC2
D−
D+
SHIELD
GND
SBU1
SBU2
L2
FB
R4
5.1k
R5
5.1k
R6
27R
R7
27R
C5
22pF
C6
22pF
DR+
DR−
D0+
D0−
U5A
SN75240
TP2
GND
L1
FB
Hub
TP1
VBUS
U4
TPS76333
VIN
VOUT
EN
GND
+3V3
TP3
3V3
C2
4.7uF
C3
0.1uF
C4
4.7uF
U1
TUSB2036
RESET
DP0PUR
DP0
DM0
BUSPWR
OCPROT/PWRSW
NP3
NPINT0
NPINT1
EECLK
EEDATA/GANGED
EXTMEM
MODE
XTAL1/CLK48
XTAL2
SUSPND
DP1
DM1
DP2
DM2
DP3
DM3
OVRCUR1
OVRCUR2
OVRCUR3
PWRON1
PWRON2
PWRON3
VCC
GND
R1
15k
C1
0.1uF
R3
1.5k
Bus powered
Overcurrent protected
Two ports
permanently
attached
Ganged power mgmt
No EEPROM
6 MHz osc
Y1
AWSCR−6.00CPLA
OUTPUT
GROUND
INPUT
1.5k
R2
U6
TPS2041
OC
IN
OUT
EN
GND
TP6
5V
UART
+5V
SW1
VCCIO
TP5
VCCIO
C12
0.1uF
C18
0.1uF
U2
FT232RL
3V3OUT
USBD+
USBD−
RESET
OSCI
OSCO
TEST
AGND
TXD
RXD
RTS
CTS
DTR
DCR
DCD
RI
CBUS0
CBUS1
CBUS2
CBUS3
CBUS4
R12
27R
R13
27R
R14
15k
R15
15k
D2+
D2−
U2+
U2−
J2
FTDI
GND
CTS
VCCIO
TXD
RXD
DTR
TXLED
RXLED
R20
330R
R21
330R
D2
TX
D3
RX
Audio
R22
0R
R23
0R
R24
0R
C7
0.1uF
C8
4.7uF
C9
4.7uF
C10
0.1uF
C11
0.1uF
AVDD1
AVDD2
DVDD
C13
4.7uF
C15
470uF
C17
470uF
C14
1uF
R16
2.2k
R17
1.2k
C16
4.7uF
R19
15k
AUDIO_R
AUDIO_L
MIC_IN
MIC_BIAS
3V3A
J3
AUDIO
GND
AUDIO_R
AUDIO_L
GND
MIC_BIAS
MIC_IN
U3
HS−100B
MODE
PWRSEL
MSEL
PDSW
USBDP
USBDM
AVDD_2
AVDD_1
DVDD
VREF
LOR
LOL
LOBS
MICIN
VBIAS
MUTEP
MUTER
VOLDN
VOLUP
DVSS_1
DVSS_2
AVSS_2
AVSS_1
TEST
DR
DW
SK
CS
DREG18
DREG33
AREG36
LEDO
LEDR
Play+Rec
100 mA
No mixer
R8
27R
R9
27R
R10
15k
R11
15k
D1+
D1−
U1+
U1−
TP4
3V3A
R18
330R
D1
AUDIO
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Chris Keller K0SWE
Sheet: /
File: tx500−data.sch
Title: TX−500 Data Interface
Size: A4
Date: 2021−05−07
Rev: 2
KiCad E.D.A. kicad 5.1.10
Id: 1/1