

40 MHz bunch crossing rate



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graph TD; A[40 MHz bunch crossing rate] --> B[L0 trigger<br/>High p_T / E_T signatures: μ, e, h, γ<br/>1 MHz detector readout]; B --> C[HLT 1<br/>Displaced high-p_T tracks]; C --> D[Buffer<br/>Detector alignment & calibration]; D --> E[HLT 2<br/>Full event reconstruction]; E --> F[~ 12.5 kHz to storage];
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The diagram is a vertical flowchart with six rectangular boxes connected by downward-pointing arrows. The boxes are colored as follows: grey, light blue, yellow, light grey, yellow, and light green. Each box contains text describing a stage in a data processing pipeline.

L0 trigger

High p_T/E_T signatures: μ, e, h, γ

1 MHz detector readout

HLT 1

Displaced high- p_T tracks

Buffer

Detector alignment & calibration

HLT 2

Full event reconstruction

~ 12.5 kHz to storage