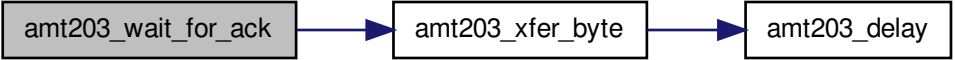


amt203_wait_for_ack



```
graph LR; A[amt203_wait_for_ack] --> B[amt203_xfer_byte]; B --> C[amt203_delay];
```

The diagram consists of three rectangular boxes arranged horizontally. The first box on the left is shaded gray and contains the text 'amt203_wait_for_ack'. A dark blue arrow points from the right side of this box to the left side of the second box. The second box is white with a black border and contains the text 'amt203_xfer_byte'. Another dark blue arrow points from the right side of the second box to the left side of the third box. The third box is white with a black border and contains the text 'amt203_delay'.

amt203_xfer_byte

amt203_delay